

# Feeling Safe and Secure in a Learning-Enabled Cyber-physical World

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*1/29/20*

## About myself...

- Postdoctoral Scholar in ECE/CS at UCLA
  - NESL Research Group
- PhD in Computer Engineering with Cybersecurity Track at Rutgers
  - 4N6 Research Group
  - GAANN Fellow (Graduate Assistance in Areas of National Need)
- BS and MS in ECE from the University of Miami
- Academic Intern at CMU with André Platzer
- Previous Intern at Siemens Corporate Research





Conclusion and  
Future Work ↙

Formal  
Methods and  
Guarantees ↙

Learning-Enabled CPS  
Safety + Security ↗

Physics-Aware Attacks  
and Defenses ↗

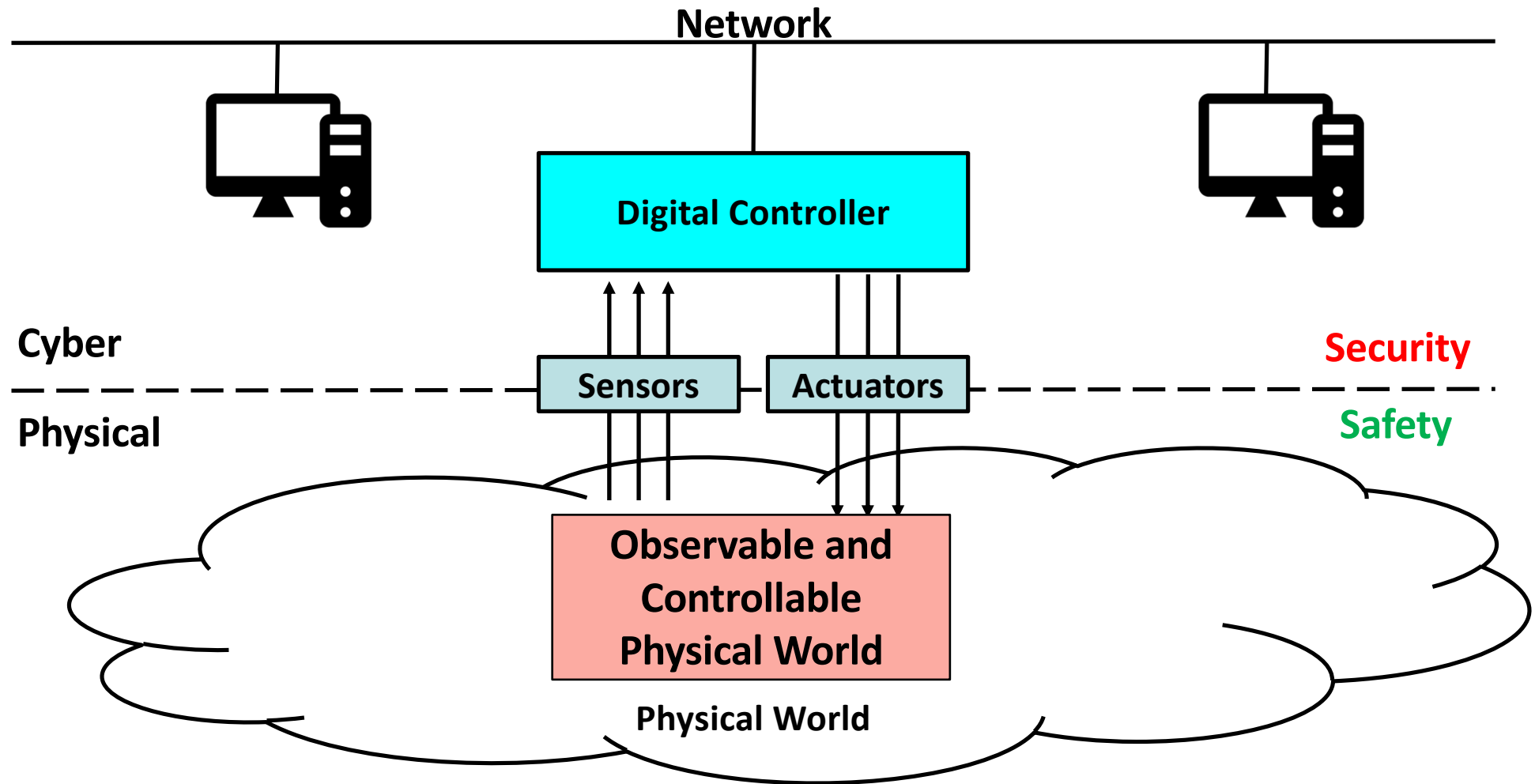
Motivation ↙

Postdoc and beyond

Stems from PhD



What does it mean to feel “safe” and “secure”?



# Cyber-Physical Industrial Control Systems (ICS)



Water Treatment



Factory Automation

Great targets to **attack!**



Electric Power Grid



Nuclear Reactor

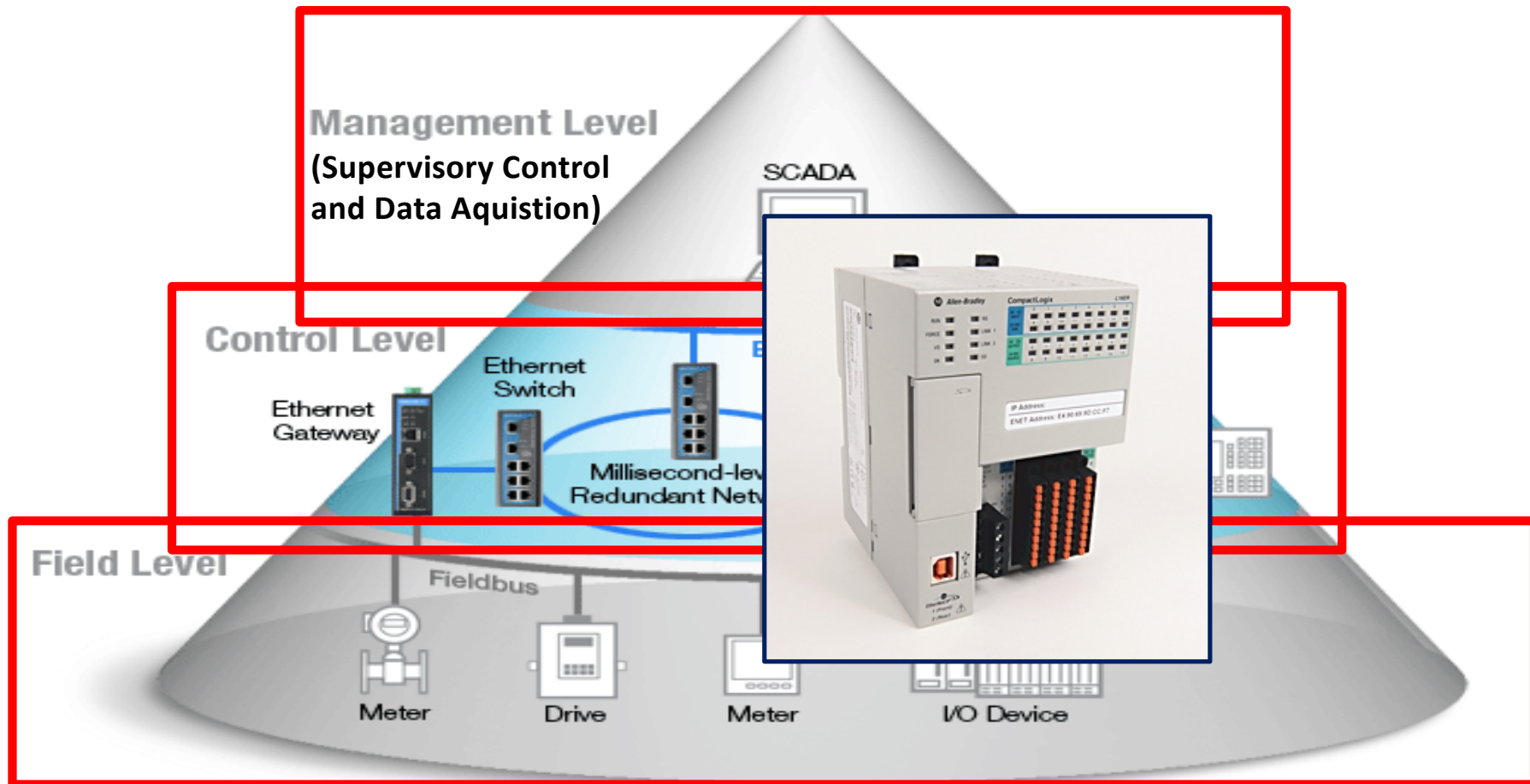
# Industrial Control System (ICS) Attacks

**Objective: Maximize Physical Impact through Stealthiness**

Hack attack  
steel works

© 22 December 2014 | Technology

# Programmable Logic Controllers (PLCs) and Industrial Control Systems (ICS)





# Stuxnet/BlackEnergy Attack Overview

BlackEnergy3 plugins	functionality
fs.dll	File system operations
jn.dll	Parasitic infector with a given payload
ps.dll	Password stealer
si.dll	System information
ss.dll	screenshots
vs.dll	Network discovery and remote execution
up.dll	Update malware
tv.dll	TeamViewer
dc.dll	List Windows accounts
bs.dll	Query system HW, BIOS, Windows info.
dstr.dll	Destroy system
kl.dll	Key-logger
scan.dll	Network host port scan
rd.dll	Simple pseudo 'remote desktop'
grc.dll	Back comm. channel using plus.google.com
cert.dll	certificate stealer
sn.dll	Logs traffic, extracts login-passwords
usb.dll	gathers info. on connected USB devices



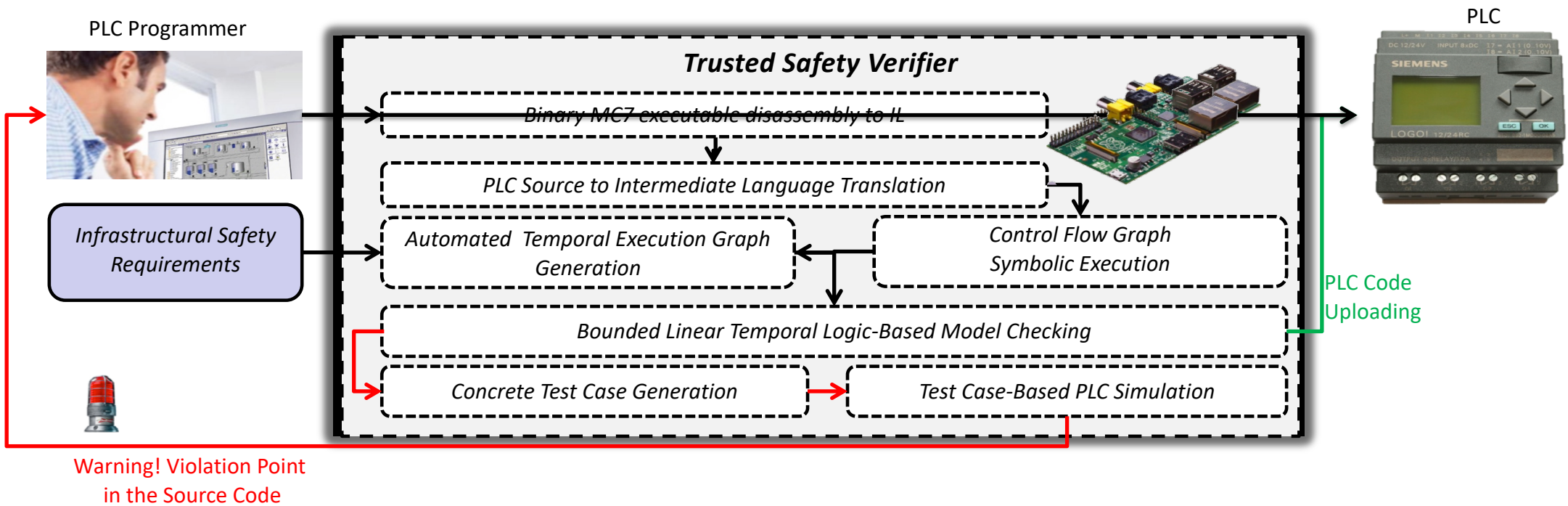
PLC

STL  
code  
block



**Observation:** BlackEnergy3 – compared to Stuxnet - included many more reconnaissance plugins!

# Trusted Safety Verifier (TSV) Overview



# Infrastructural Safety Requirements

- Formulated using linear temporal logic (LTL) expressions
- Example safety requirement
  - *English expression*
    - Relay  $R_1$  should **NOT** open **UNTIL** Generator  $G_2$  turns on
  - *Logical expression*
    - Atomic propositions
      - $a_1$ : “Relay  $R_1$  is open”
      - $a_2$ : “Generator  $G_2$  is on”

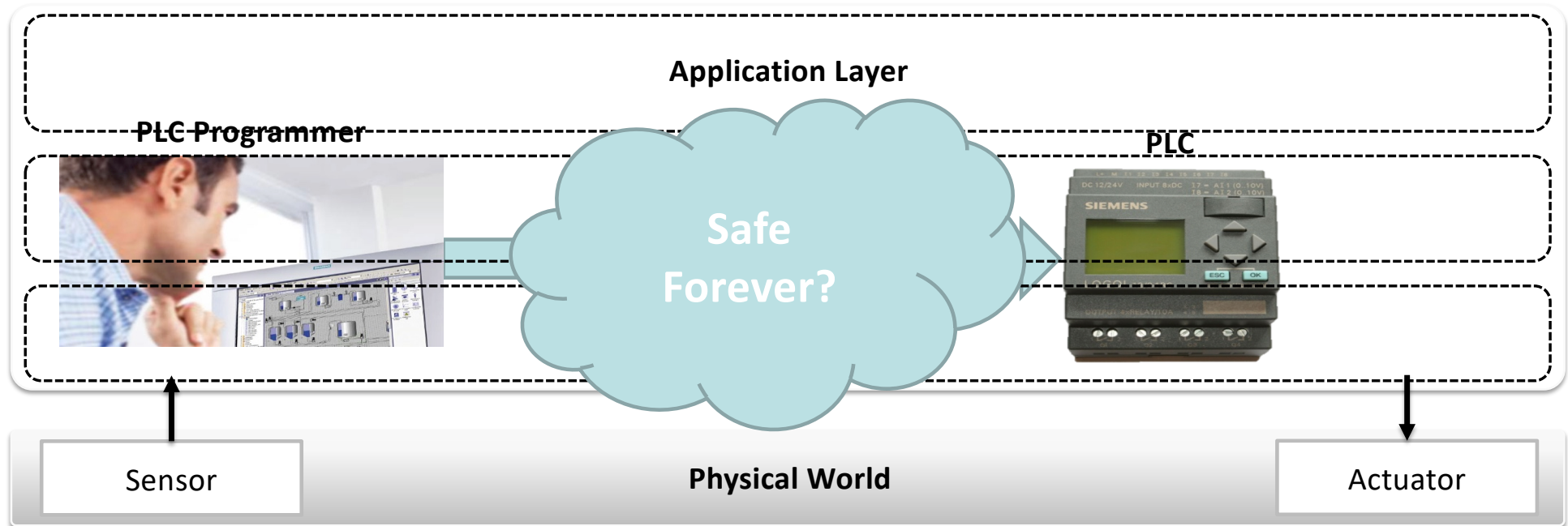


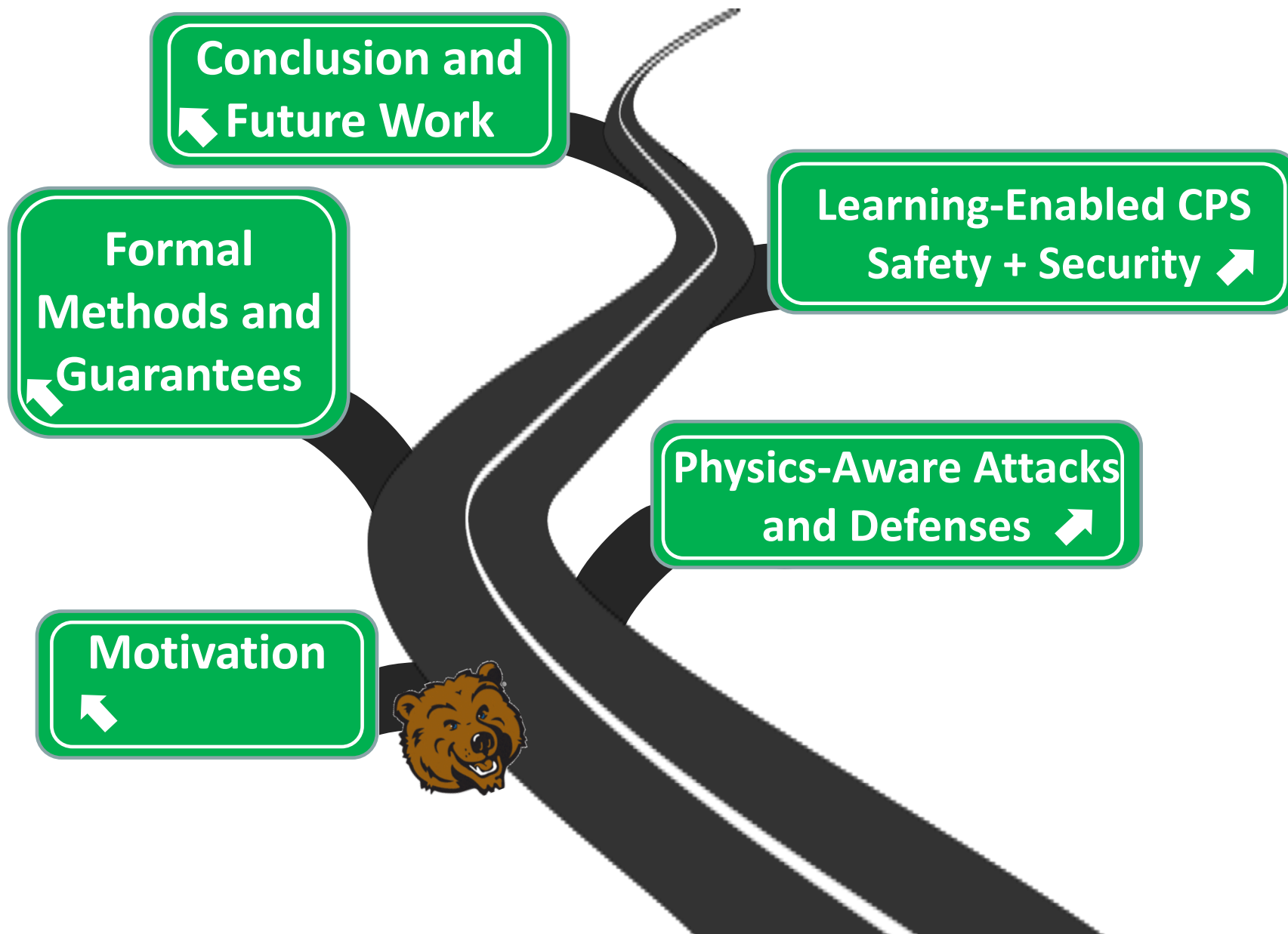
LTL:  $\neg a_1 \mathbf{U} a_2$

# Limitations of Previous Solutions

- Application layer protection
  - No means of protecting lower levels of abstraction
- Physical safety properties are assumed to be provided

## PLC Architecture







# Hey, My Malware Knows Physics! Attacking PLCs with Physical Model Aware Rootkit

NDSS 2017

**R**



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DARMSTADT

**FIU**

# Harvey: Model-Aware Rootkit (NDSS 2017)

- A rootkit that takes into account the physical topology of the ICS
- Model
  - Uses physical models to optimize control commands for an adversarial objective function
- PLC infection: compromising the PLC's firmware
  - Utilize the firmware update mechanism to replace firmware over the network
  - Local firmware modifications, e.g., SD card or JTAG implantation
  - Run-time attacks, e.g., network exploits or remote code execution vulnerabilities (FrostyURL)

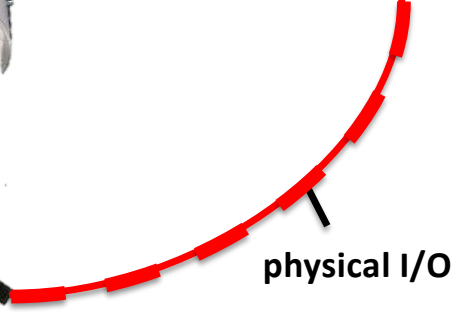


# Stuxnet vs. Harvey

Actuator/Sensor



PLC



physical I/O

Stuxnet:

1. Compromised PLC control logic
2. Modified HMI to prevent detection of PLC modifications
3. Replayed benign measurements to HMI

Harvey:

1. Does not require SCADA compromise to remain stealthy
2. Can calculate fake data for dynamic systems, Stuxnet only replays recorded measurements

cyber

Supervisory Control (SCADA)



PLC: Programmable Logic Controller  
HMI: Human Machine Interface

# Stuxnet vs. HMI-based WDS (Weaselboard (TSV), NDSS '14)

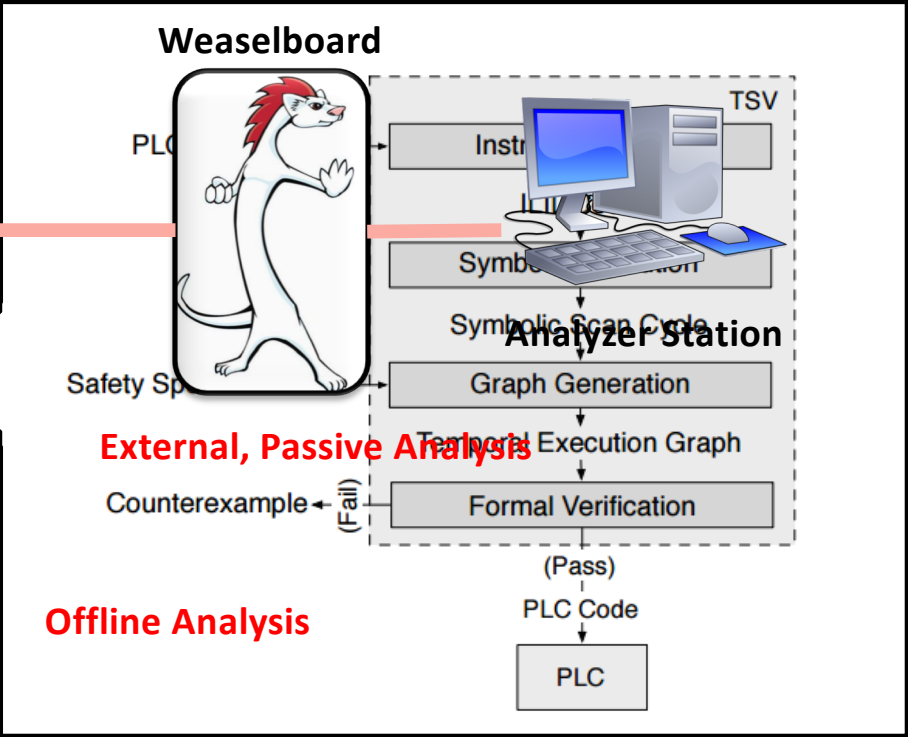
Actuator/Sensor



PLC



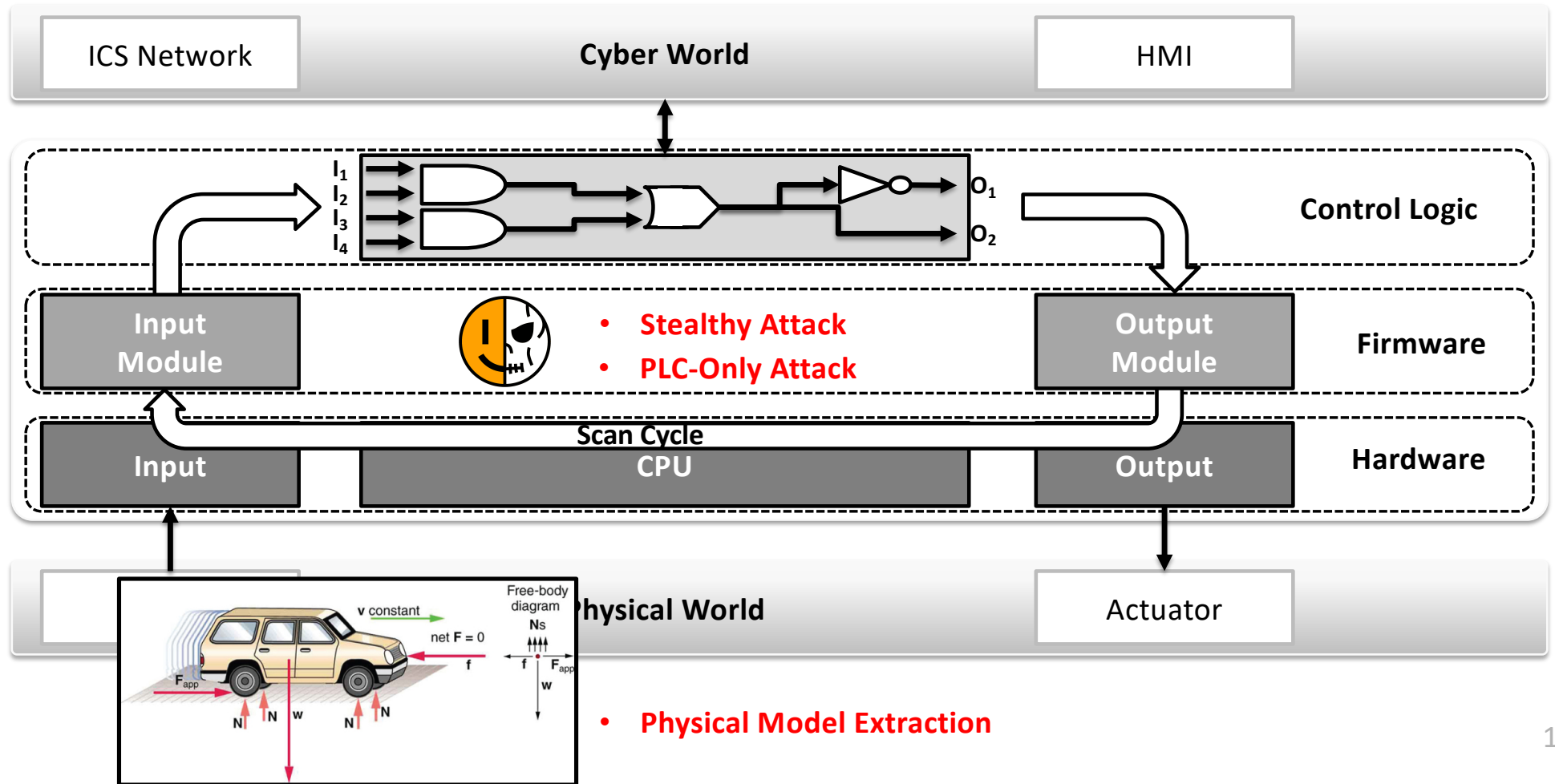
physical I/O



PLC: Programmable Logic Controller  
HMI: Human Machine Interface

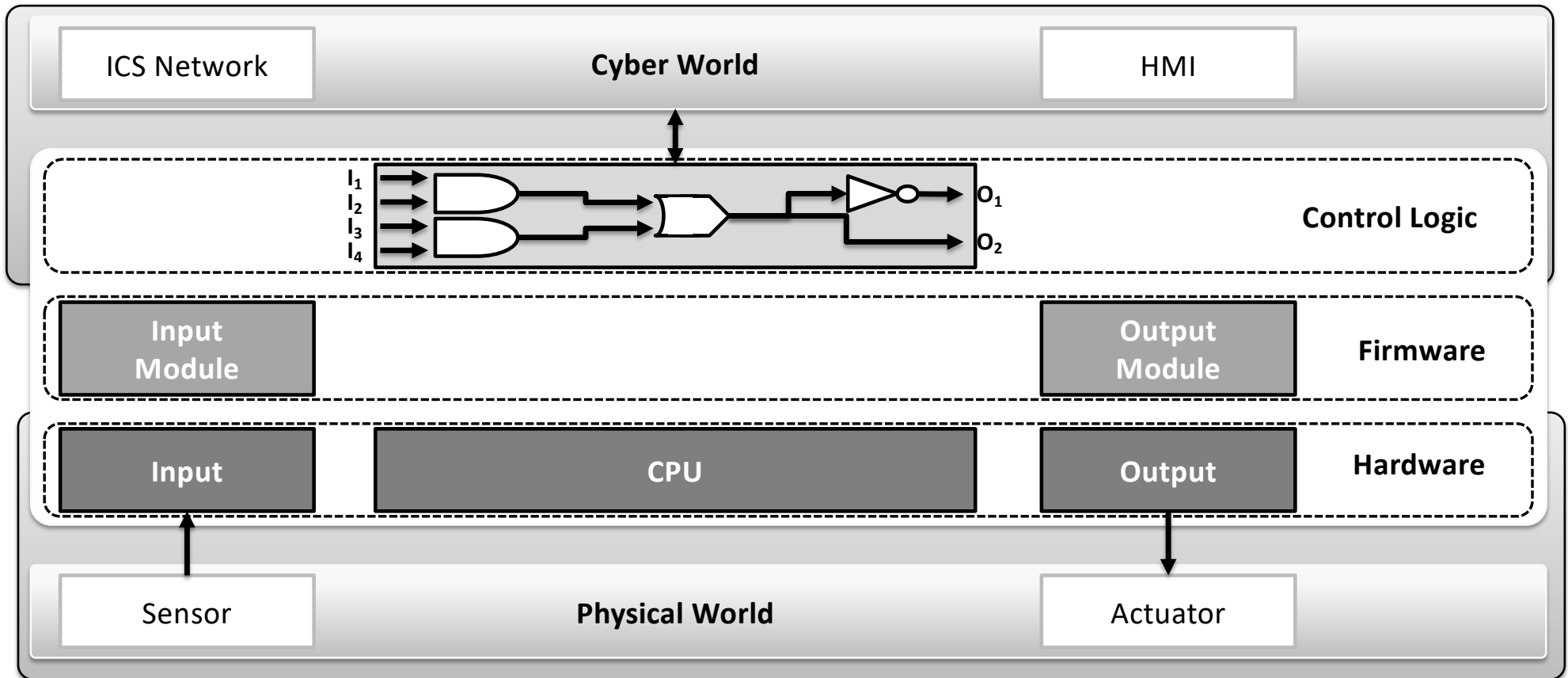
McLaughlin, Stephen E., et al. "A Trusted Safety Verifier for Process Controller Code." *NDSS*. Vol. 14. 2014.

# PLC Architecture & Adversary Model

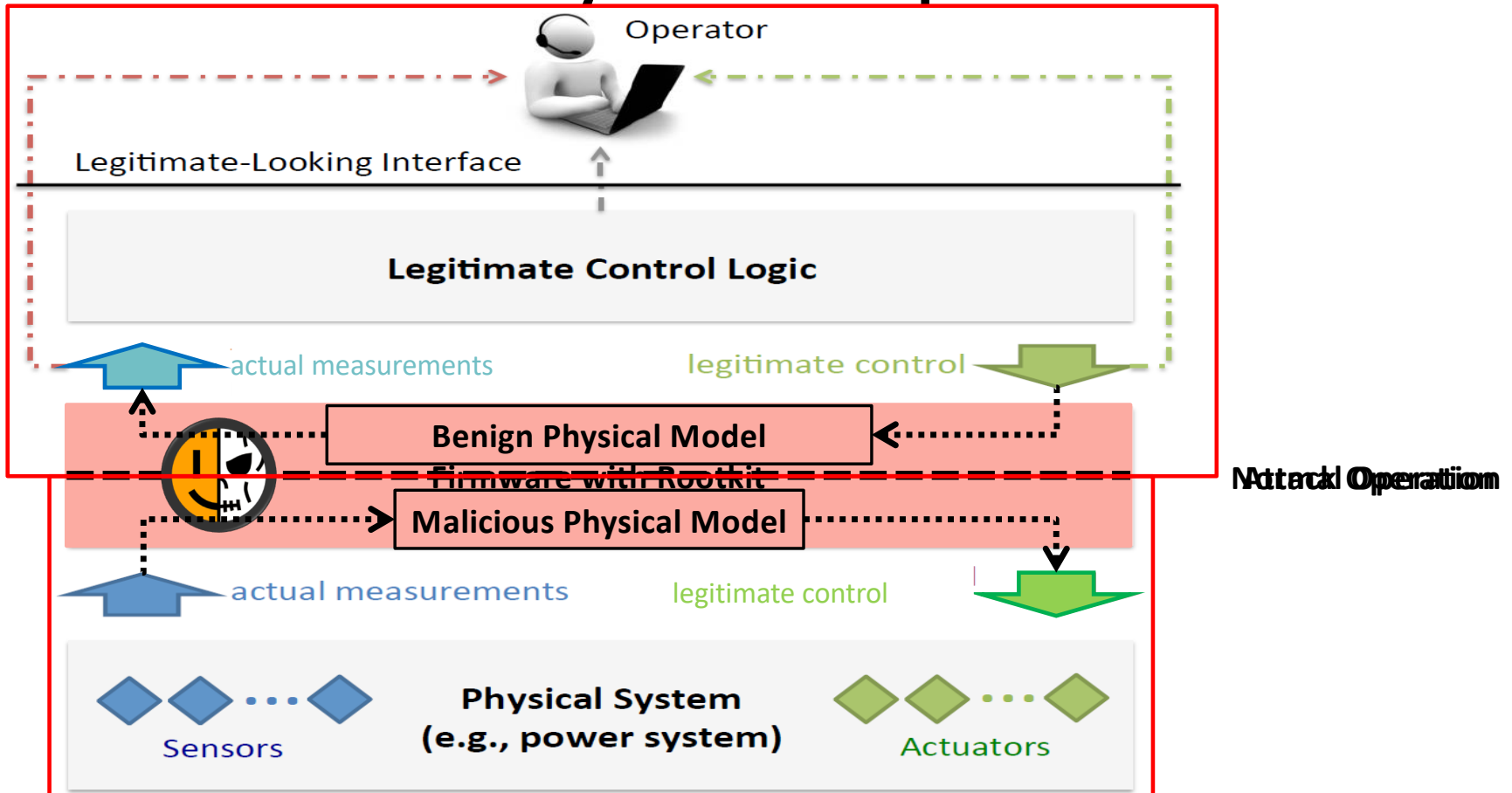




# Physical Architecture & Adversary Data Manipulation

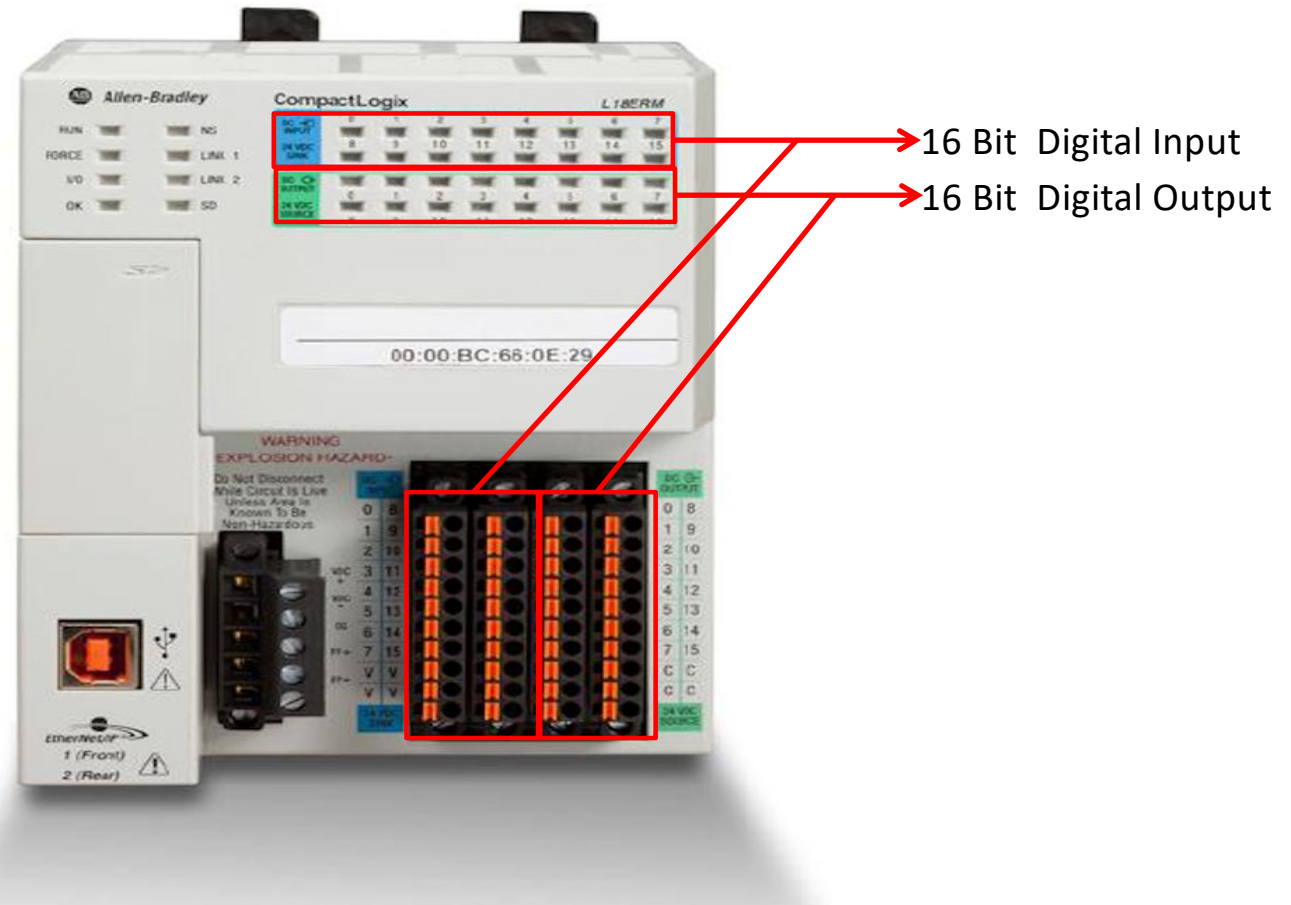


# Physics-Awareness: 2-Way Data Manipulation

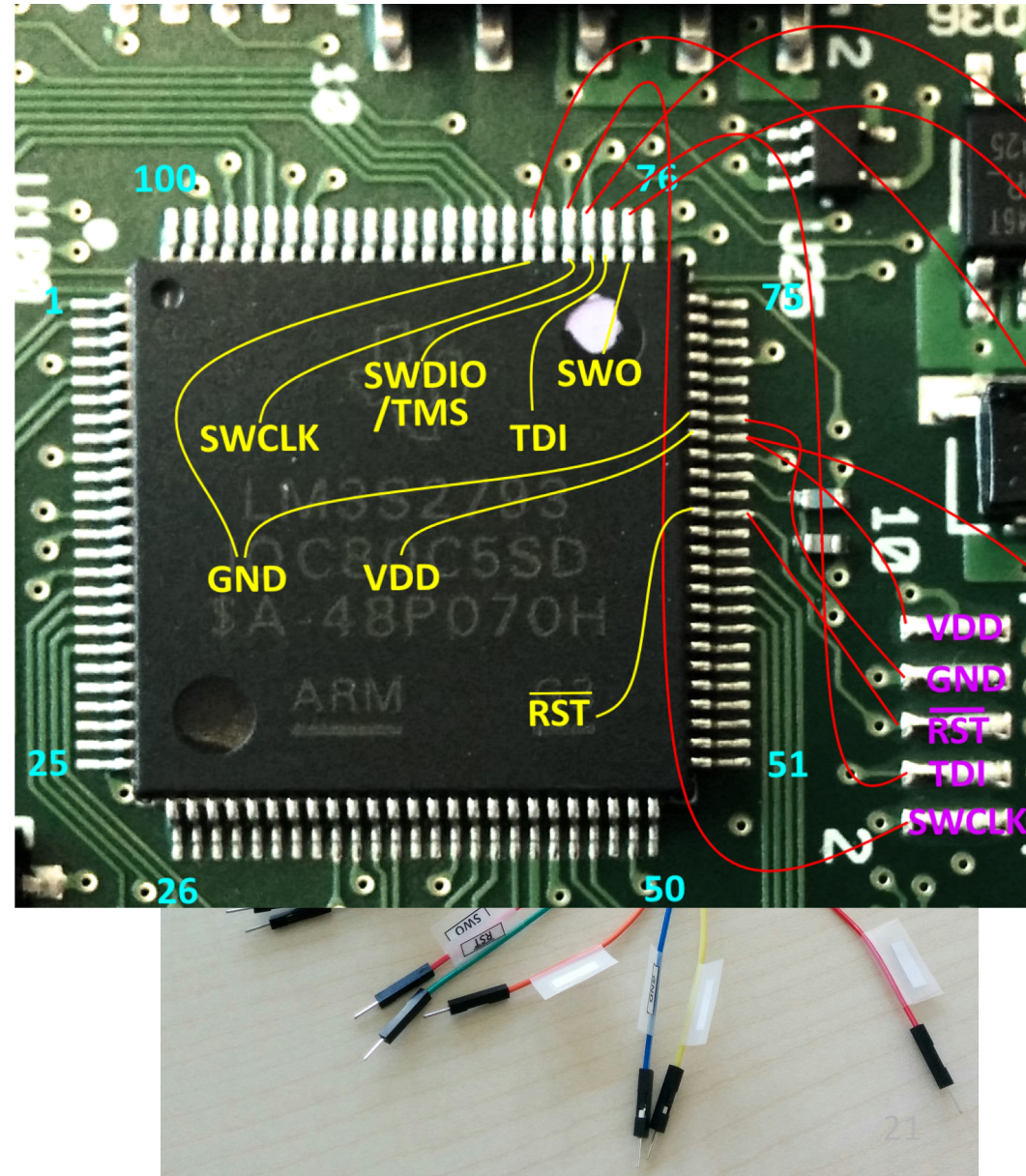


# CompactLogix L1 PLC

- High Value (1) ~ 24 V DC
- Low Value (0) ~ 8 V DC



# Memory Analysis with JTAG



# Memory Analysis with JTAG

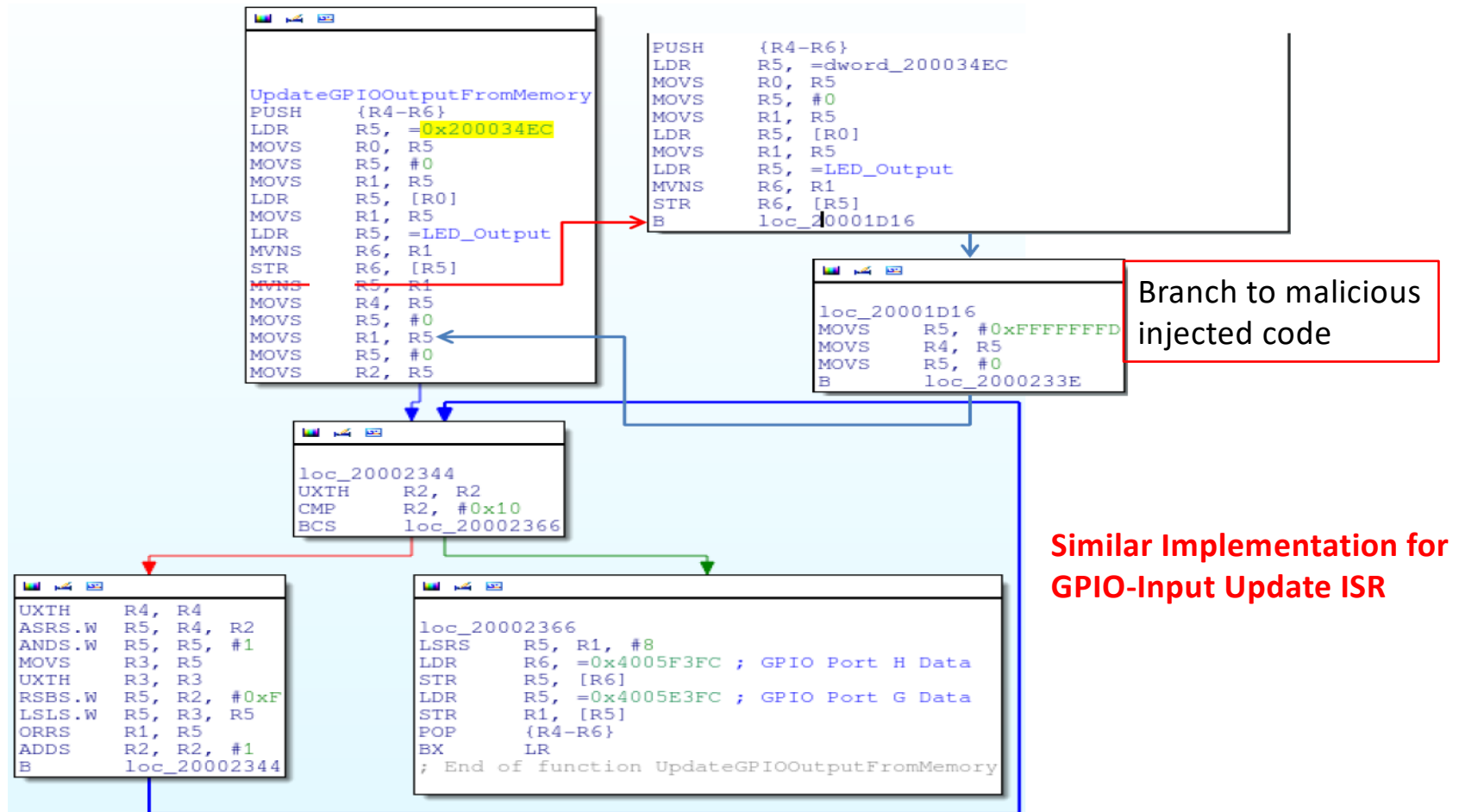
- JTAG interface to dump memory for code disassembly
- TI Stellaris LM3S2793 data sheet to find memory layout and built-in ROM functions



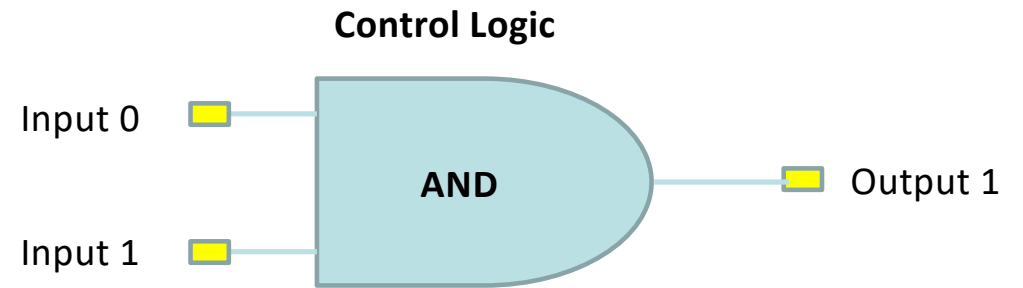
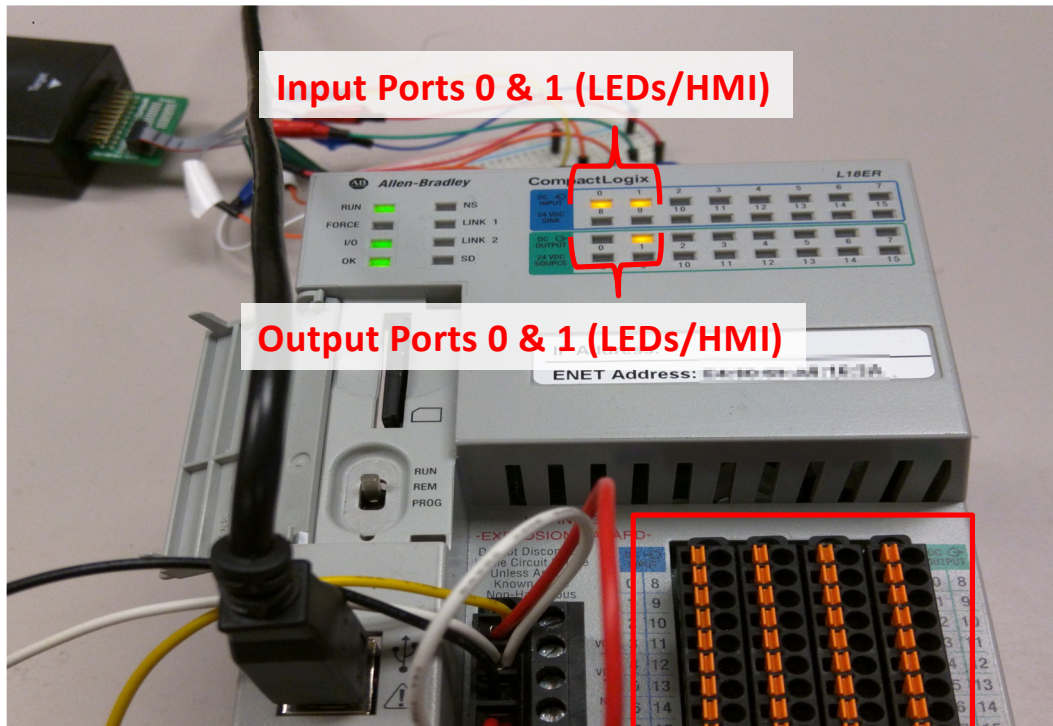
Start	End	Description
0x00000000	0x0001FFFF	On-chip Flash
0x00020000	0x00FFFFFF	Reserved
0x01000000	0x1FFFFFFF	ROM
0x20000000	0x2000FFFF	On-chip SRAM
0x20010000	0x21FFFFFF	Reserved
0x22000000	0x221FFFFF	Bit-band alias of SRAM
...	...	
0x4005C000	0x4005CFFF	GPIO Port E (AHB)
0x4005D000	0x4005DFFF	GPIO Port F (AHB)
0x4005E000	0x4005EFFF	GPIO Port H (AHB)
0x4005F000	0x4005FFFF	GPIO Port G (AHB)
...	...	



# Modified GPIO-Output Update ISR



# Harvey Spoofing Inputs



LEDs/HMI see:

Inputs 0 & 1=High



# Manipulation of Sensor Data

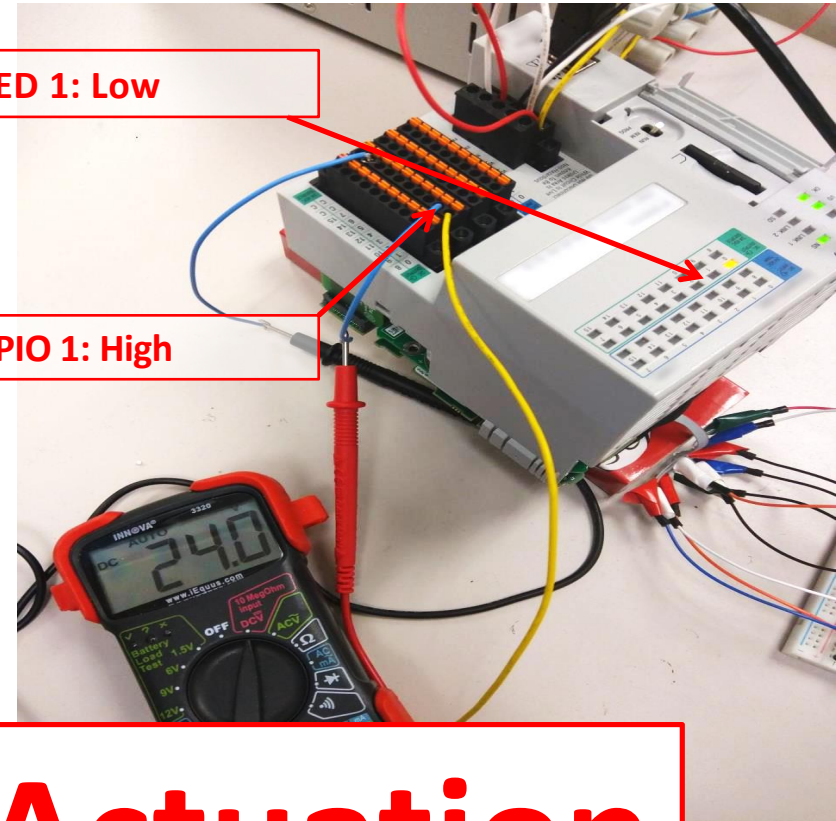
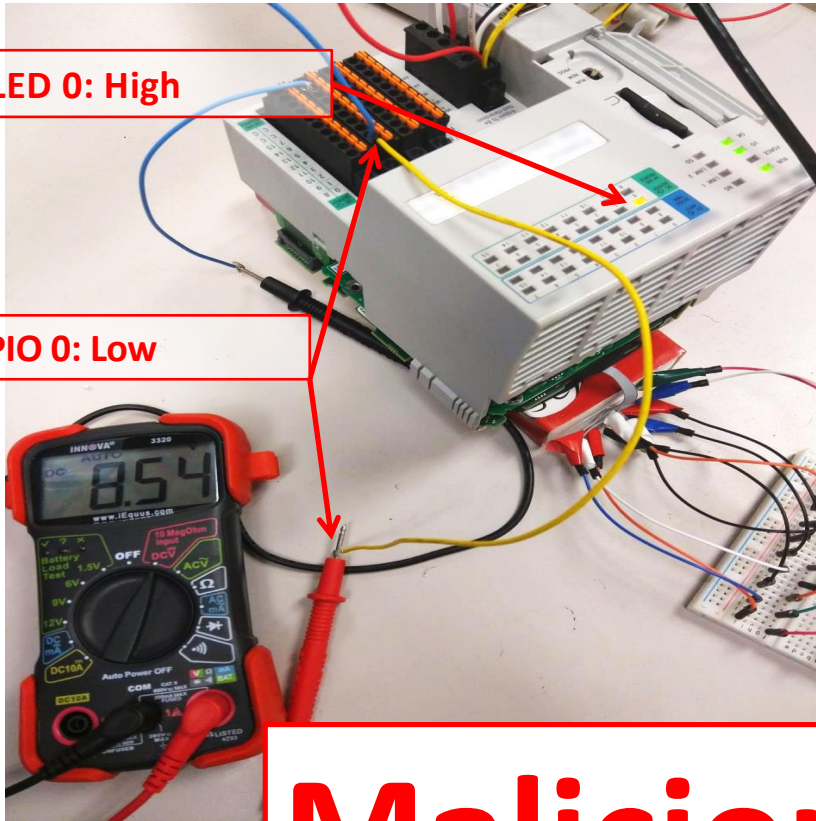
# Harvey Spoofing Outputs

Output LED 0: High

Output LED 1: Low

Output GPIO 0: Low

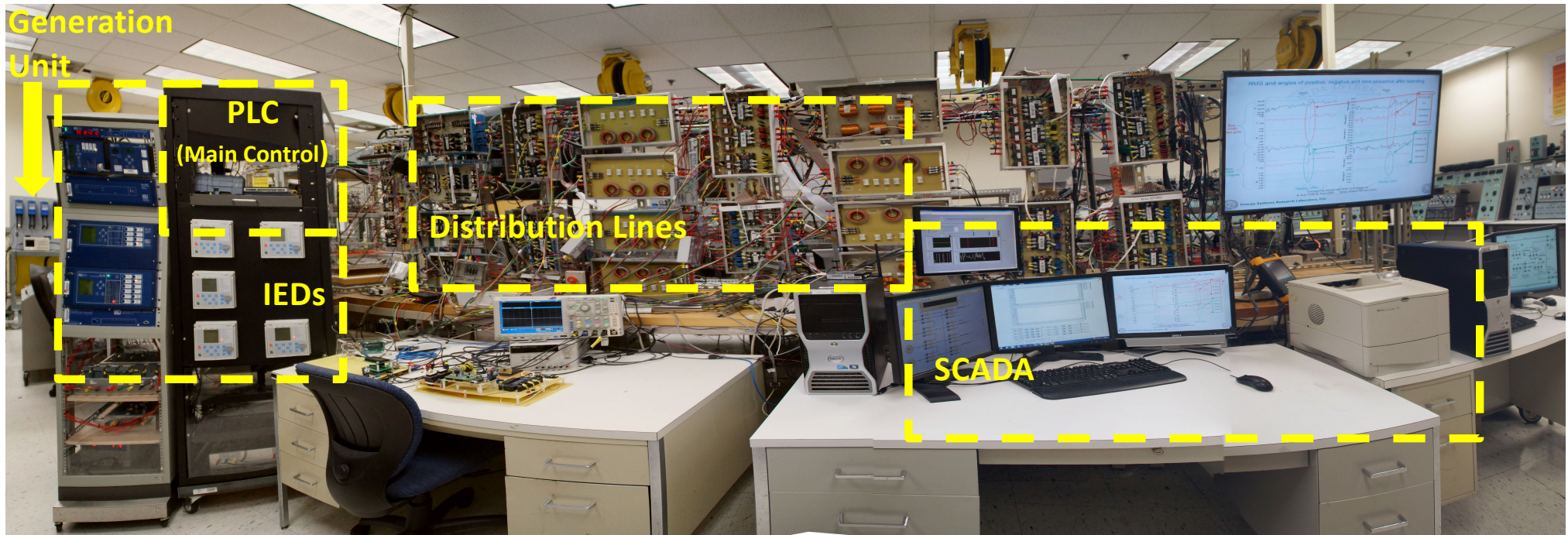
Output GPIO 1: High



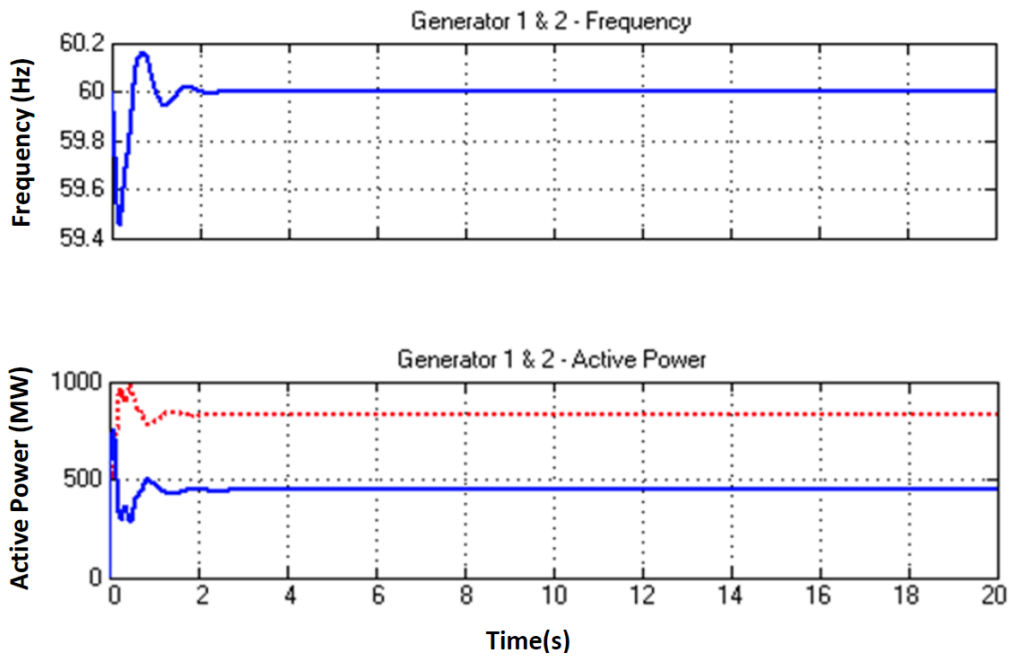
**Malicious Actuation**



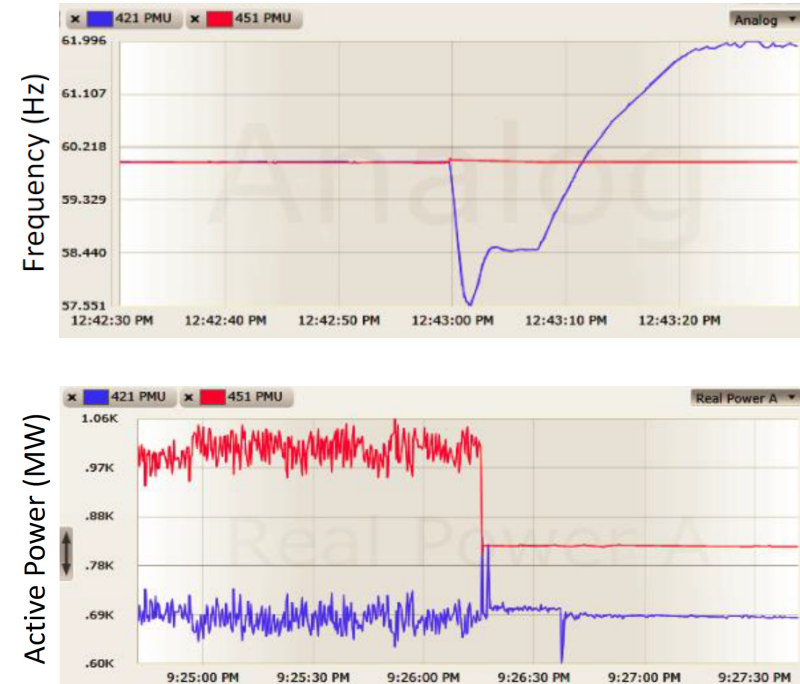
# Real-world Attack Demo: Attacking a Power System



# Real-world Attack Demo: Attacking a Power System



HMI Measurements



Actual System Measurements

# Let's Get Defensive

- **Problem:** Legacy devices (such as PLCs) don't support a hardware trusted computing base (TCB) for remote attestation

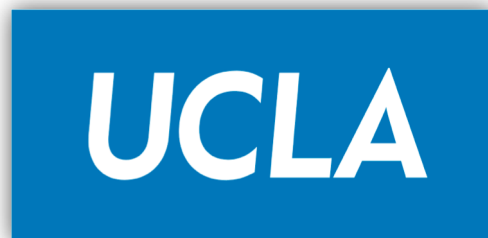


**Patt: Physics-based Attestation of Control Systems**



# Patt: Physics-based Attestation of Control Systems

(RAID 2019)



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# Patt: Cyl

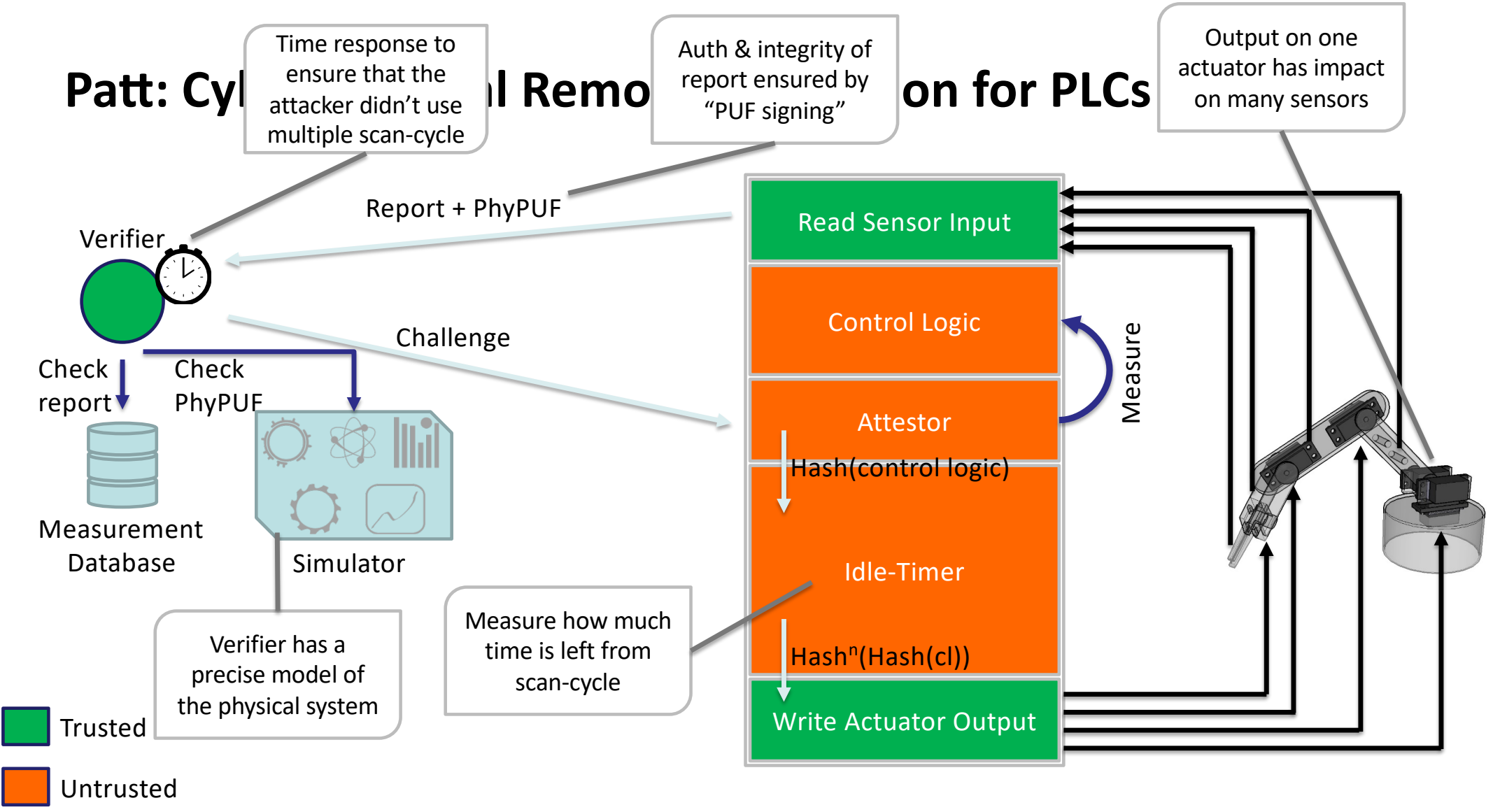
# Time Remo

# on for PLCs

Time response to ensure that the attacker didn't use multiple scan-cycle

Auth & integrity of report ensured by "PUF signing"

Output on one actuator has impact on many sensors



# Patt: Cyl

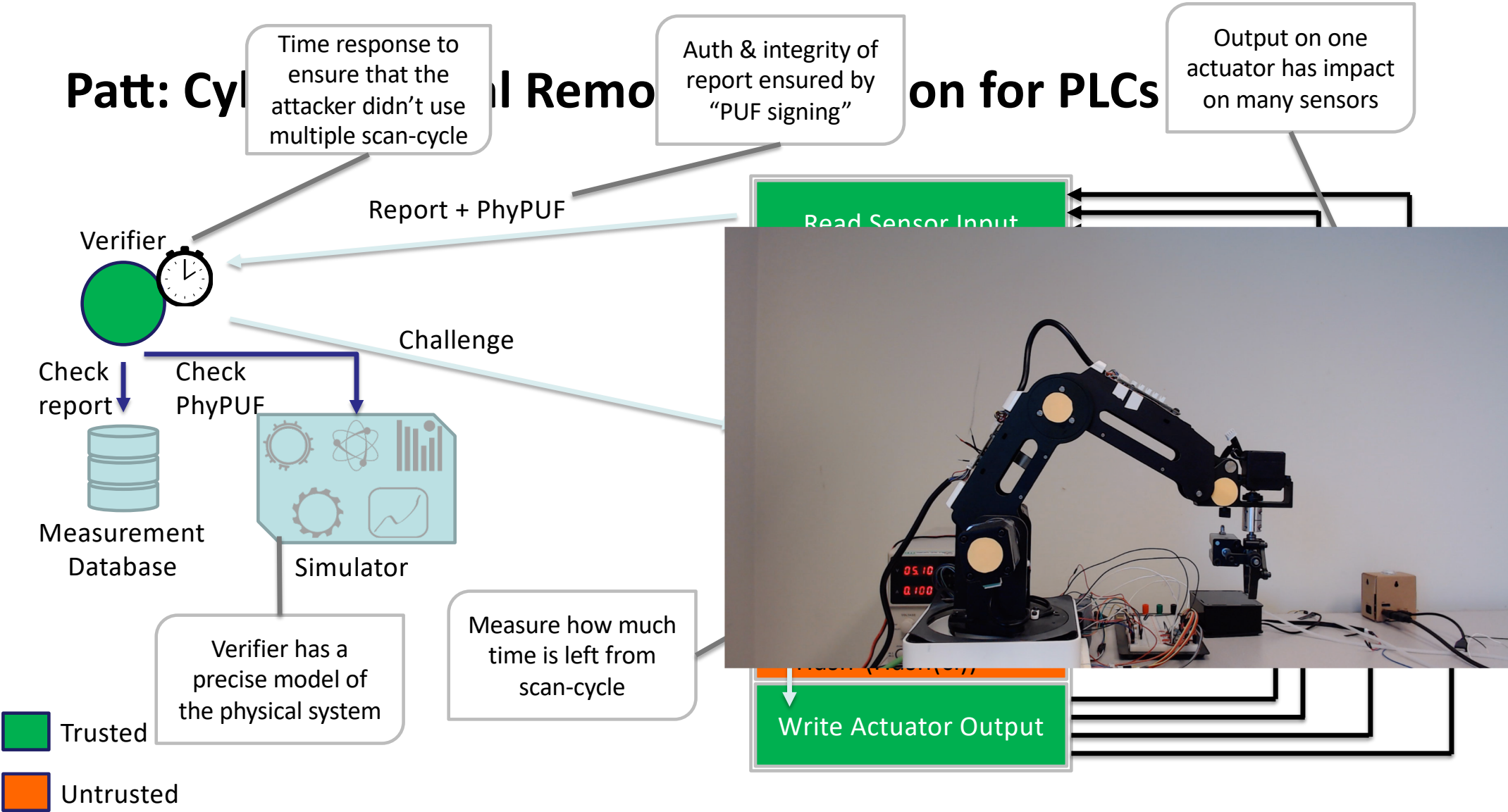
# Remo

# on for PLCs

Time response to ensure that the attacker didn't use multiple scan-cycle

Auth & integrity of report ensured by "PUF signing"

Output on one actuator has impact on many sensors



# Let's Get Defensive

- **Problem:** Legacy devices (such as PLCs) don't support a hardware trusted computing base (TCB) for remote attestation
- **Problem:** The complexity of these CPS involves several domain-specific physical processes that need to be verified



**Patt: Physics-based Attestation of Control Systems**



**Domain-specific Physics-based Verification of ICS Processes**



**See No Evil, Hear No Evil, Feel No Evil...**  
***Print No Evil?***

**Malicious Fill Pattern Detection in  
Additive Manufacturing**

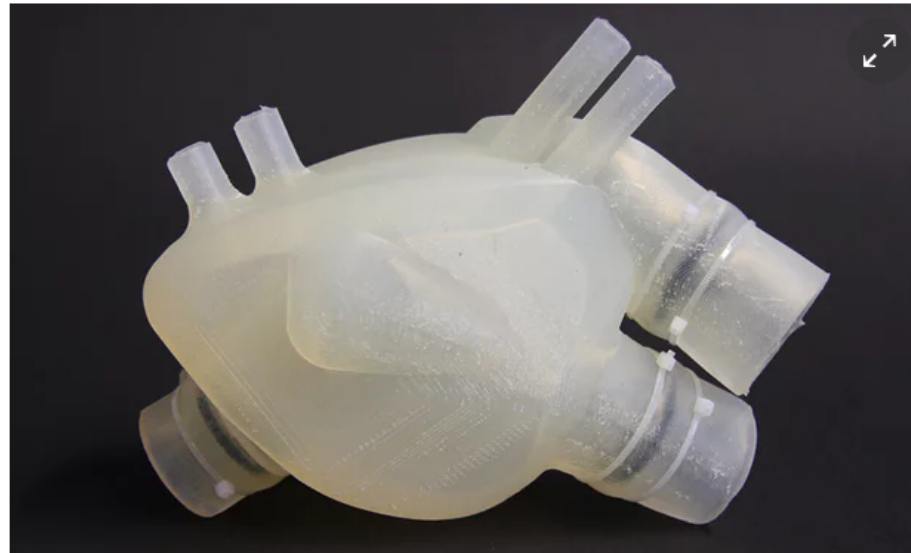
**USENIX Security 2017**



# Industrial 3D Printing

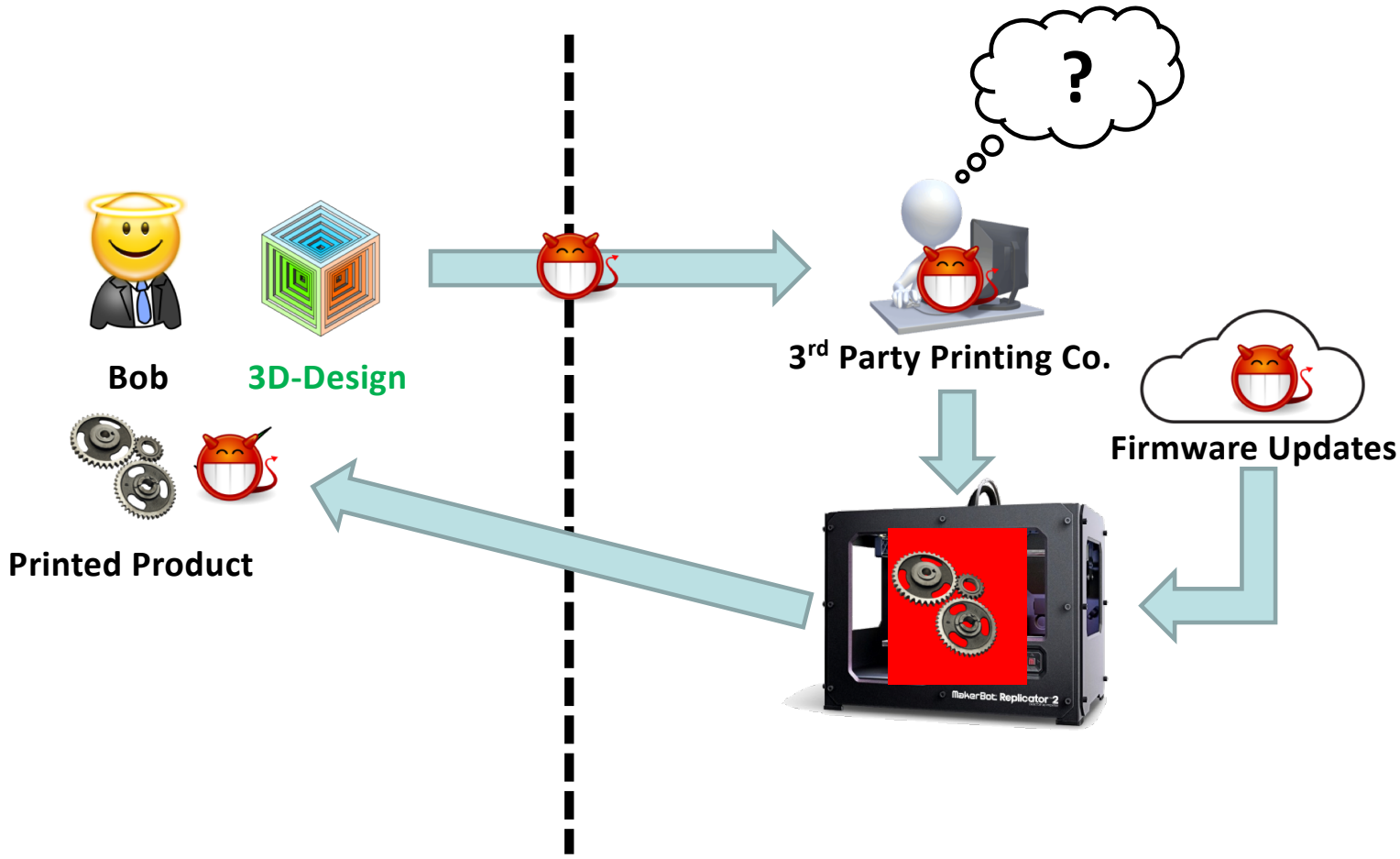
## Could 3D printing solve the organ transplant shortage?

Scientists are racing to make replacement human organs with 3D printers. But while the technology's possibilities are exciting, already there are fears we could be 'playing God'

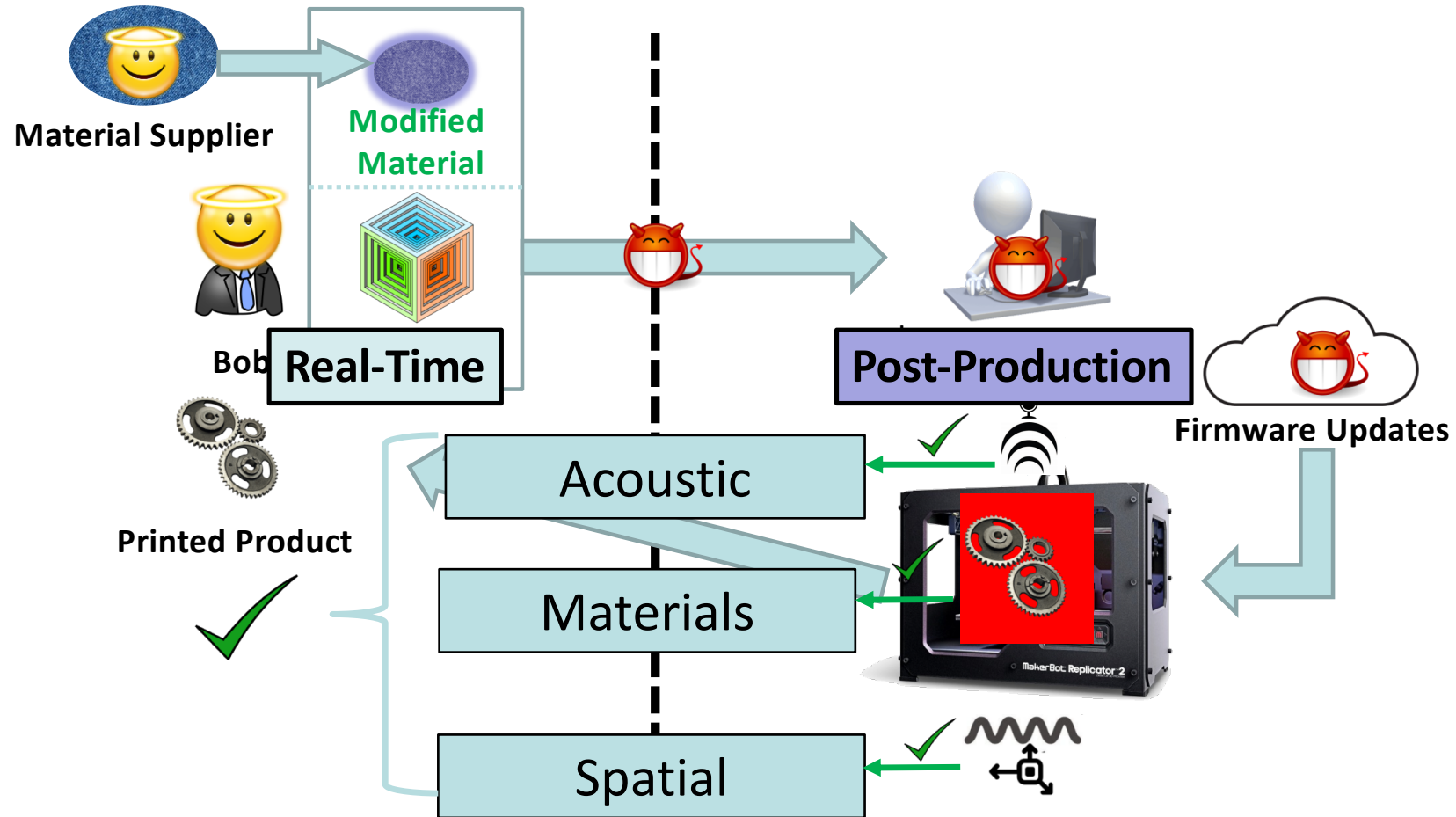


A 3D-printed silicone heart, created by engineers at ETH Zurich. Photograph: ETH Zürich

# Third Party 3D Printing



# Malicious Fill Pattern Detection (USENIX Security 2017)





# Let's Get Defensive

- **Problem:** Legacy devices (such as PLCs) don't support a hardware trusted computing base (TCB) for remote attestation
- **Problem:** The complexity of these CPS involves several domain-specific physical processes that need to be verified
- **Problem:** How can we verify these cyber-physical properties in distributed settings?



**Patt: Physics-based Attestation of Control Systems**



**Domain-specific Physics-based Verification of ICS**



**Cyber-physical Control Behavior Integrity**



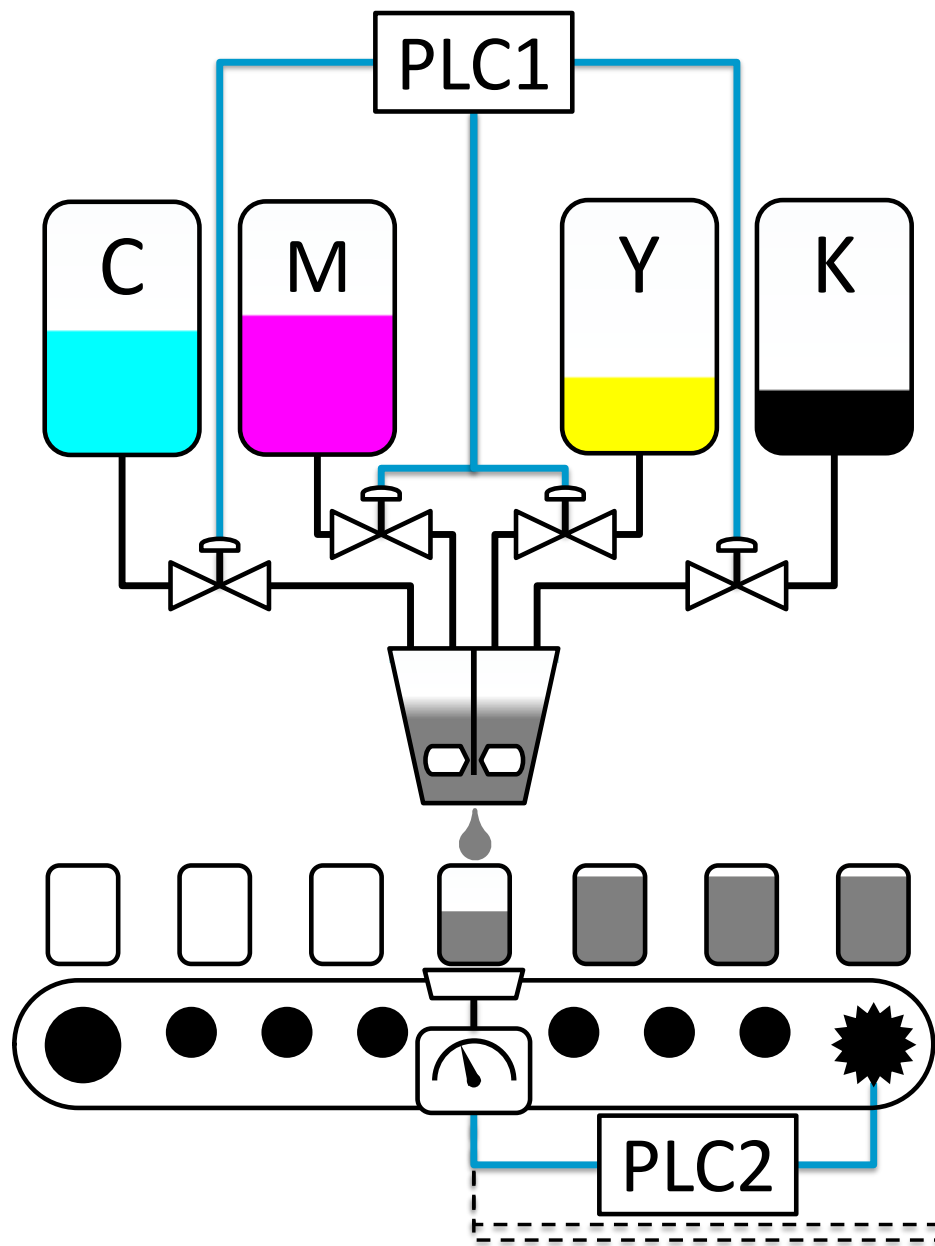
# Control Behavior Integrity for Distributed Cyber-Physical Systems

ICCPS 2020



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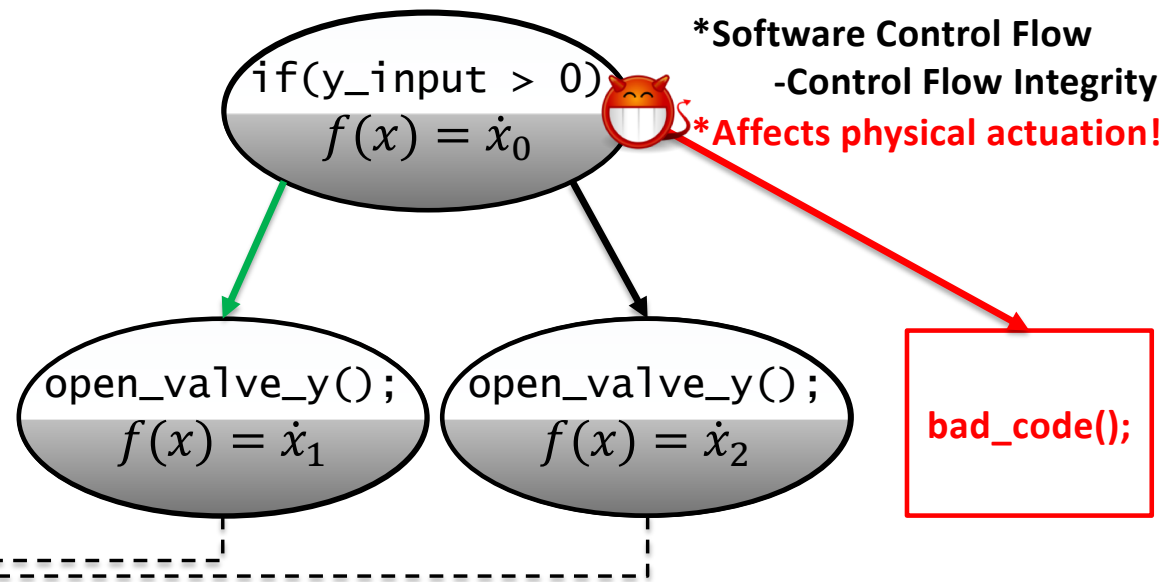




```

if(y_input > 0)
{
  open_valve_y();
}
else
{
  close_valve_y();
}

```

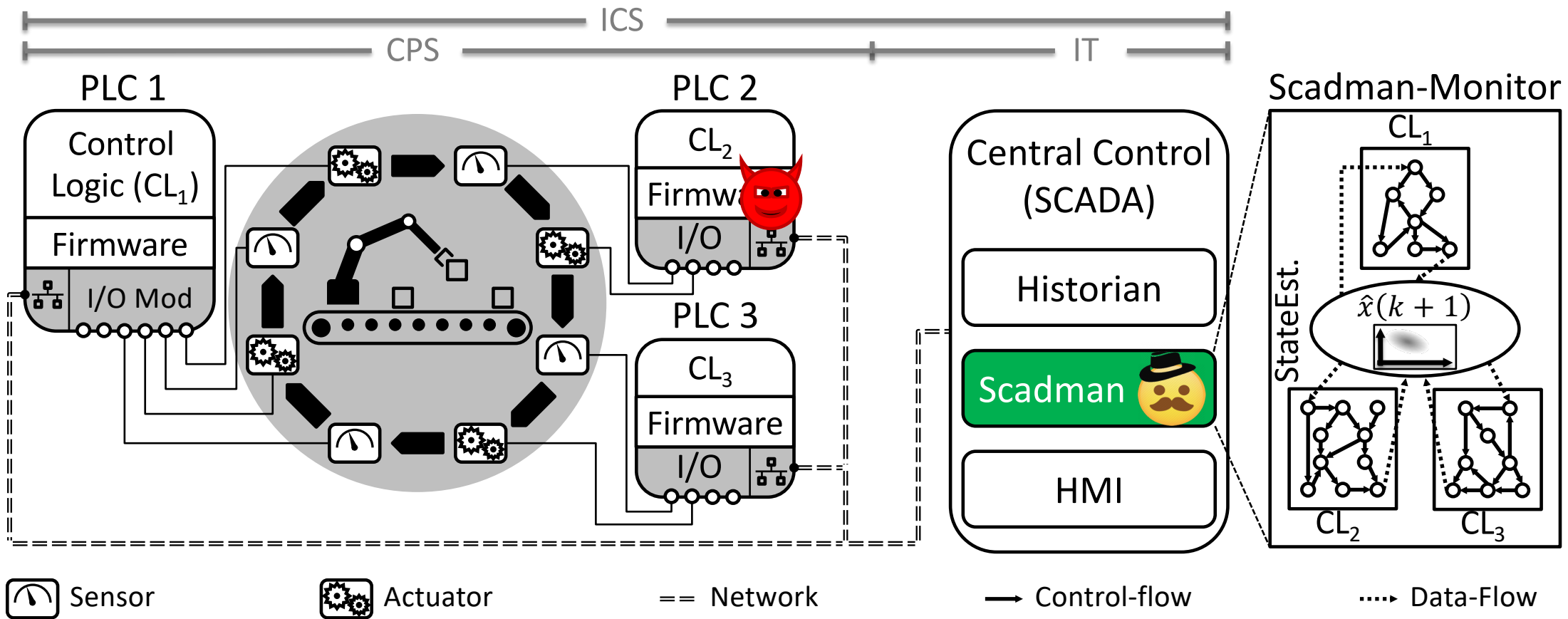


# Scadman: Control Behavior Intrusion Detection

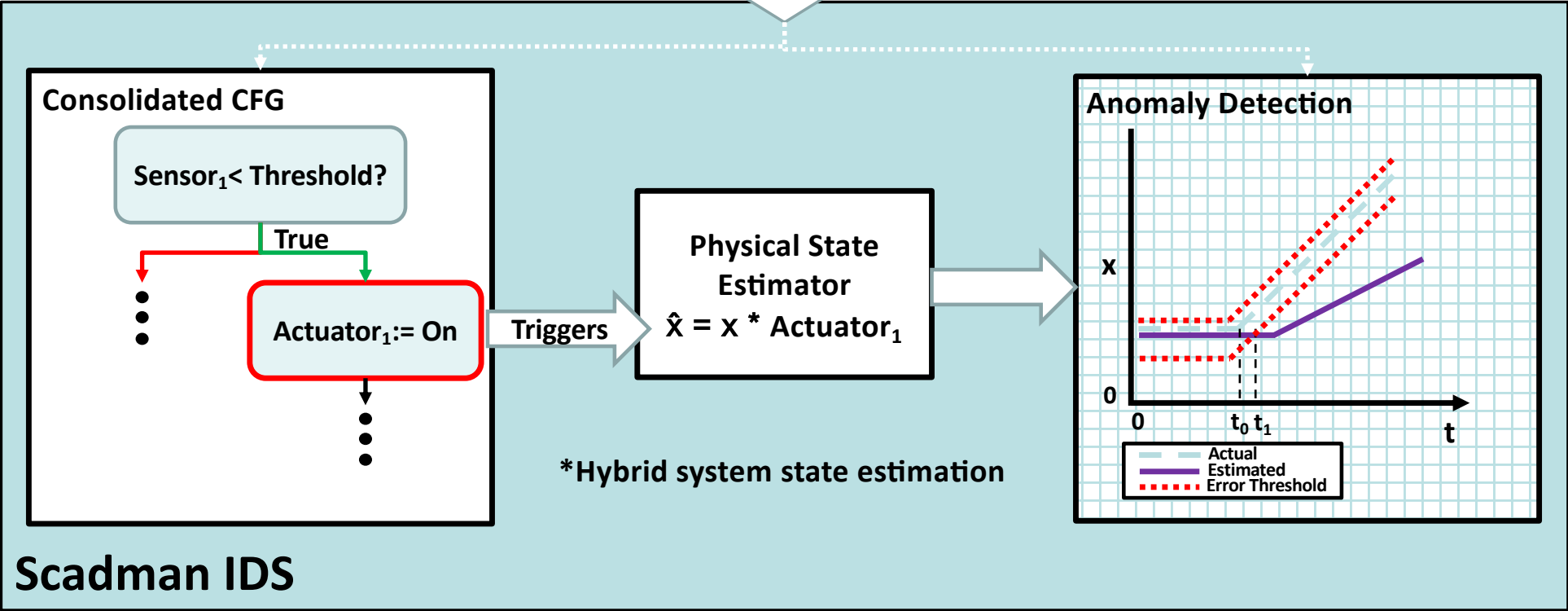
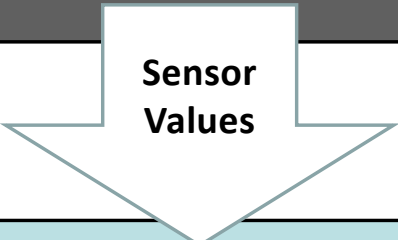
- An intrusion detection solution for distributed ICS
- Hybrid model
  - Uses physical state estimation for IDS
  - Updates physical state estimation based on software control flow



# Scadman Overview

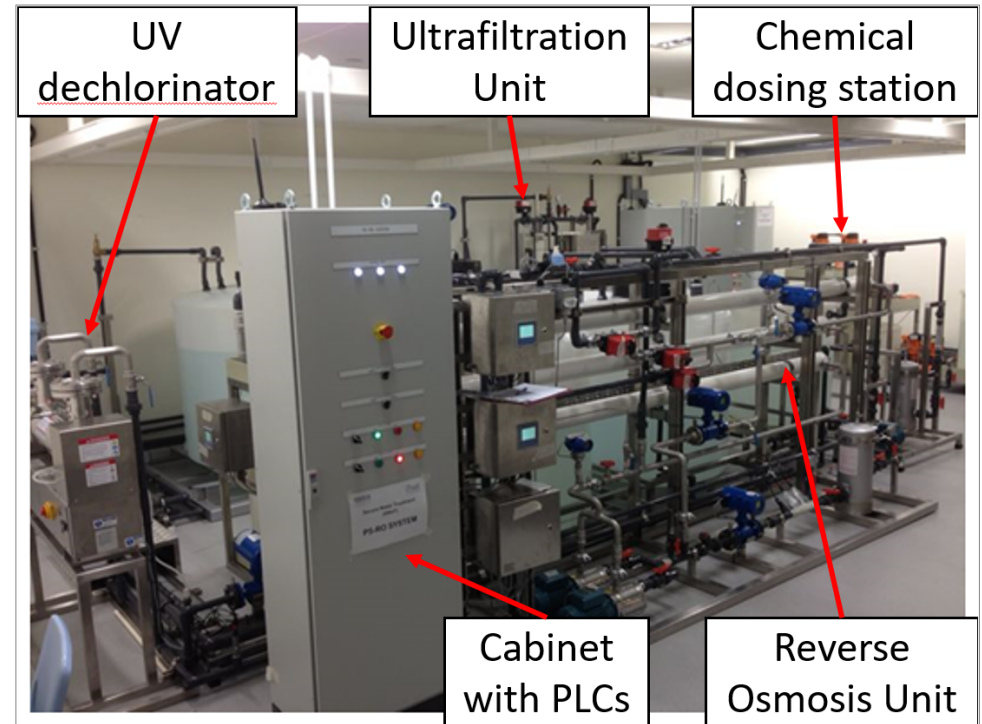


ICS Network

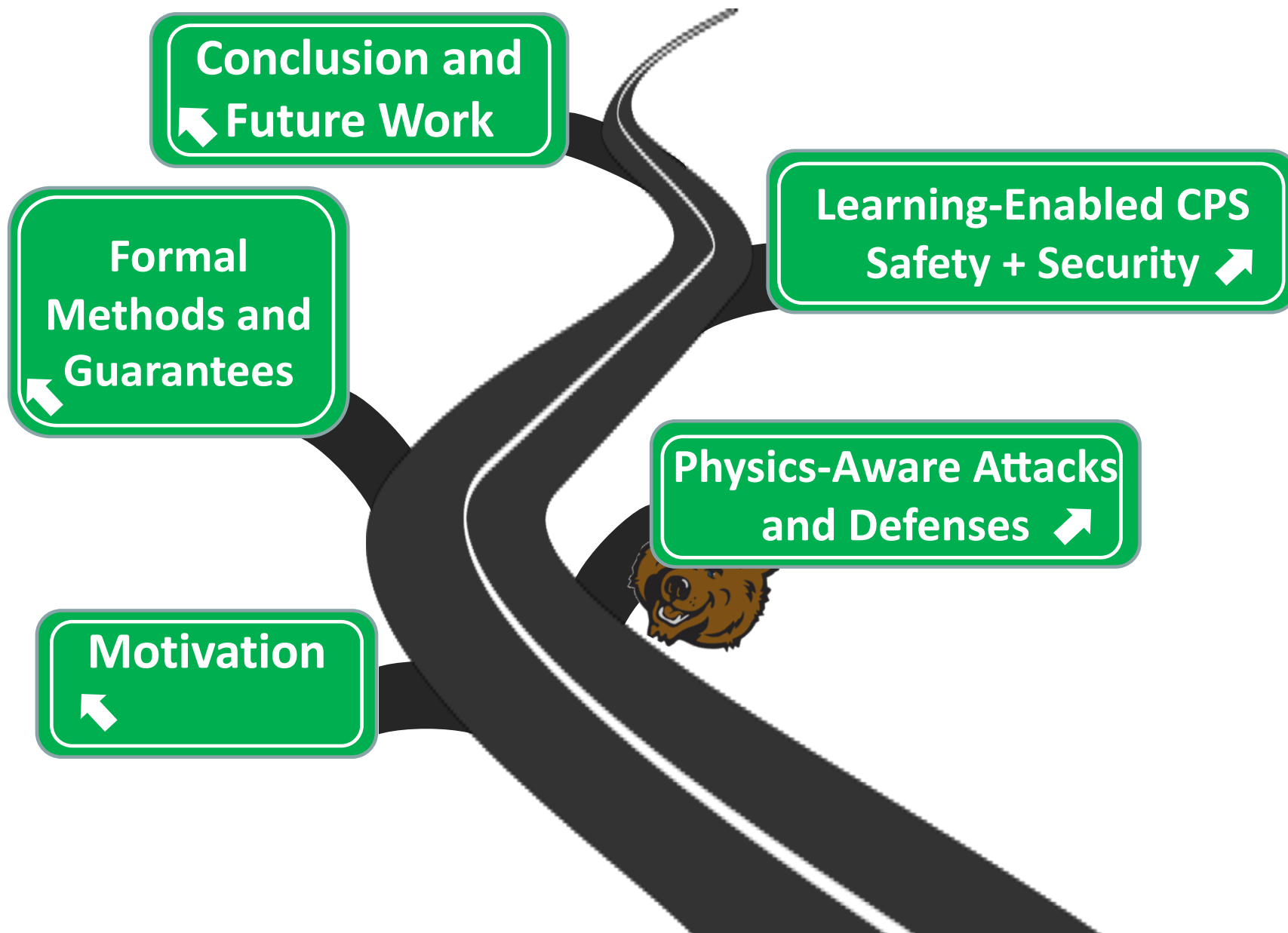


# Evaluation: Water Treatment Testbed

- Evaluated against known set of ICS attacks from
  - 7 days worth of data
  - Multi-point attacks included
- Detected all attacks
  - Also detected faulty sensor data
  - Zero false positives
- No overhead on ICS operation
  - Scadman utilizes historian data

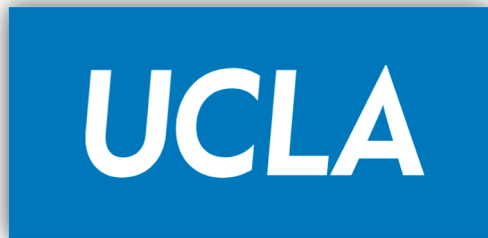




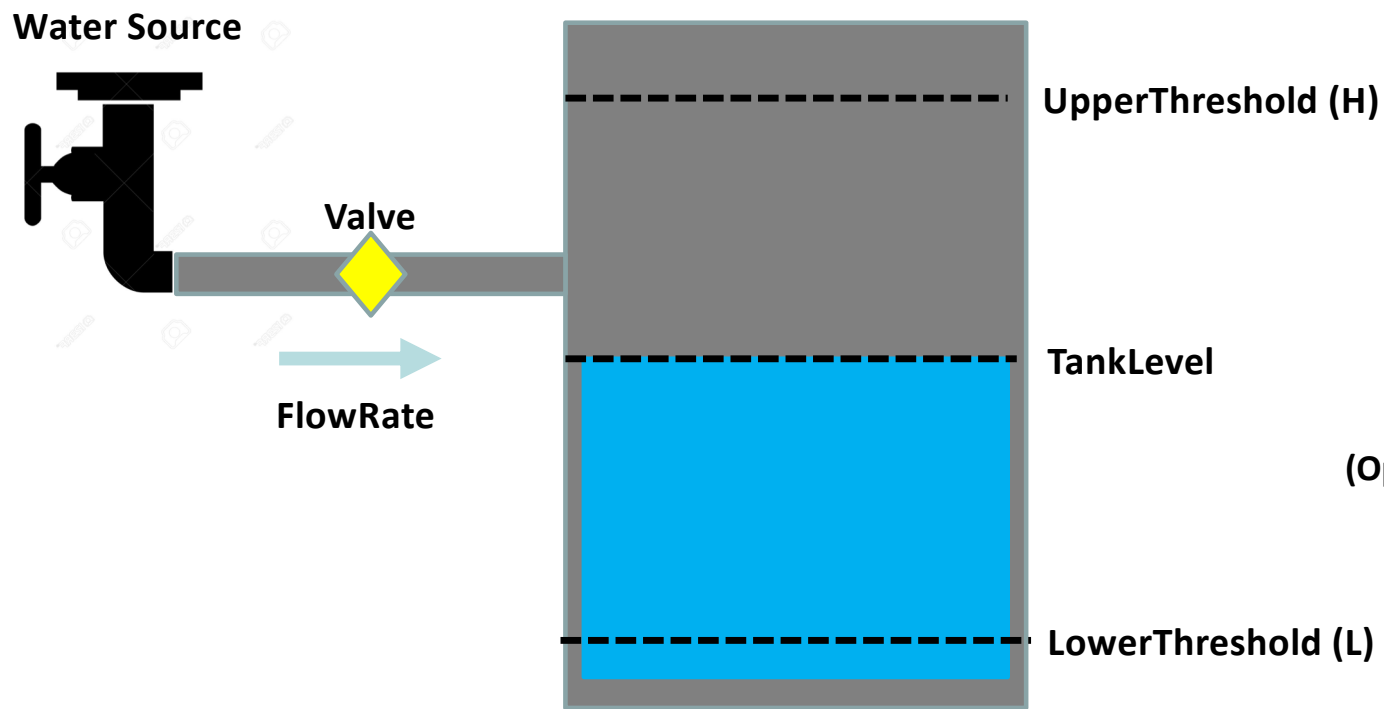


# HyPLC: Hybrid PLC Program Translation for Verification

ICCPS 2019 (Best Paper Finalist)

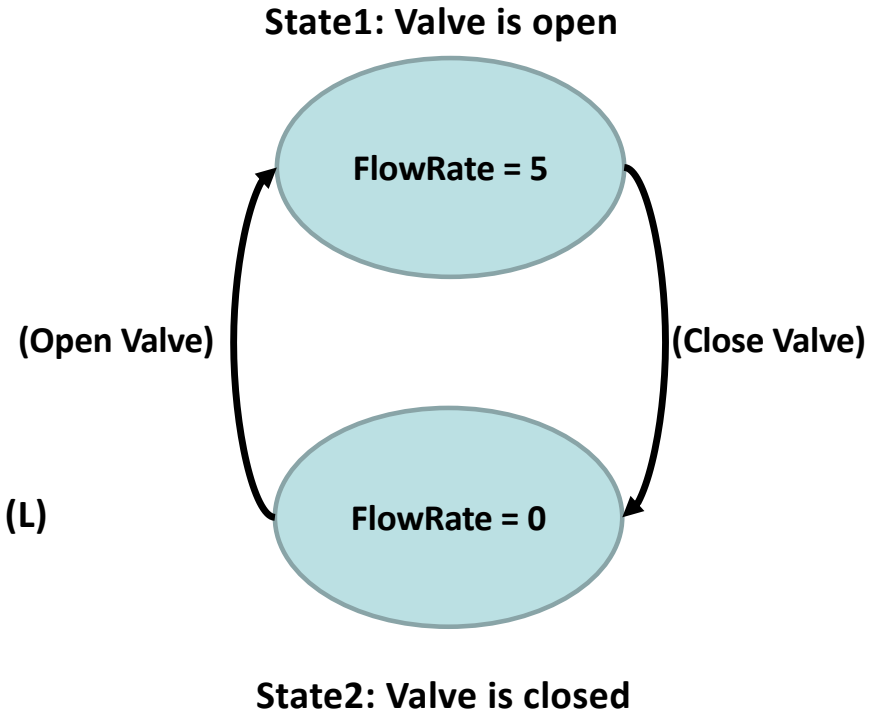


# Hybrid Systems Modeling of CPS



**Safety:  $L < \text{TankLevel} < H$**

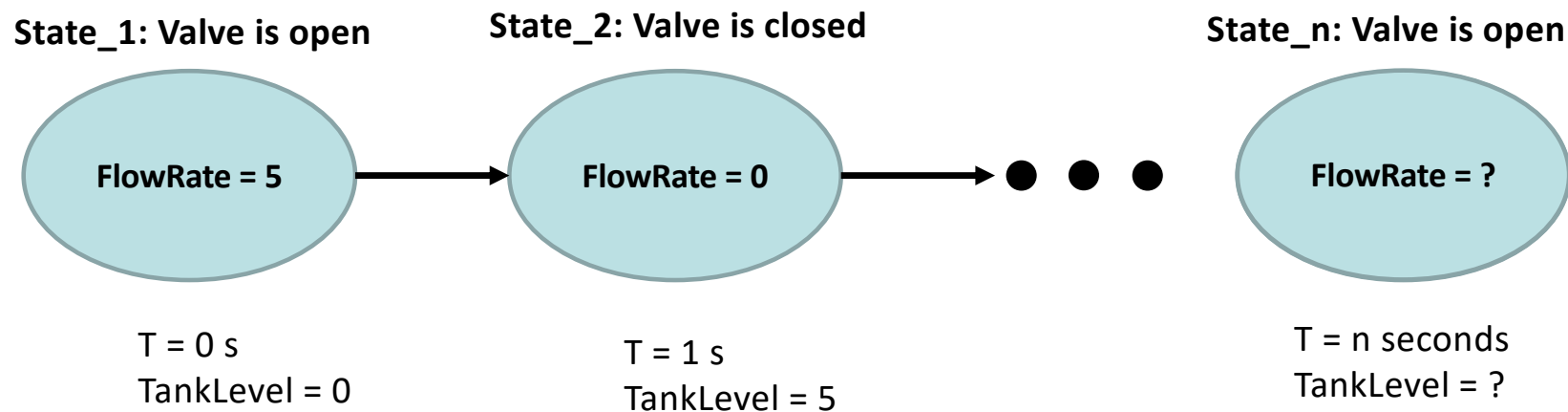
**Physical Plant:**  
 $\text{TankLevel} = \text{TankLevel} + \text{FlowRate} * \Delta t$



# Moving Away from Linear Temporal Logic (LTL)

Consider previous example

- **LTL expression: Globally(L<TankLevel<H)**



- Infinite amount of states for real-time systems
  - Can only provide runtime checks
- Real systems have non-deterministic states

**How can we guarantee safety forever for complex CPS?**

**Safety:  $L < \text{TankLevel} < H$**

# Verification Using Differential Dynamic Logic

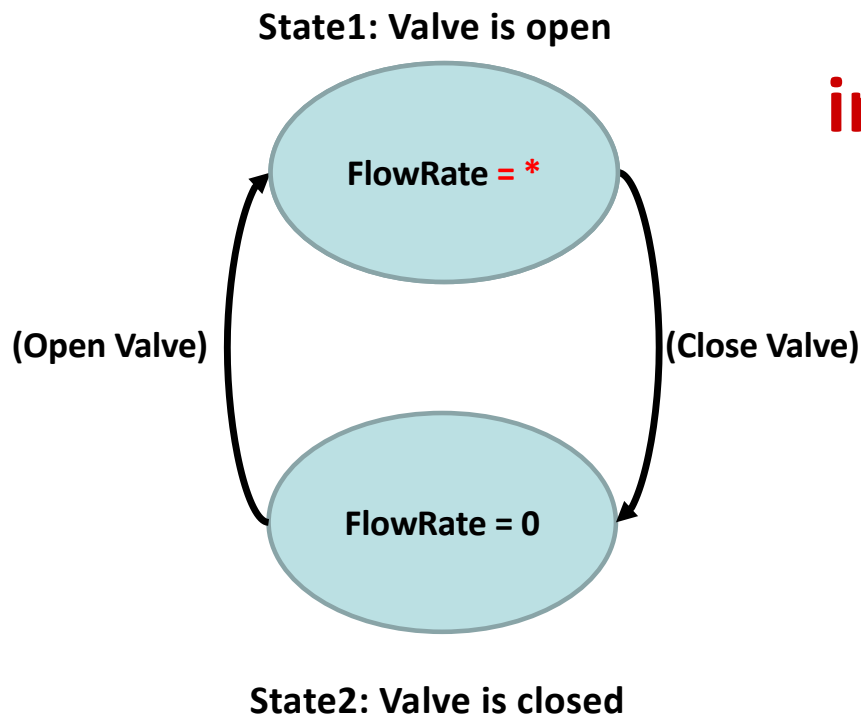
- $[\alpha]\psi$  For all states in  $\alpha$ ,  $\psi$  holds true (safety)
- $\langle\alpha\rangle\psi$  There exists a state  $\alpha$  where  $\psi$  is true (liveness)

## Definition (Hybrid program $\alpha$ )

$x' = f(x)$	(continuous evolution)
$x := \theta$	(discrete jump)
$? \chi$	(conditional execution)
$\alpha; \beta$	(seq. composition)
$\alpha \cup \beta$	(nondet. choice)
$\alpha^*$	(nondet. repetition)

# Verification Using Differential Dynamic Logic

$[\alpha]\psi$  For all states in  $\alpha$ ,  $\psi$  holds true (safety)



init

KeYmaera X Dashboard Models Proofs Theme - Help -

Escalator ▶ Auto ✎ Normalize ↺ Step back

Propositional - Quantifiers - Hybrid Programs - Differential Equations - Closing - Inspect -

Exhaustive prop

-R notR  
-L notL  
^R andR  
^L andL  
vR orR  
vL orL  
→R implyR  
→L implyL  
↔R equivR  
↔L equivL

↔CR commuteEquivR  
↔CL commuteEquivL

Cut ... cut

Goal 3

$x \geq 2 \vdash \{ \{ ?x > 1; x := x - 1; \cup \{ x' = v \wedge \text{true} \} \}^* x \geq 0$

loop

$\Gamma \vdash j(x), \Delta$

$j(x) \vdash [a] j(x)$

$j(x) \vdash P$

---

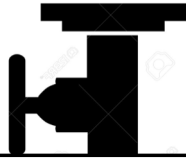
$\Gamma \vdash [a^*]P, \Delta$

[\*]  $[a^*]P \leftrightarrow P \wedge [a][a^*]P$

H)

# HyPLC: Hybrid Programmable Logic Controller Program Translation for Verification

Raw Water Source



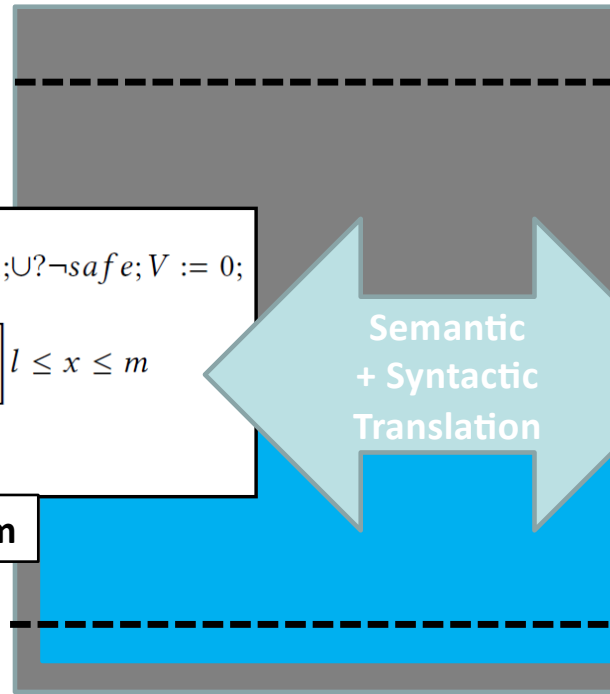
$$l \leq x \leq m \wedge \epsilon > 0 \rightarrow \left[ (f := *; ?safe; V := 1; \cup ?\neg safe; V := 0; t := 0; (x' = f * V, t' = 1 \wedge x \geq 0 \wedge t \leq \epsilon))^* \right] l \leq x \leq m$$

Where  $safe \equiv \left( \frac{l-x}{\epsilon} \leq f \leq \frac{m-x}{\epsilon} \right)$ .

Verified Hybrid Program



PLC



Safety:  $L < \text{TankLevel}$

```
PROGRAM prog0
  VAR_INPUT
    f : REAL;
    x : REAL;
  END_VAR

  VAR_OUTPUT
    V : BOOL;
  END_VAR

  IF((f<((L-x)/(Tsample+Tplc)))) THEN
    V:=0; ELSE
  IF((f>((H-x)/(Tsample+Tplc)))) THEN
    V:=0; ELSE
  IF((f>=((L-x)/(Tsample+Tplc)))) THEN
    IF((f<((H-x)/(Tsample+Tplc)))) THEN
      V:=1;
    END_IF;
  END_IF;
  END_IF;
  END_IF;
END_PROGRAM

CONFIGURATION Config0
RESOURCE Res0 ON PLC
TASK Main(INTERVAL:=T#Tsamplems,PRIORITY:=0);
PROGRAM Inst0 WITH Main : prog0;
END_RESOURCE
END_CONFIGURATION
```

PLC Structured Text



# Industrial Control System

Central Control (SCADA)

PLC

Control Logic

Firmware

I/O

Physical Plant

$$y' = x$$

 Sensor

 Actuator

 Network

# Verifier

Verifiable Hybrid Program

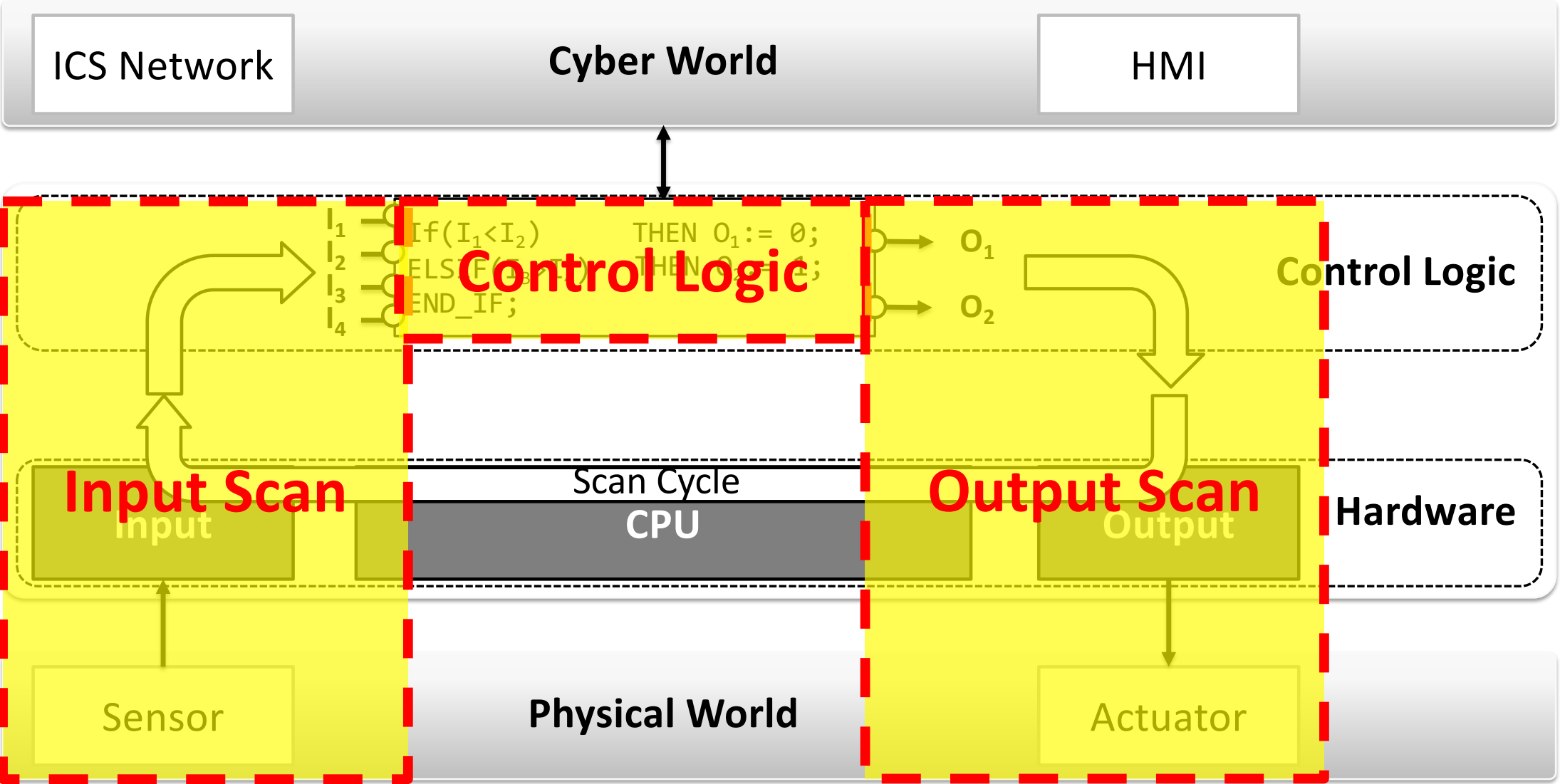
$$A \rightarrow [\{in; ctrl; plant\}^*]S$$

HyPLC

- Compilation rules
- Preserves semantics

Safety Theorem Prover  
(KeYmaera X)

# Revisiting the PLC Scan Cycle

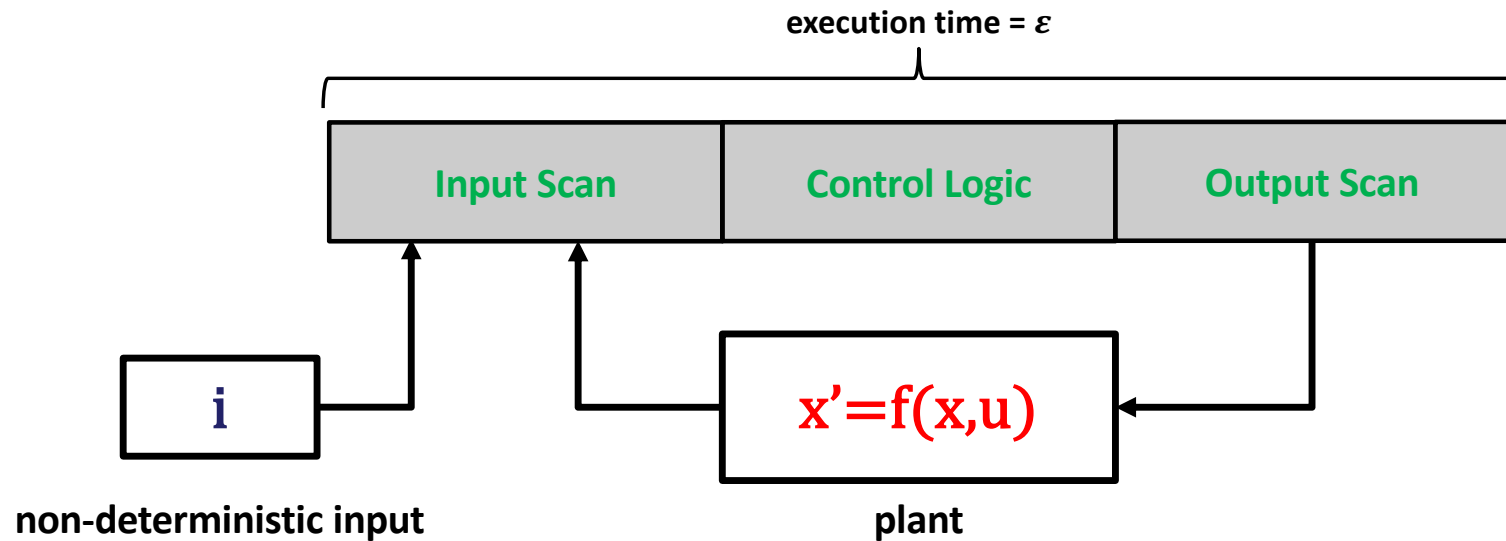


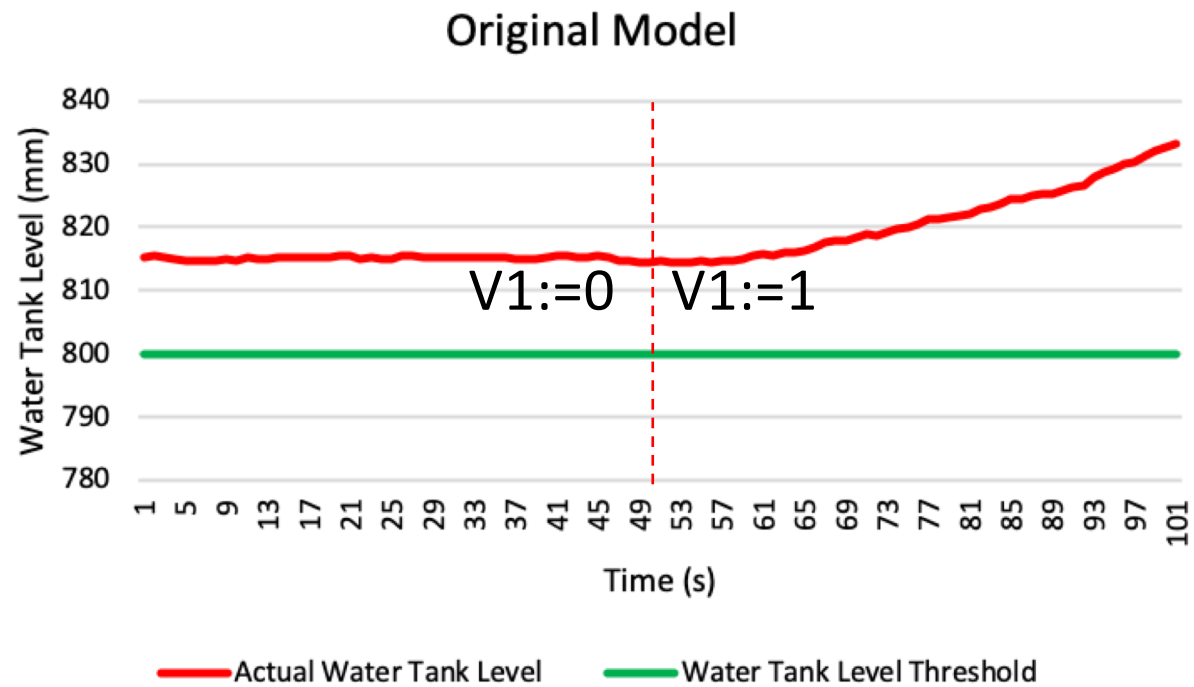
# Revisiting the PLC Scan Cycle

Hybrid Program Scan Cycle:  
(Normal Form)

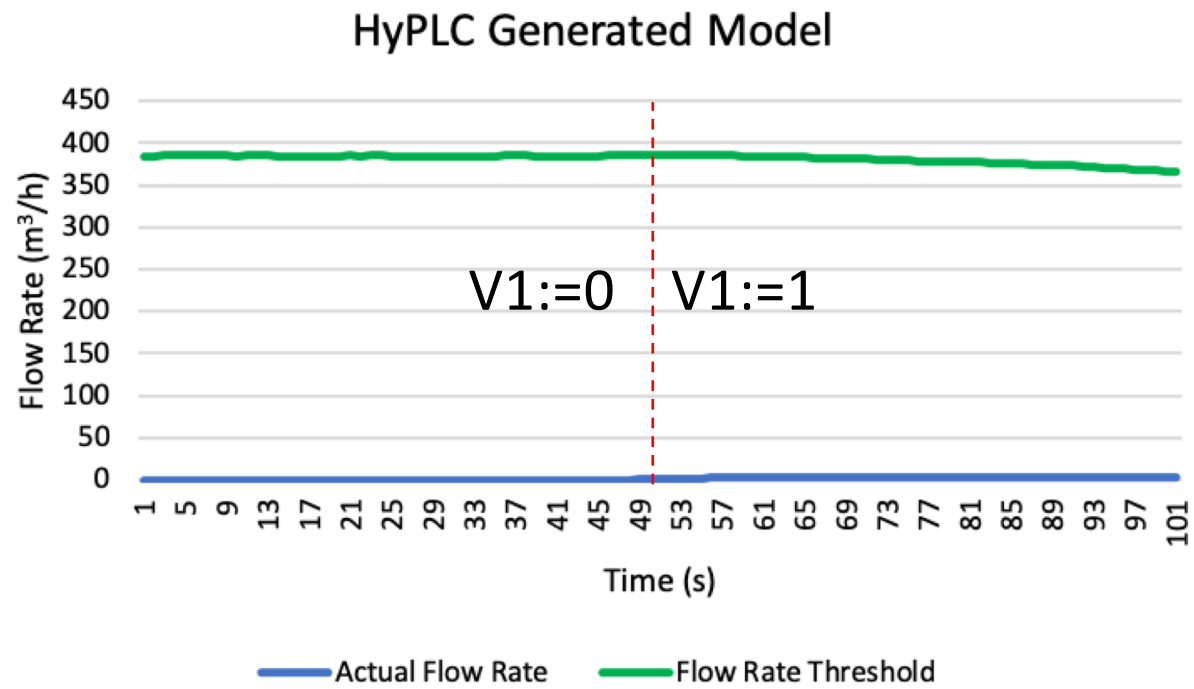
$$A \rightarrow [(i := *; u \in \text{ctrl}(x, i); t := 0; \{x' = f(x, u); t' = 1 \ \& \ t \leq \varepsilon\})^*] S$$

PLC Scan Cycle:

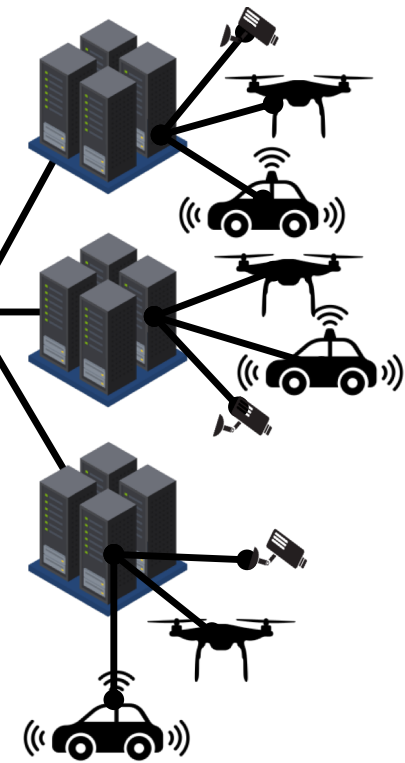
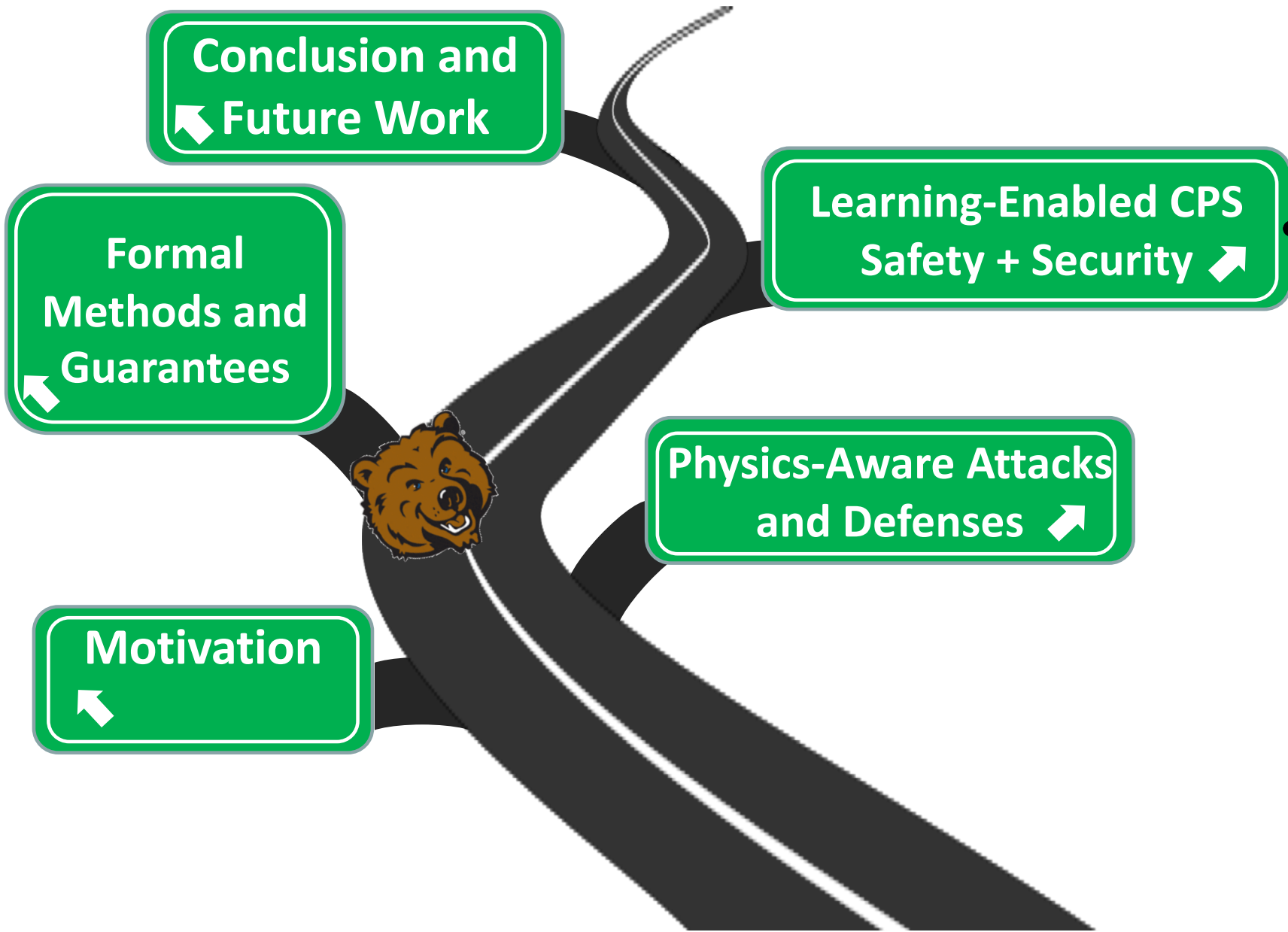


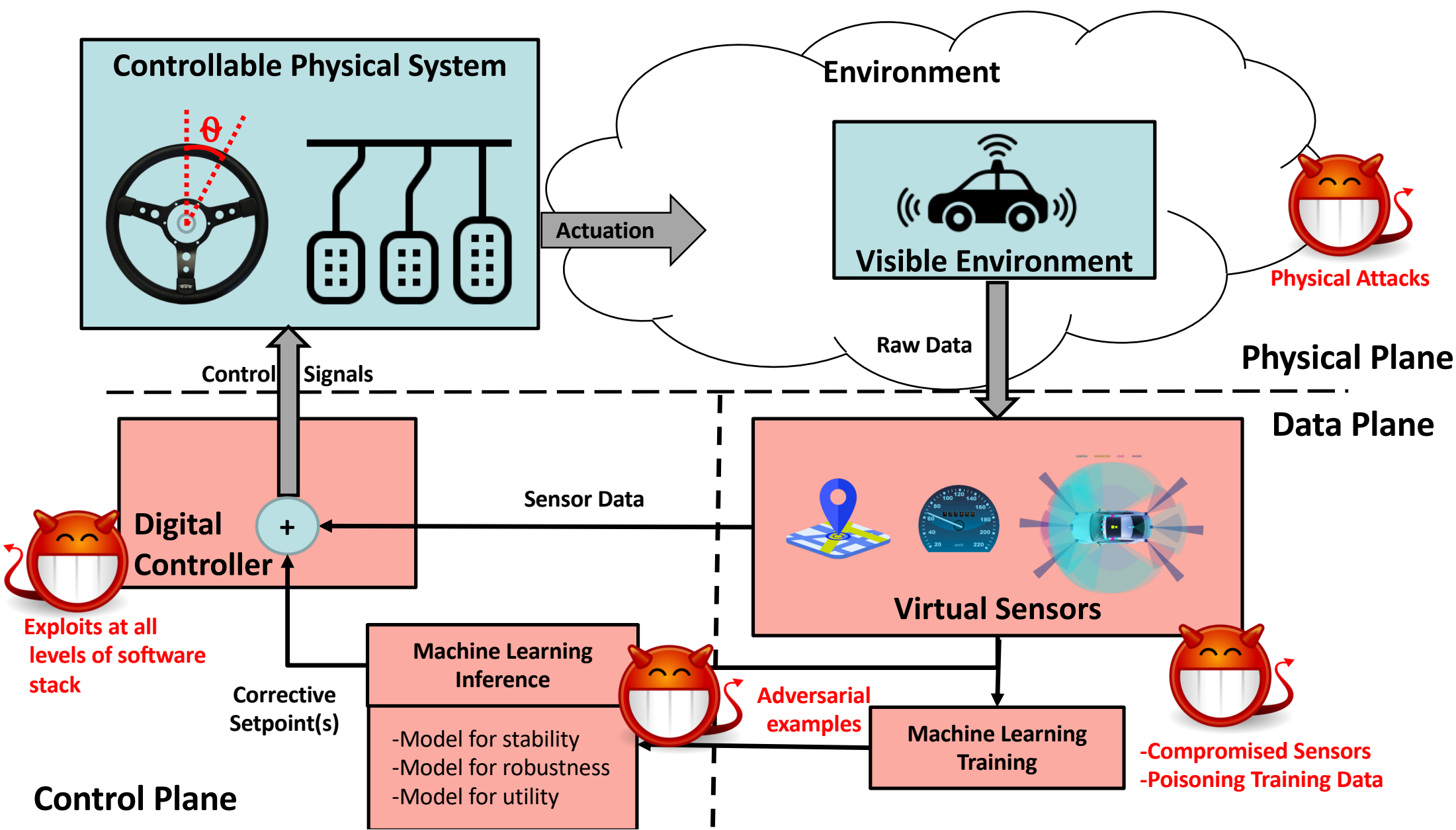


**\* Original model would have reported a violation for this entire sequence based on the water tank level**



\* HyPLC model reports this same sequence as safe operation based on flow rate

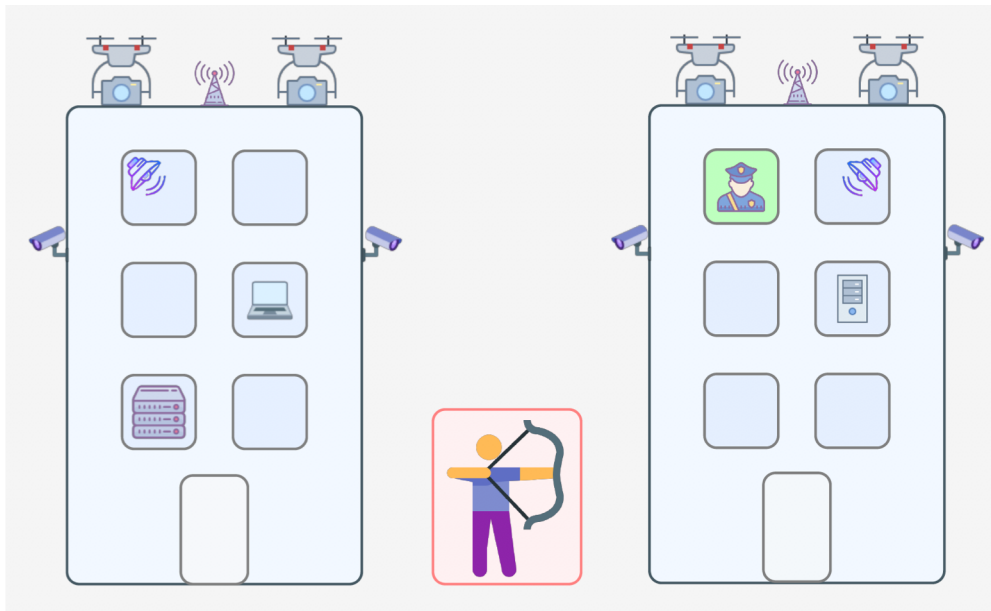






# Formal Guarantees for Macroprogramming Heterogeneous IoT Networks

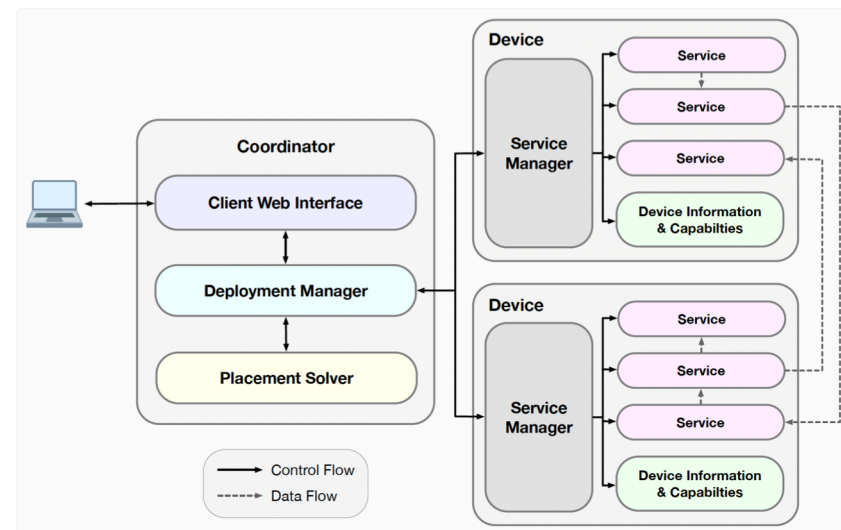
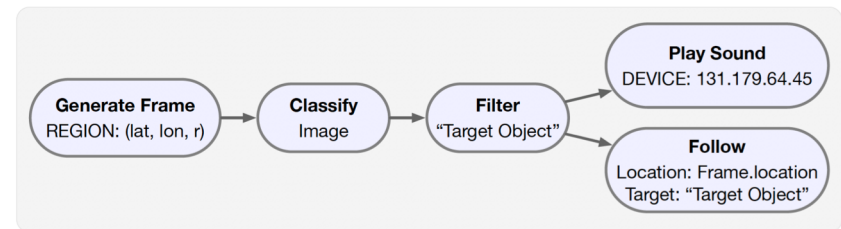
Smart City Environment



Officer to IoT Network: “Identify shooter and follow”

\*How do we expose device services safely?

DDFlow: Visualized Declarative Programming for  
Heterogeneous IoT Networks (IoTDI 2019)



# Formal Guarantees for Macroprogramming Heterogeneous IoT Networks

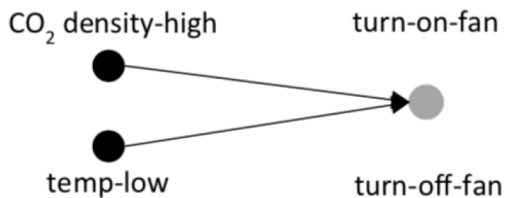
## Types of Policies

**P1: Mutually exclusive states must not exist in the environment.**

**P2: User-defined rules.**

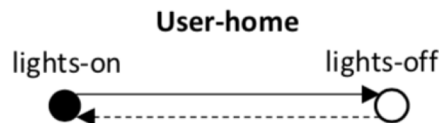
### Racing Events

E1: CO<sub>2</sub> density-high -> turn-on-fan  
E2: temp-low -> turn-off-fan



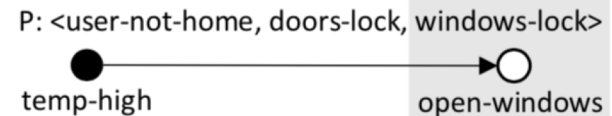
### Cyclic Events

E1: user-home, lights-on -> lights-off  
E2: user-home, lights-off -> lights-on



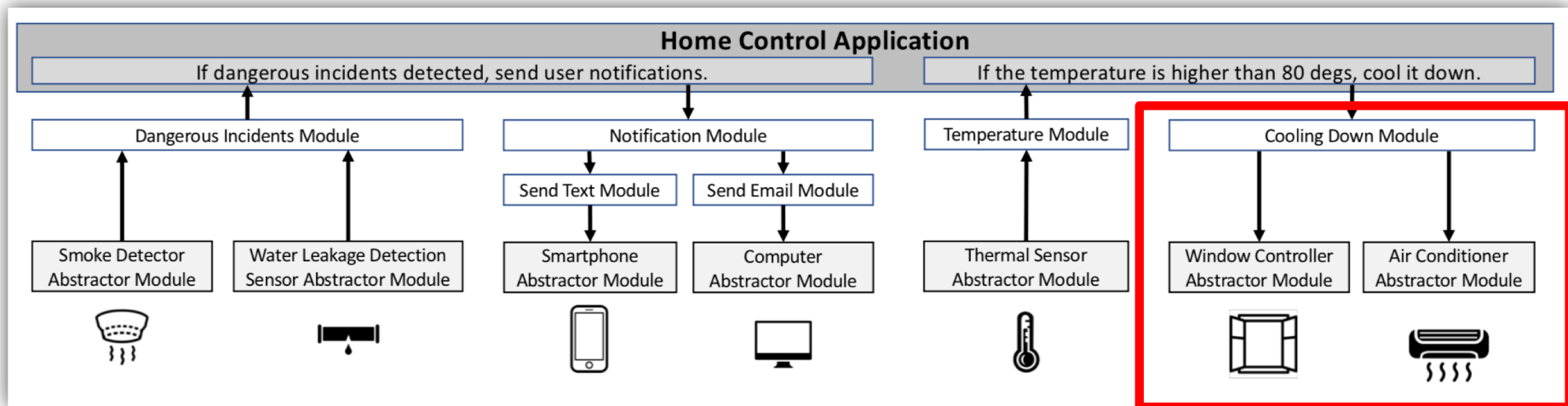
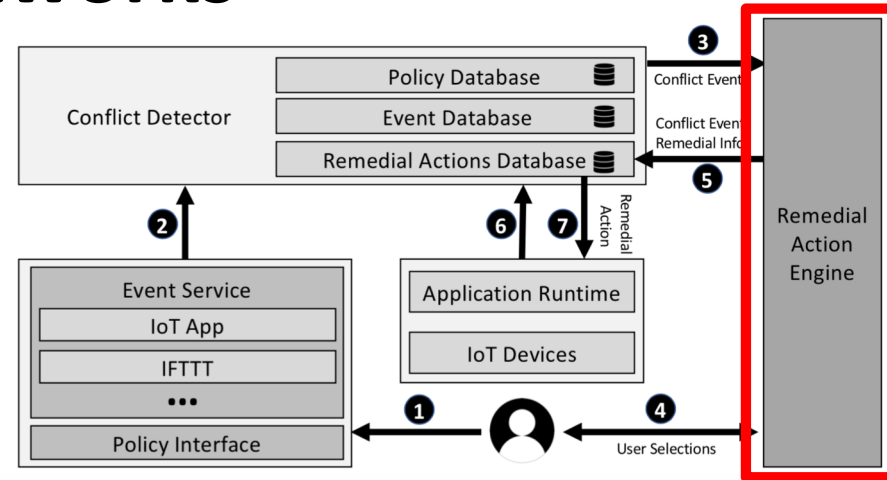
**E.g.: Doors and windows must be locked is user is not home**

E1: user-away -> user-away-mode-on  
E2: user-away-mode-on, temp-high -> windows-on

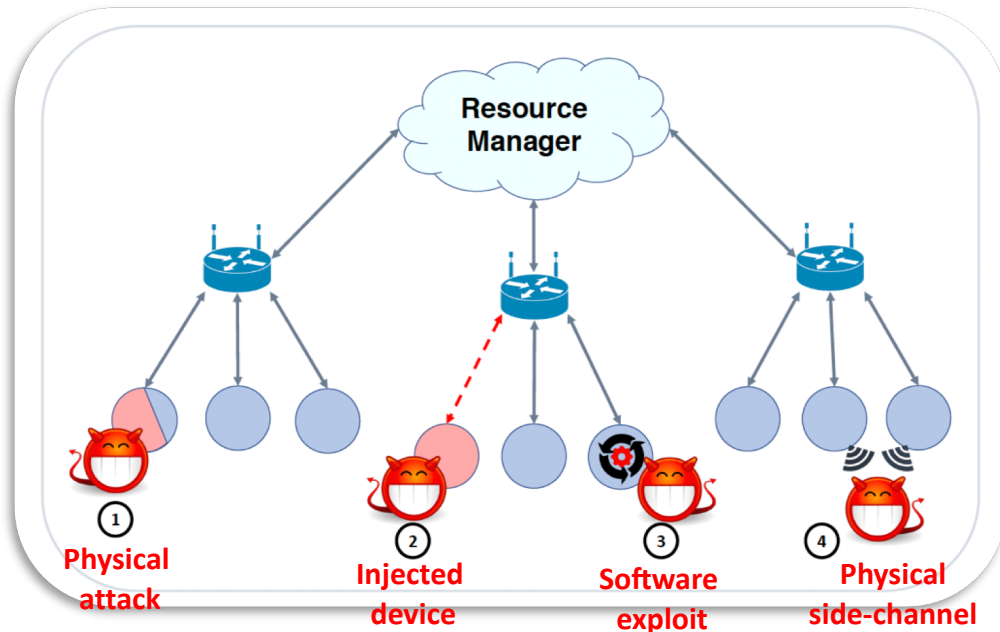


# Formal Guarantees for Macroprogramming Heterogeneous IoT Networks

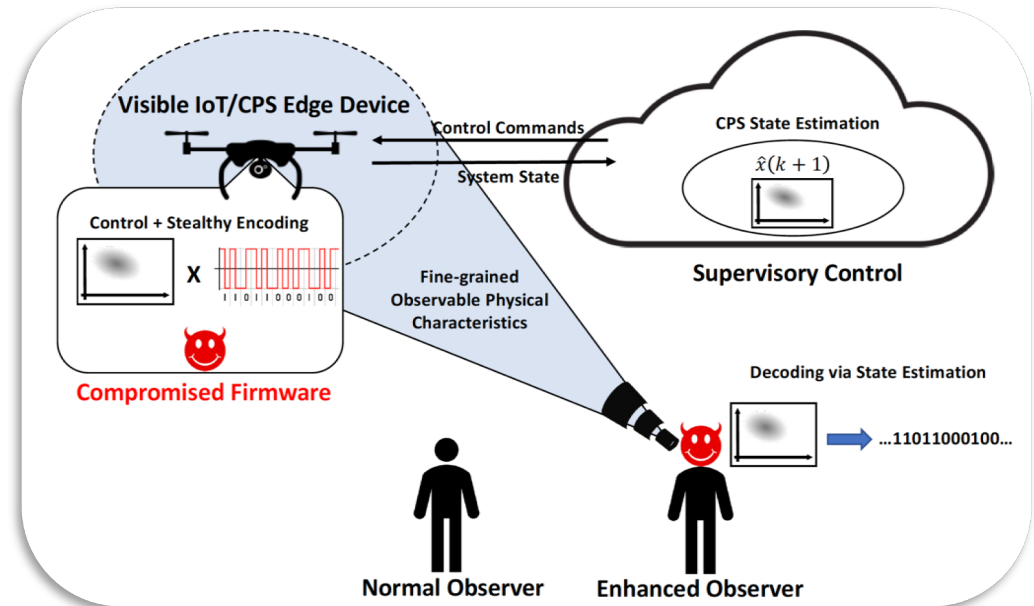
RemedIoT: Remedial Actions for Internet-of-Things Conflicts (BuildSys 2019)



# Characterizing Security in IoT Macroprogramming Environments

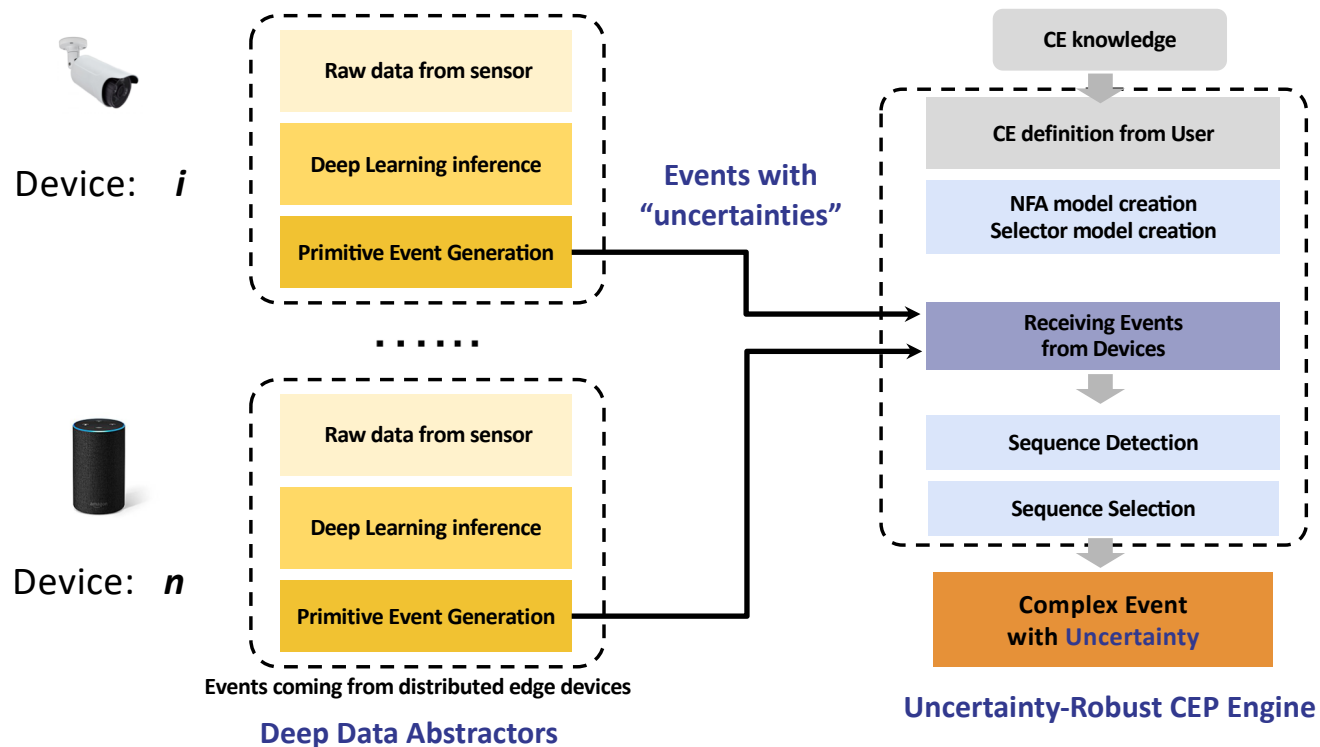


The Case for Robust Adaptation: Autonomic Resource Management is a Vulnerability (MILCOM 2019)



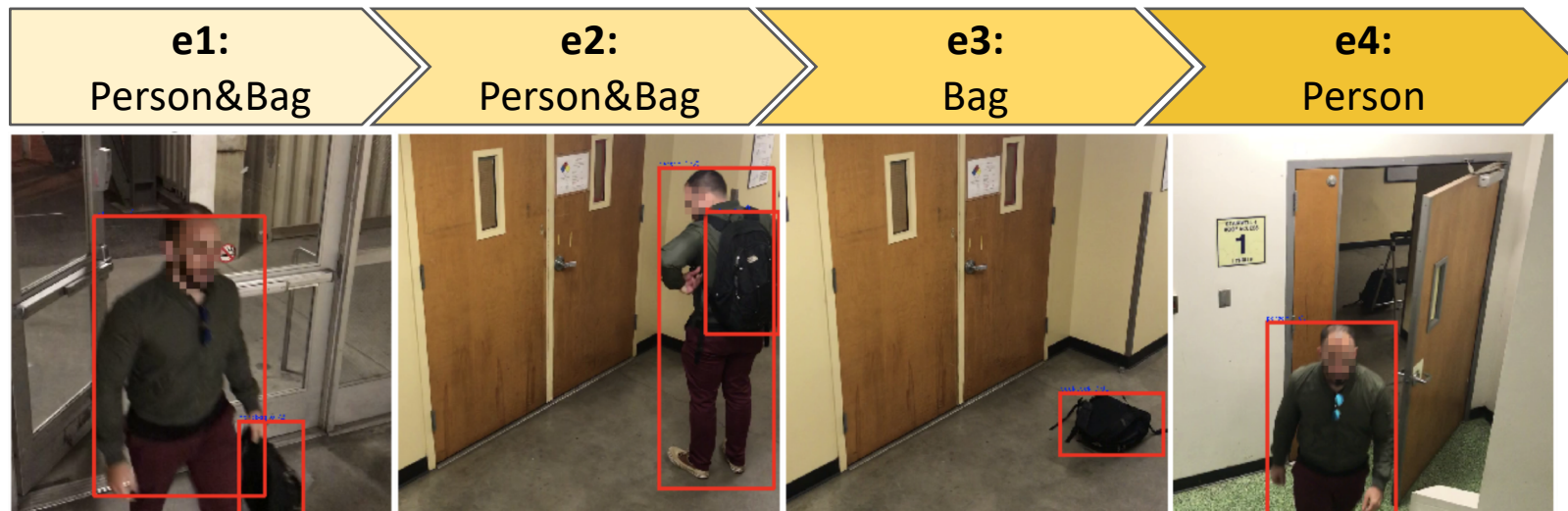
Let's Talk Through Physics! Covert Cyber-Physical Data Exfiltration on Air-Gapped Edge Devices (Submitting to USENIX 2020)

# Robust Multi-modal Inferencing in Heterogenous IoT Environments



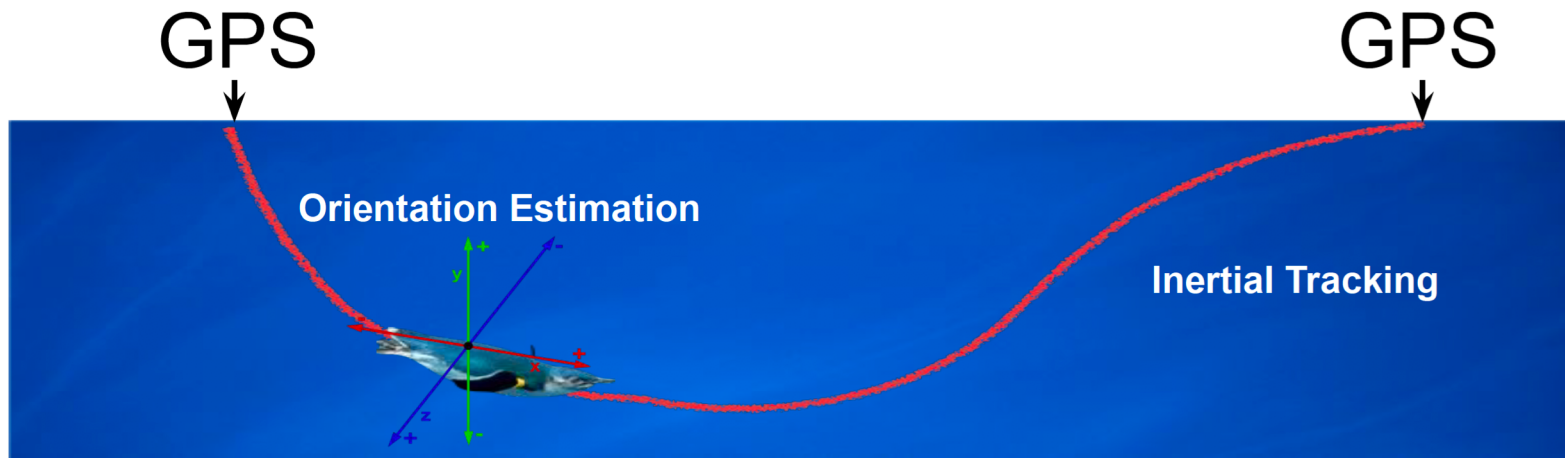
**DeepCEP: Deep Complex Event Processing Using Distributed Multimodal Information (SMARTCOMP 2019)**

# Robust Multi-modal Inferencing in Heterogenous IoT Environments

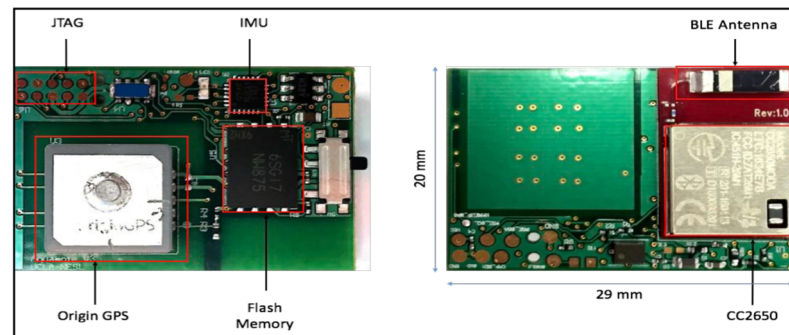


Example: Detecting an unattended bag

# Some future applications...



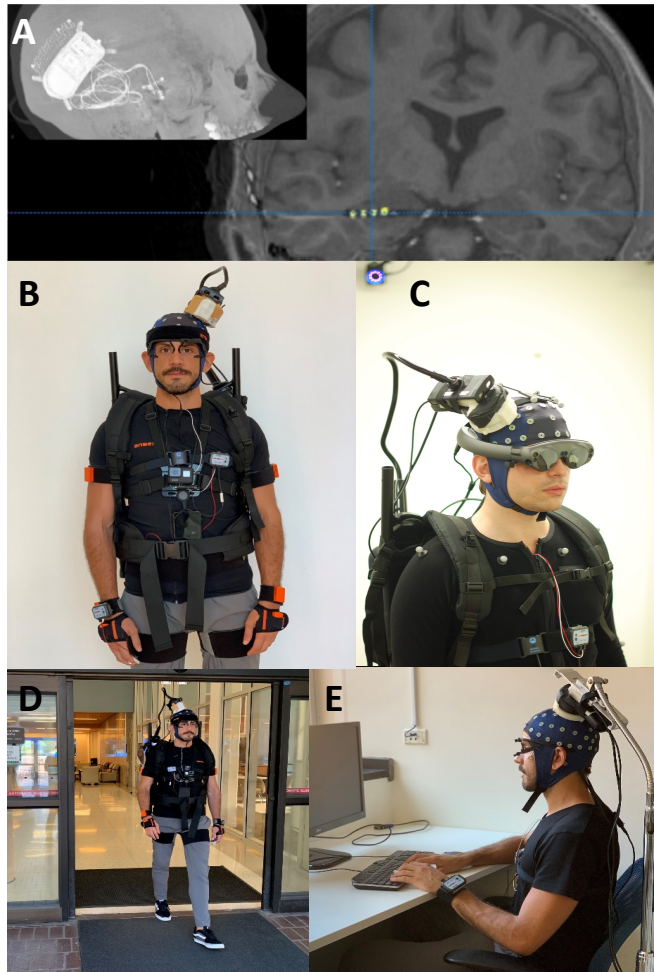
## 3D Trajectory and Orientation Estimation of Marine Animals



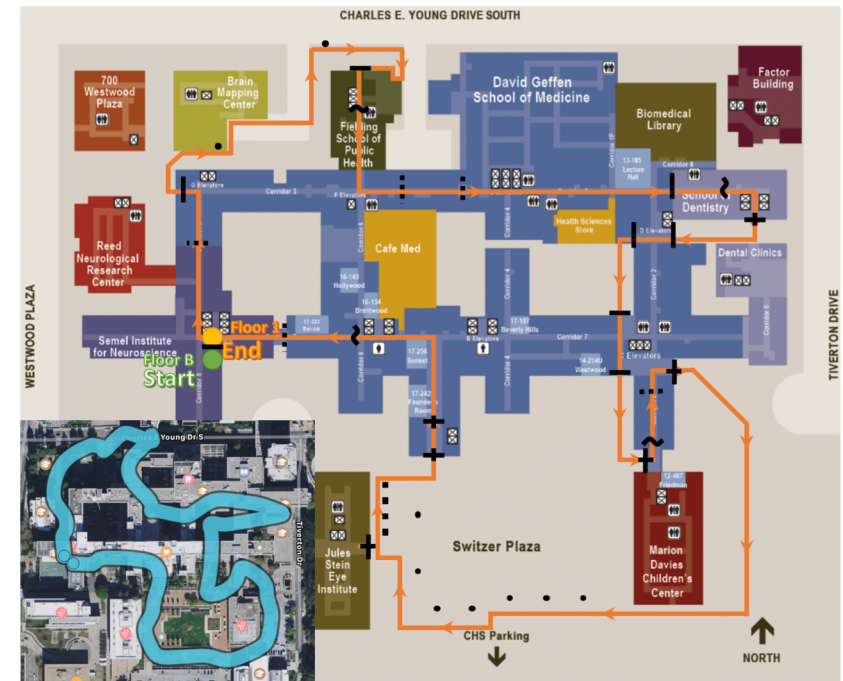
Aquamote v3



# Some future applications...

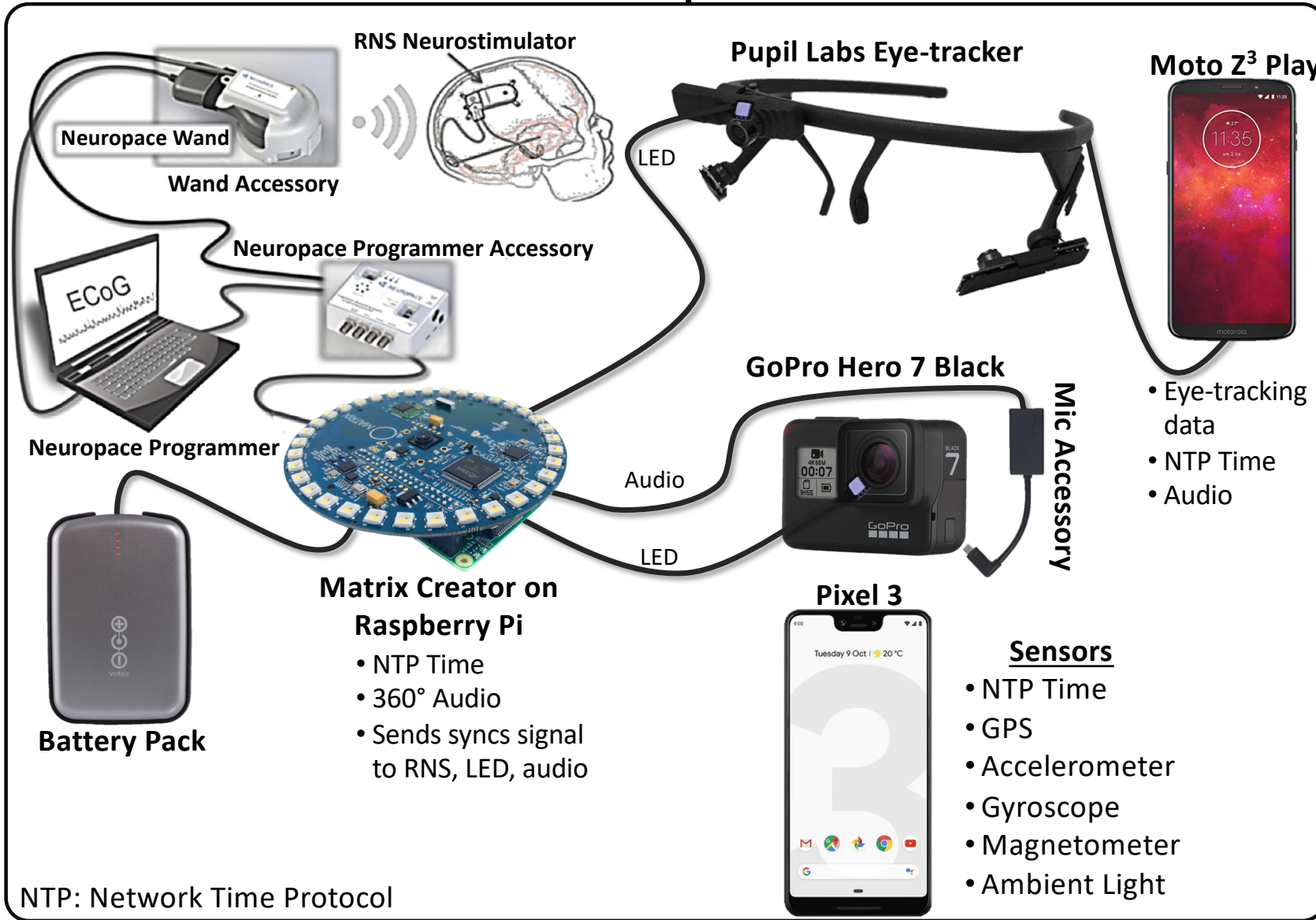


## Decoding How Humans Encode Memory

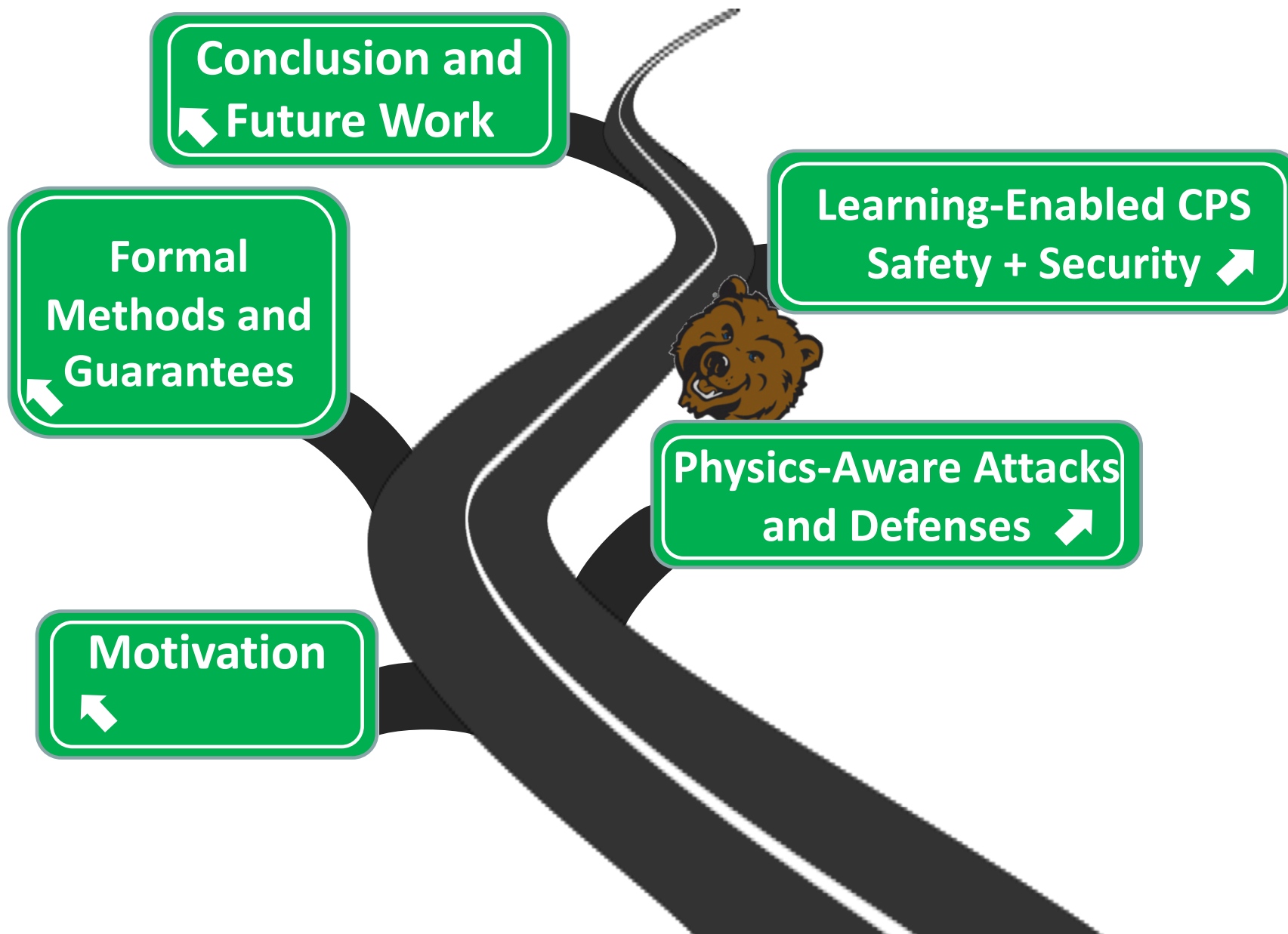


# Participant

# Researcher



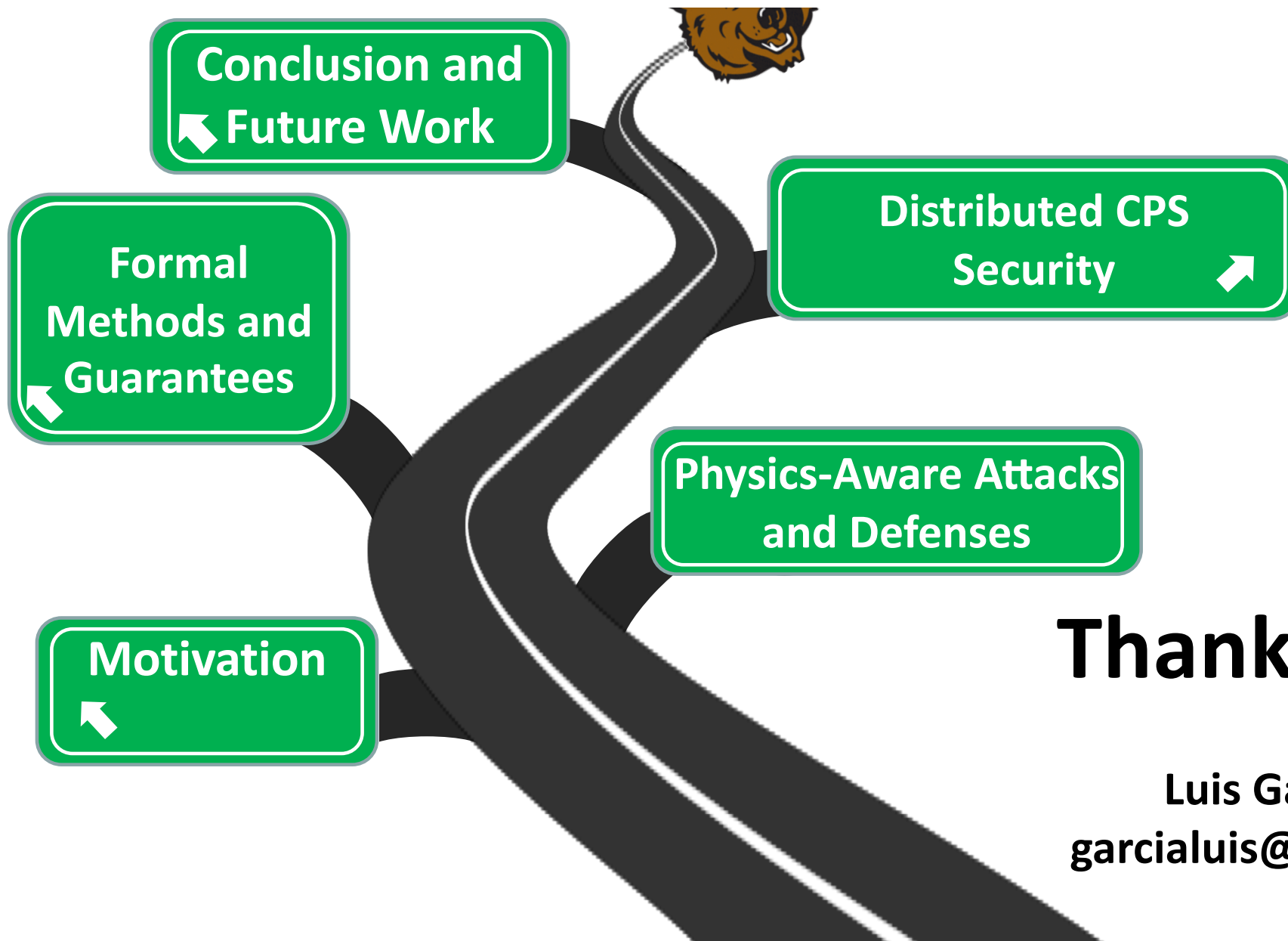
NTP: Network Time Protocol



# Conclusion

- Practical security analysis based on **physical properties** of CPS
  - *Harvey: a physics-aware, two-faced rootkit malware*
  - *Physics-aware defenses for at various levels of distribution as well as in domain-specific scenarios*
- Applicability of **formal deductive verification** techniques in complex CPS
  - *HyPLC: a bi-directional translation of PLC controller code and differential dynamic logic hybrid programs*
- Practical security and safety considerations for **learning-enabled IoT/CPS**
  - *Formal guarantees for macroprogramming heterogenous IoT networks*
  - *Multi-modal inferencing in distributed and heterogenous IoT networks*
- **Future Work**
  - *Scalable deductive security verification via compositional verification and safety contracts*
  - *Robust multi-modal inferencing in distributed and contested environments*
  - *Security detection and intervention in IoT macroprogramming environments*

**Questions?** 87



**Thank You!**

**Luis Garcia**  
garcialuis@ucla.edu

# Publications

- Data Flow Security for Mobile Devices
  - Context Aware Information-Flow-Based Micro-Security Perimeters for Mobile Devices. *DSN*, 2016
- Cyber-physical Vulnerability Assessment
  - A Cyber-Physical Modeling and Assessment Framework for Power Grid Infrastructures. *IEEE Transactions on Smart Grid*, 2015
  - Covert Channel Communication Through Physical Interdependencies in Cyber-Physical Infrastructures. *IEEE SmartGridComm*, 2014
  - Threat Model Quantification in Smart Grid Critical Infrastructures. *IEEE SmartGridComm 2014*
- Embedded Systems Verification and Controller Security
  - Hey, My Malware Knows Physics! Attacking PLCs with Physical Model Aware Rootkit. *NDSS*, 2017
  - Detecting PLC Control Corruption via On-Device Runtime Verification. *IEEE Resilience Week 2016*
- Cyber-physical Control Flow Integrity
  - Cyber-Physical Control Flow Integrity for Distributed Controllers. *Submitting to ICCPS 2019*
  - PAtt: Physics-based Attestation of Control Systems. *RAID 2019*
  - Tell Me More than Assembly Instructions! Reversing Semantics of IoT Software Binaries. *DSN 2019*
  - See No Evil, Hear No Evil, Feel No Evil, Print No Evil? Malicious Fill Patterns Detection in Additive Manufacturing. *USENIX Security*, 2017
- Hybrid Systems Modeling and Verification
  - HyPLC: Hybrid Programmable Logic Controller Program Translation for Verification. *ICCPS 2019 (Best Paper Finalist)*
- Macroprogramming of Distributed and Heterogeneous IoT/CPS
  - Let's Talk Through Physics! Covert Cyber-Physical Data Exfiltration on Air-Gapped Edge Devices. *Submitting to USENIX Security*, 2020
  - DDFlow: Visualized Declarative Programming for Heterogeneous IoT Networks. *IoTDI 2019*
  - RemedIoT: Remedial Actions for Internet-of-Things Conflicts. *BuildSys 2019*
- Robust Multi-modal Inferencing
  - PhysioGAN: Training High Fidelity Generative Model for Physiological Sensor Readings. *Submitted to TPAMI 2019*
  - DeepCEP: Deep Complex Event Processing Using Distributed Multimodal Information. *SmartComp 2019*
  - RadHAR: Human Activity Recognition from Point Clouds Generated through a Millimeter-wave Radar. *MMNets 2019*

# Formal Verification of Hybrid Controller Logic for Transient Stability

*(Case Study)*





# Transient Stability of a Simple Power System

Final Complete SMIB Hybrid Program

$init \Rightarrow [\{ctrl; plant \& H\}^*](req)$

$$init \equiv P_M = 1 \wedge P_{e,max} = \frac{3}{2} \wedge \omega = 0 \wedge \theta = \arcsin\left(\frac{P_M}{P_{e,max}}\right)$$

$$\wedge \theta_{max} = \pi - \theta \wedge \sin \theta = \frac{P_M}{P_{e,max}} \wedge \cos \theta = \frac{\sqrt{P_{e,max}^2 - P_M^2}}{P_{e,max}}$$

$$\wedge c = 2P_M\theta_{max} - 2P_{e,max} \cos(\theta)$$

$$ctrl \equiv \{(a := P_M - P_{e,max} \sin(\theta)) \cup (a := P_M)\};$$

$$t := 0;$$

$$d0 := d;$$

$$v0 := v;$$

$$T := *;$$

$$?T \geq 0 \wedge \theta_f(T) \leq \theta_e$$

$$\wedge 360c \leq 720\theta P_M + 720P_{e,max} - 360\theta_f(T)^2 P_{e,max} + 30\theta_f(T)^4 P_{e,max} - \theta_f(T)^6 P_{e,max} - 360\omega_f(T)^2$$

$$plant \equiv \theta' = \omega, \omega' = a, \sin \theta' = \omega \cos \theta, \cos \theta' = -\omega \sin \theta, t' = 1$$

$$H \equiv \sin^2 \theta + \cos^2 \theta = 1$$

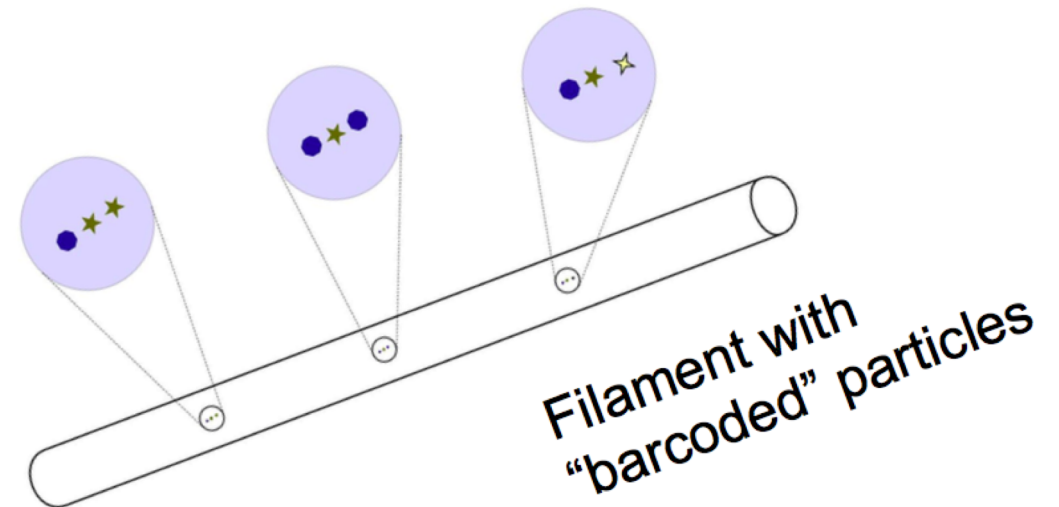
$$req \equiv \theta \leq \theta_{max}$$

The screenshot shows the KeYmaera X interface with a goal and a loop invariant. The goal is  $x \geq 2 \vdash \{ \{ ?x > 1; x := x - 1; \cup \{ x' = v \wedge true \} \}^* x \geq 0$ . The loop invariant is  $[a^*]P \leftrightarrow P \wedge [a][a^*]P$ . The interface includes a menu bar with 'Escalator', 'Auto', 'Normalize', and 'Step back'. Below the menu bar are tabs for 'Propositional', 'Quantifiers', 'Hybrid Programs', 'Differential Equations', 'Closing', and 'Inspect'. A list of logical rules is visible on the left, including  $\neg R$ ,  $\wedge R$ ,  $\vee R$ ,  $\rightarrow R$ ,  $\leftrightarrow R$ , and 'Cut ...'. The main area shows a goal and a loop invariant with a 'loop' button. A large red and green watermark 'Not Faulted' is overlaid on the bottom right of the screenshot.

# Embedding material as identification markers

Embedded materials are unknown to third party

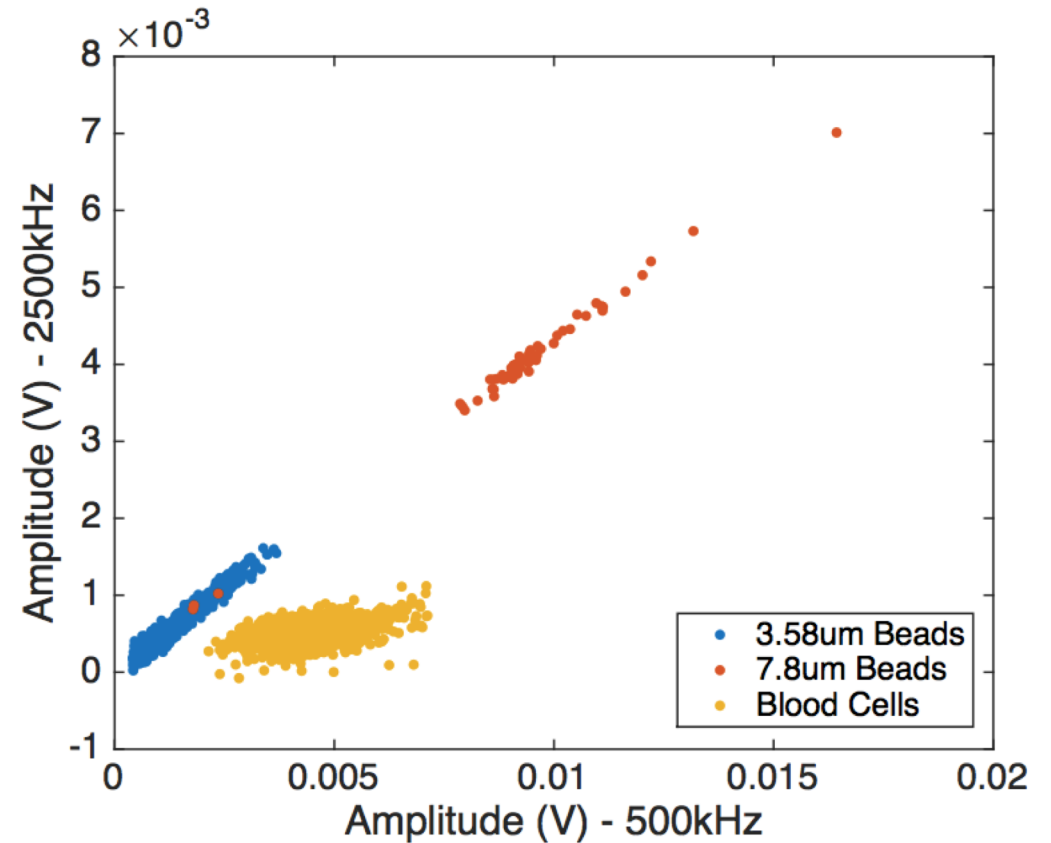
- Micro/nano materials are invisible to naked eyes
- Unknown materials averts the scanning attempts without correct modality
- Embedding material as “barcode” for unique identification



## Previous study: identification markers in blood tests



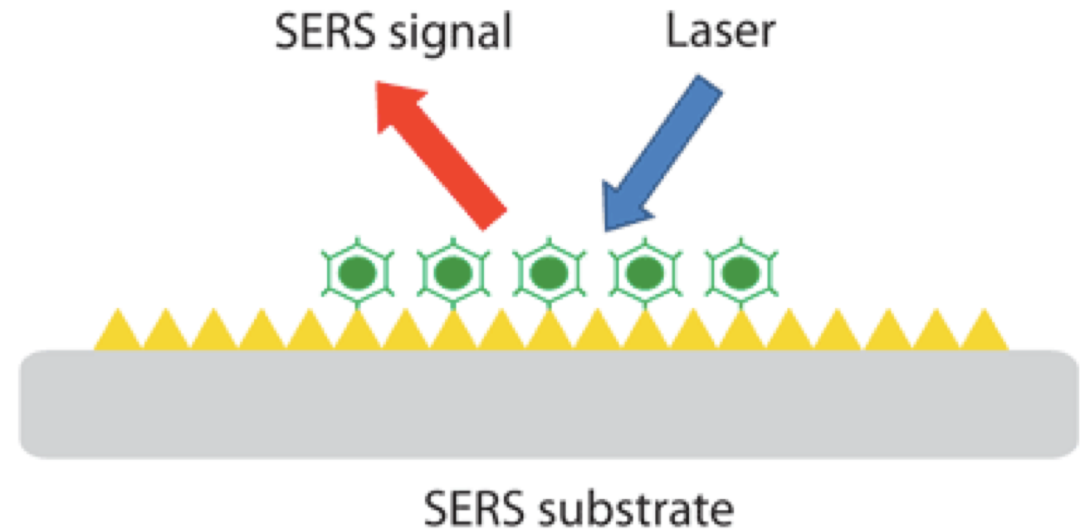
Microbeads with different size and concentration can identify multiple test results



## Embedding markers

### Raman spectroscopy

- Monochromatic light source
- Measure the shifted frequency of incident light

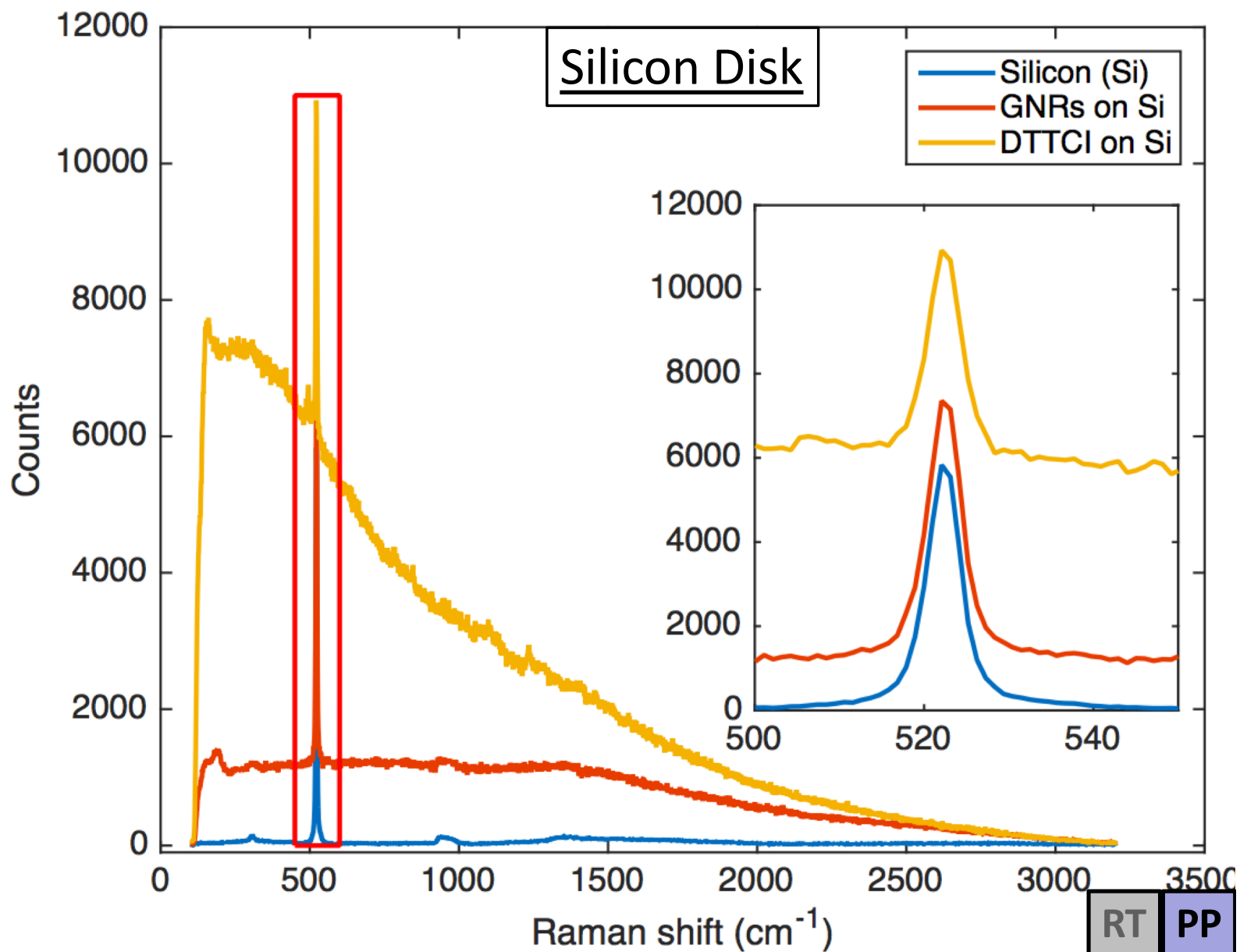


Surface-enhanced Raman spectroscopy (SERS) using gold nanorods (GNRs) or 3,3'-Diethylthiatricarbocyanine iodide (DTTCI) as enhancers

***Embedded markers selection based on the availability of scanning modalities***

# Raman Spectroscopy

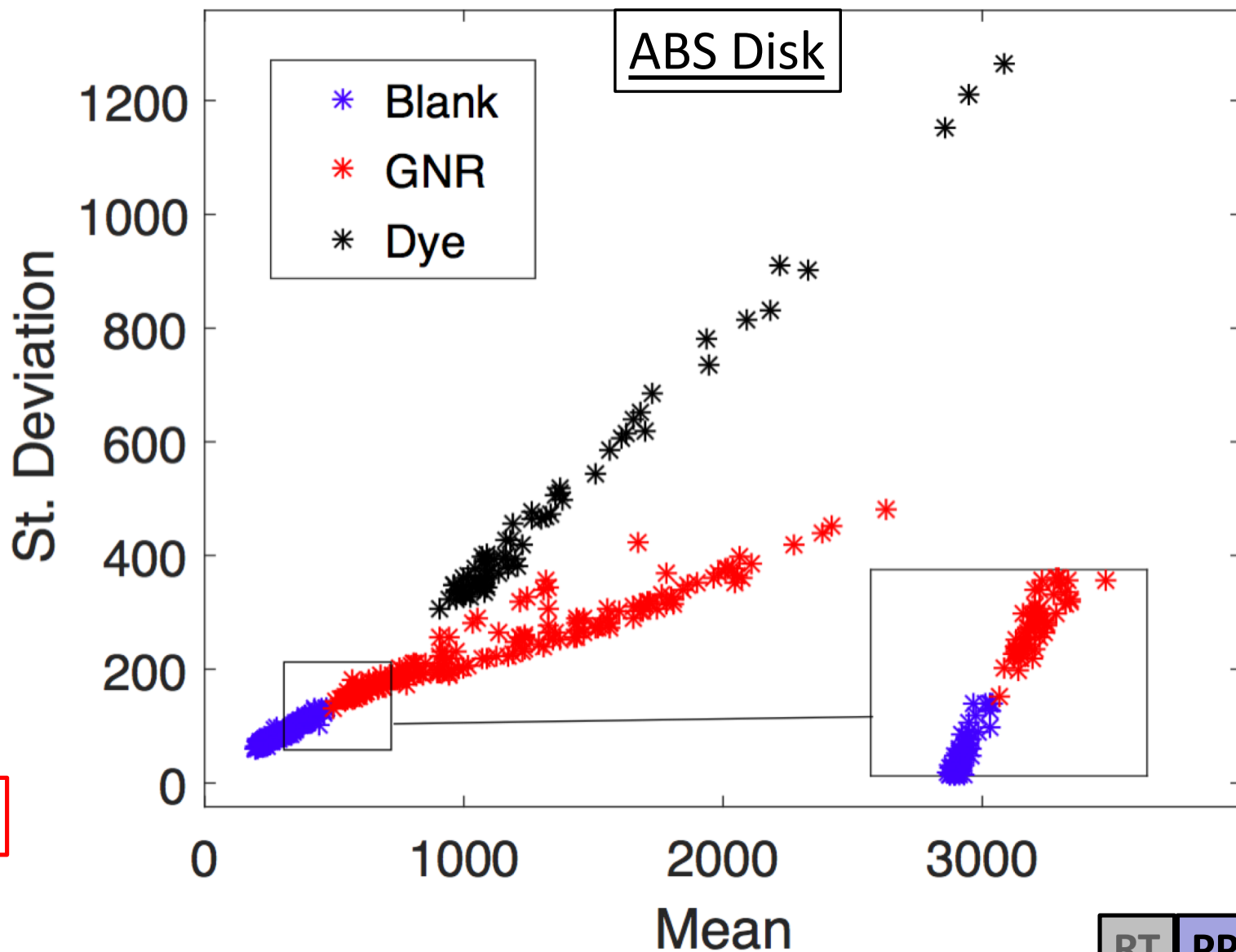
GNRs and dye (DTTCl) in SERS result in **enhanced spectral response**



# Raman Spectroscopy

Spectral response of markers are easily distinguishable.

**300um depth limitation**



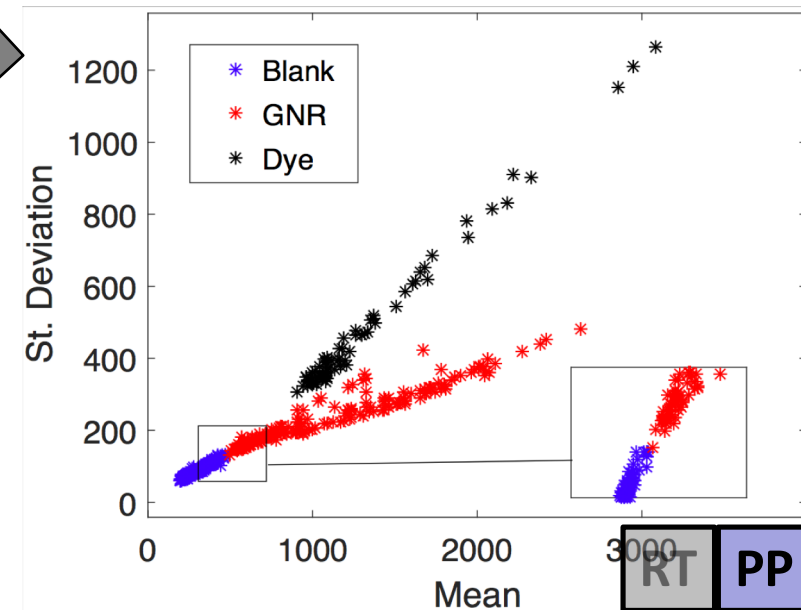
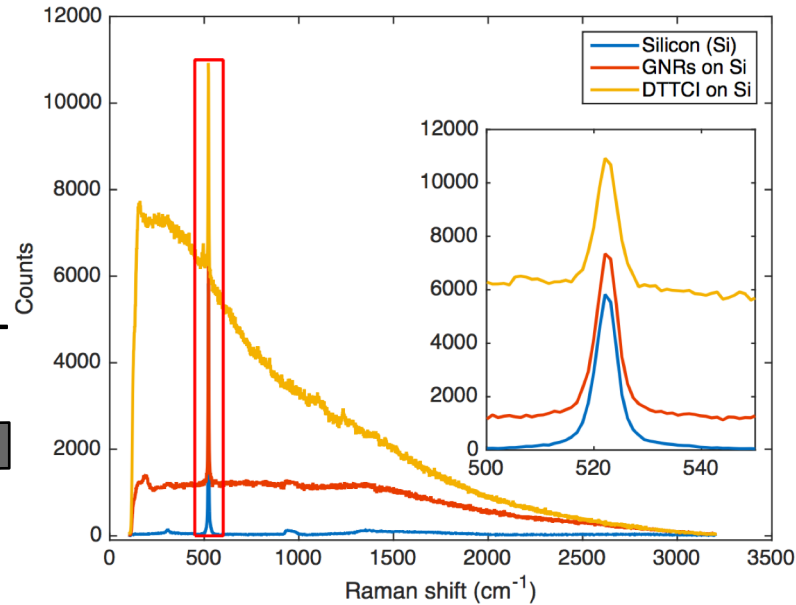
# Raman spectroscopy

- Enhanced spectral response of Silicon using GNRs and dye (DTTCI) in SERS
- Spectral responses of control ABS and embedded ABS show
  - Unadulterated ABS has more concentrated spectral response
  - Spreading response due to uncontrolled concentration of embedded markers

***300um depth limitation***

SERS initial results of Silicon spectrum.

SERS response of control and embedded 3D disks





# MicroCT

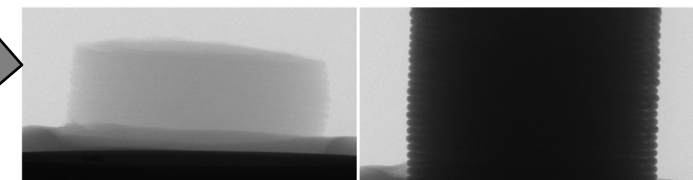
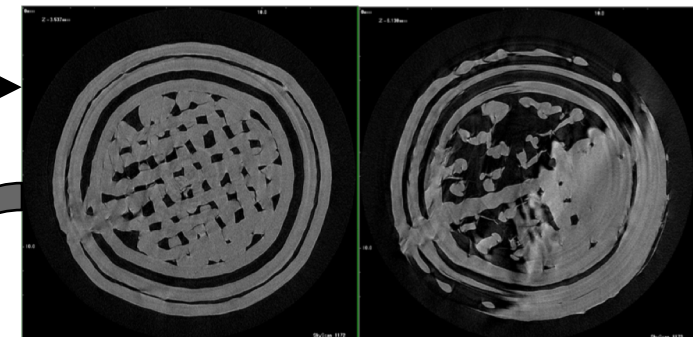
- Higher depth scan for 3D object reconstruction
- Steel saturated filament is used for higher contrast

MicroCT scan of ABS cylindrical tube with embedded GNRs



Skyscan MicroCT 1172 scanner

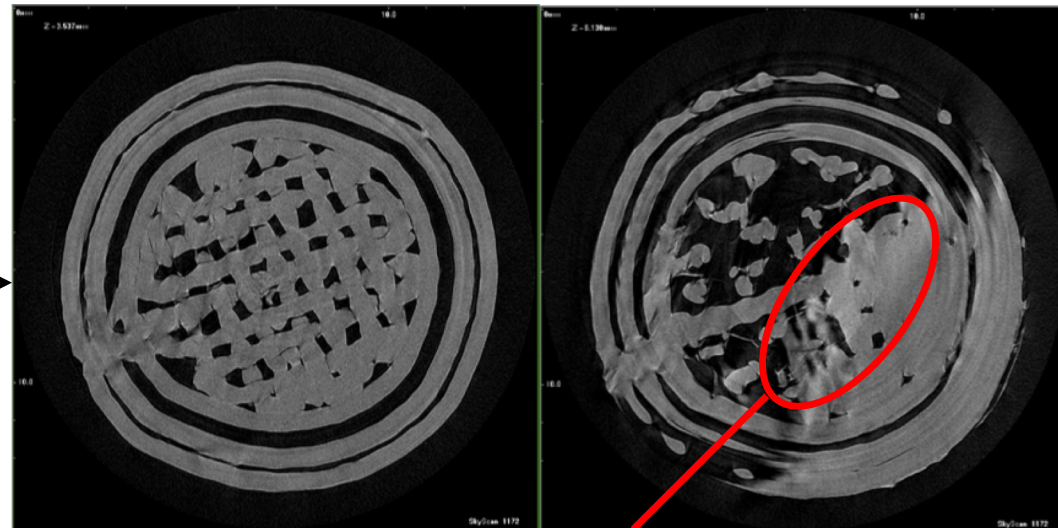
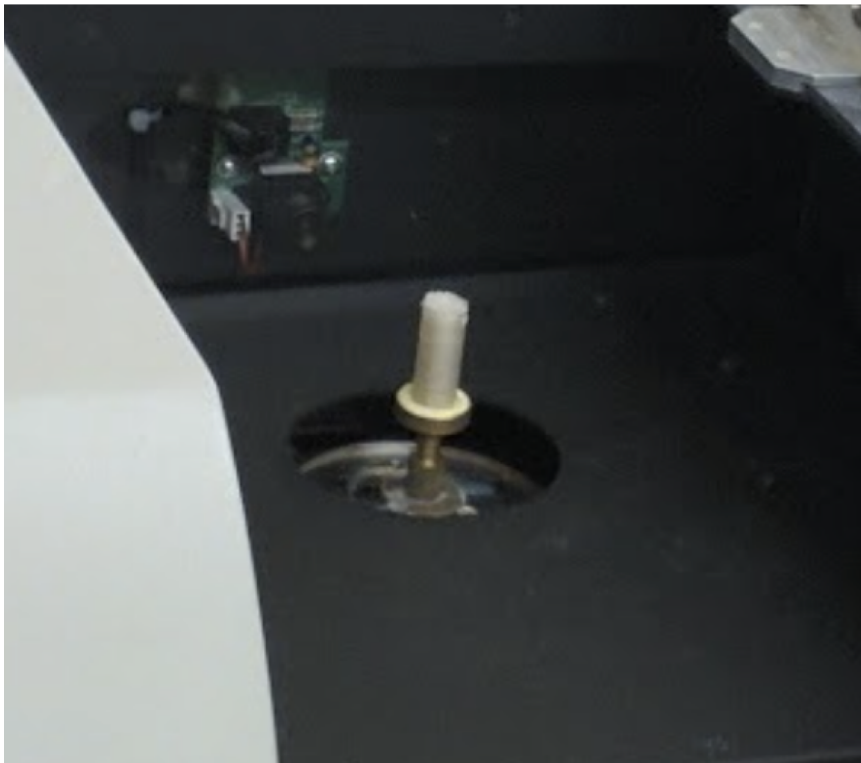
Changing filament and markers for higher contrast



MicroCT scan of PLA (left) and steel embedded

## MicroCT scanning: ABS filament

MicroCT scan of ABS cylindrical tube with embedded GNRs

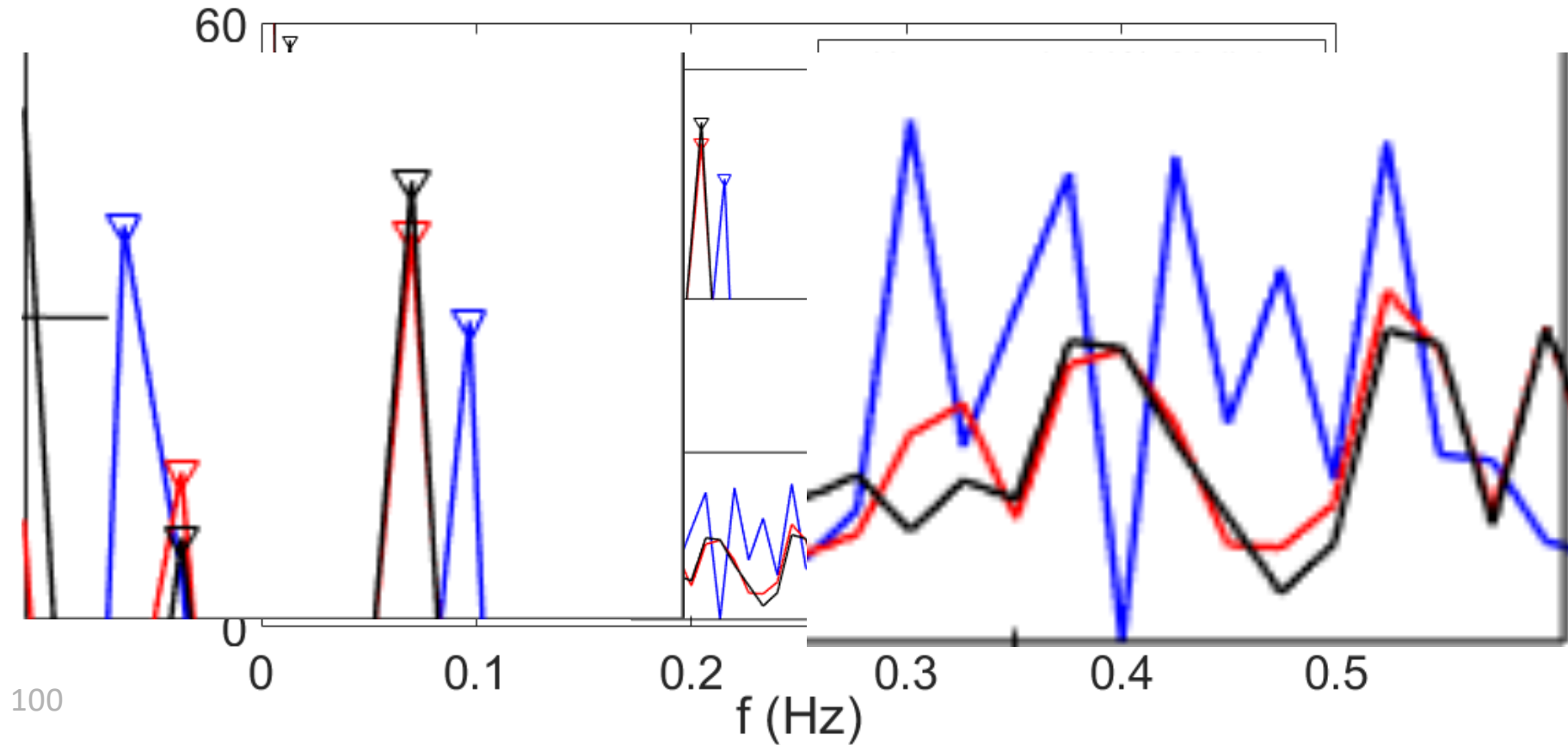


Embedded GNRs  
with high reflection

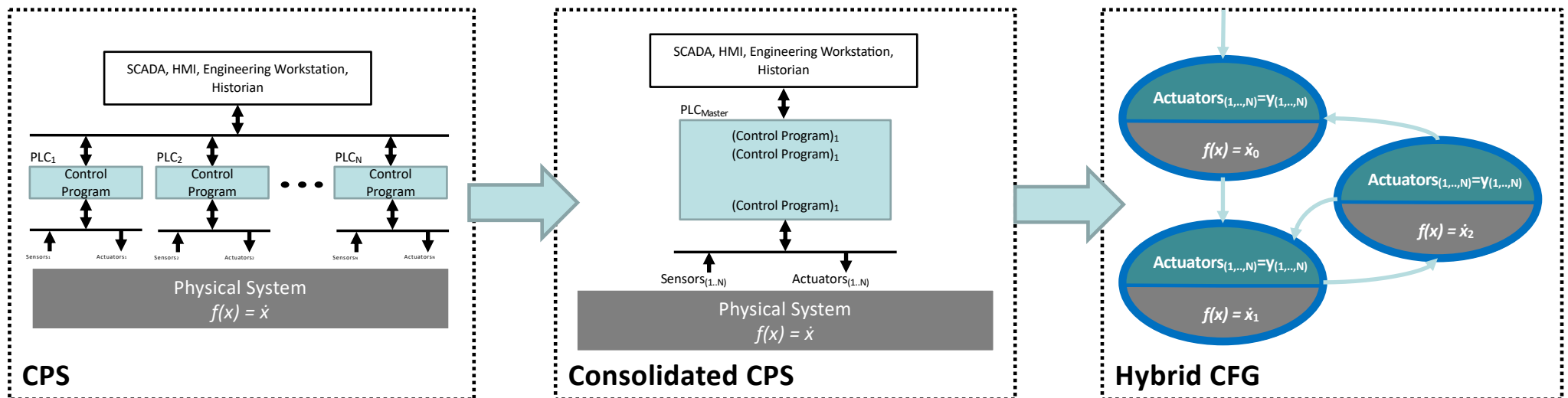
*The precise placement of GNRs is out of scope for the current work*

# Quantitative spatial analysis also shows error

High Density (Ground Truth)      Low Density (Error)



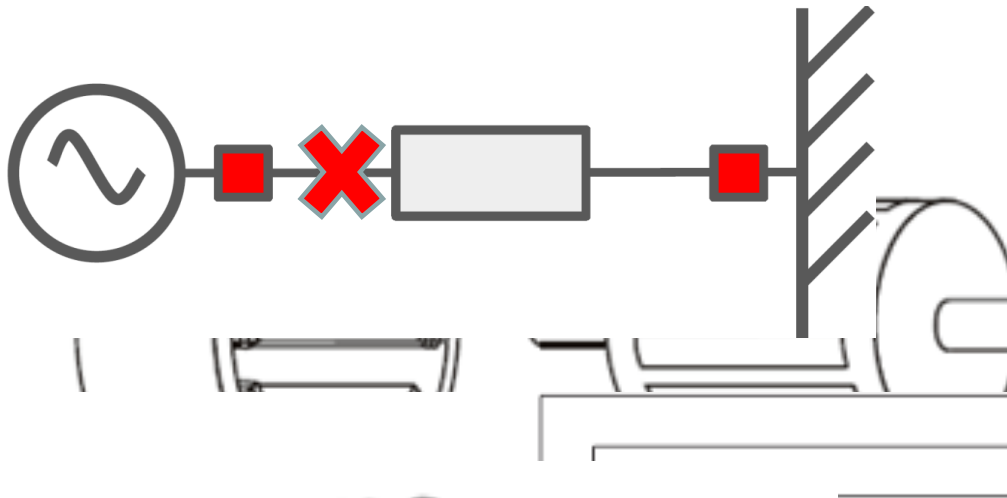
# Scadman: Cyber-physical Control Flow Integrity (Submitting to USENIX Security 2018)



# Rotor Angle Stability of Synchronous Machine

## EQUAL AREA CRITERION

1

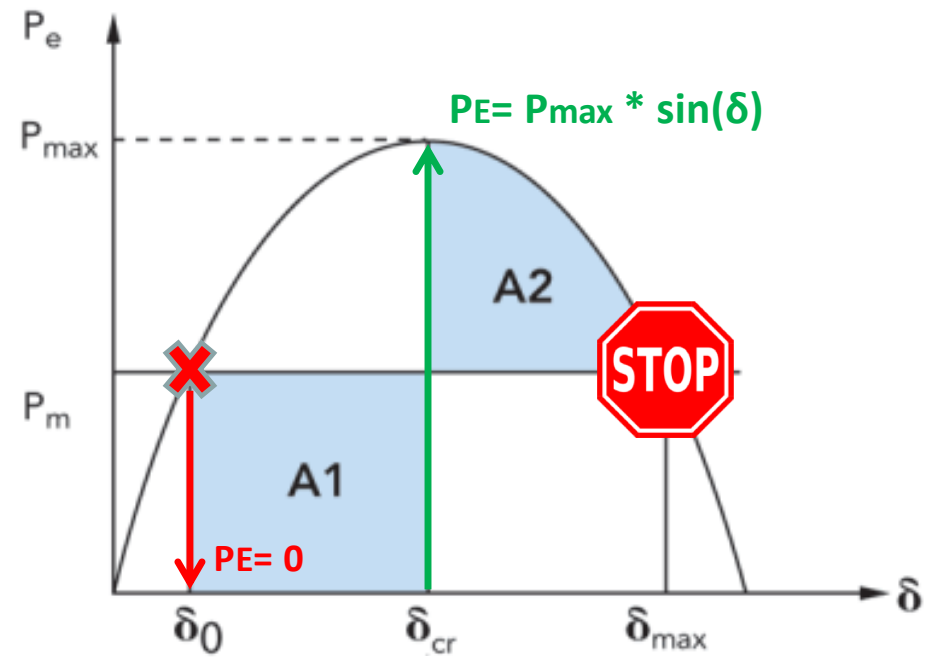


$$\frac{d^2\delta}{dt^2} = P_M$$

$P_S$

'S W

$$P_E = P_{max} * \sin(\delta)$$



# Hybrid Program Representation

---

Final Complete SMIB Hybrid Program

---

$init \Rightarrow [\{ctrl; plant \& H\}^*](req)$

$init \equiv P_M = 1 \wedge P_{e,max} = \frac{3}{2} \wedge \omega = 0 \wedge \theta = \arcsin\left(\frac{P_M}{P_{e,max}}\right)$

$\wedge \theta_{max} = \pi - \theta \wedge \sin \theta = \frac{P_M}{P_{e,max}} \wedge \cos \theta = \frac{\sqrt{P_{e,max}^2 - P_M^2}}{P_{e,max}}$

$\wedge c = 2P_M\theta_{max} - 2P_{e,max} \cos(\theta)$

$ctrl \equiv \{(a := P_M - P_{e,max} \sin(\theta)) \cup (a := P_M)\};$

$t := 0;$

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$T := *;$

$?T \geq 0 \wedge \theta_f(T) \leq \theta_e$

$\wedge 360c \leq 720\theta P_M + 720P_{e,max} - 360\theta_f(T)^2 P_{e,max}$

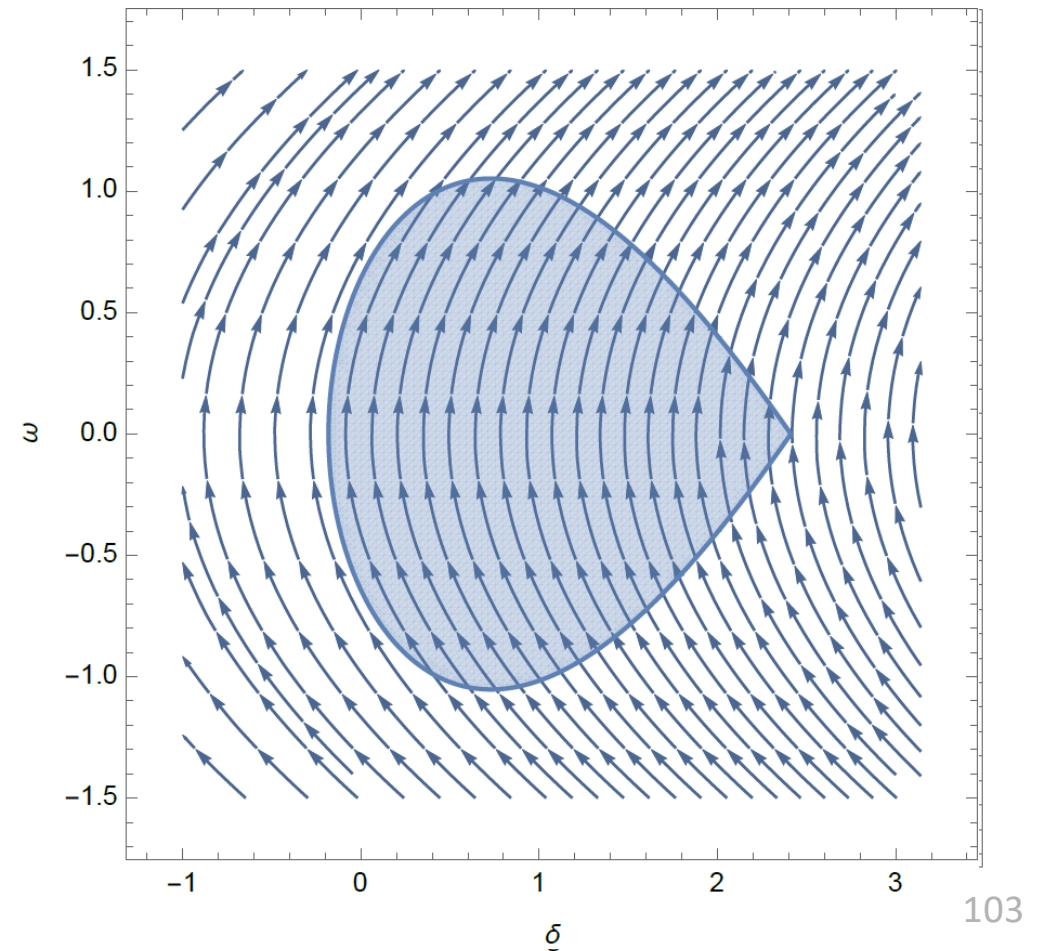
$+ 30\theta_f(T)^4 P_{e,max} - \theta_f(T)^6 P_{e,max} - 360\omega_f(T)^2$

$plant \equiv \theta' = \omega, \omega' = a, \sin \theta' = \omega \cos \theta, \cos \theta' = -\omega \sin \theta, t' = 1$

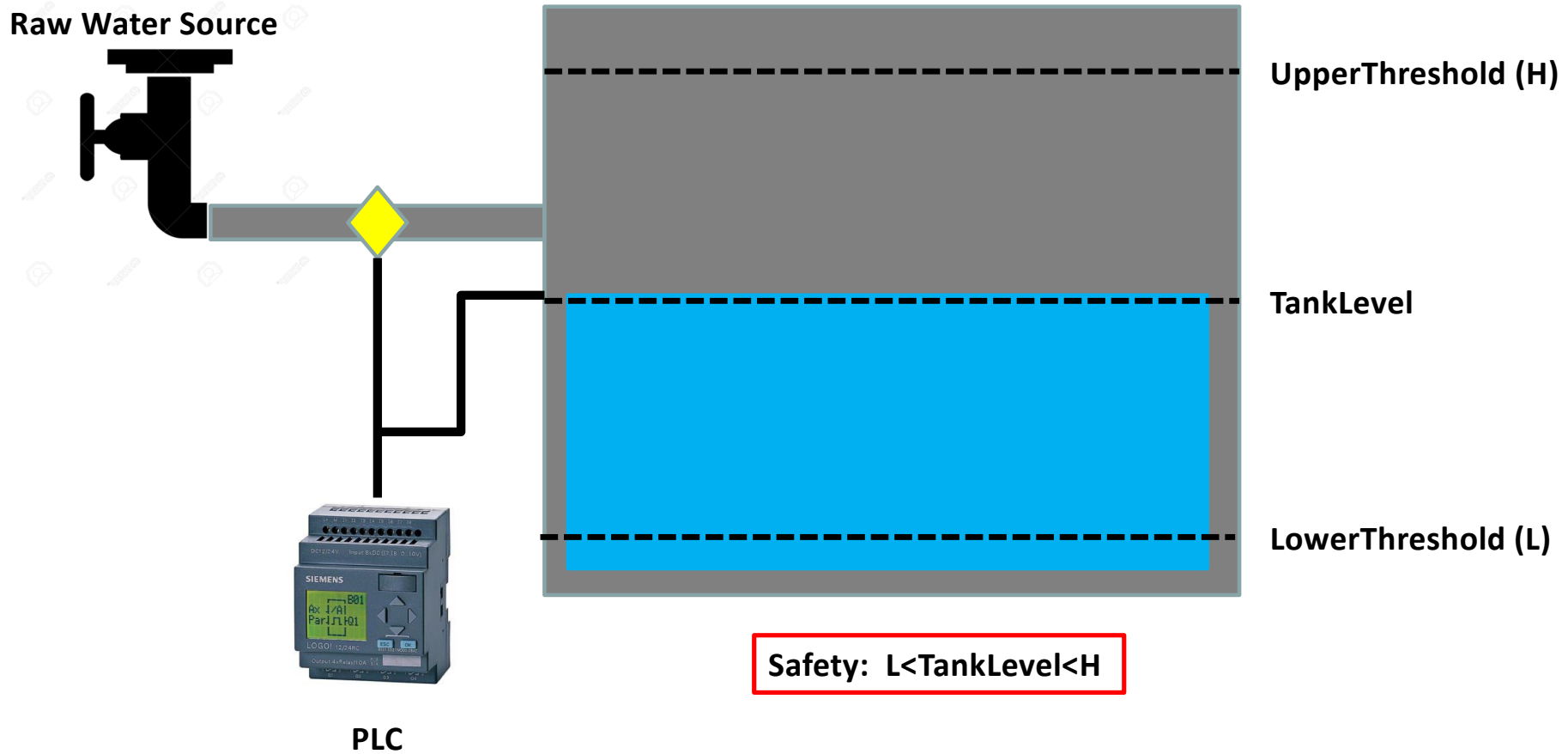
$H \equiv \sin^2 \theta + \cos^2 \theta = 1$

$req \equiv \theta \leq \theta_{max}$

---



# HyPLC: Hybrid PLC Program Translation for Verification



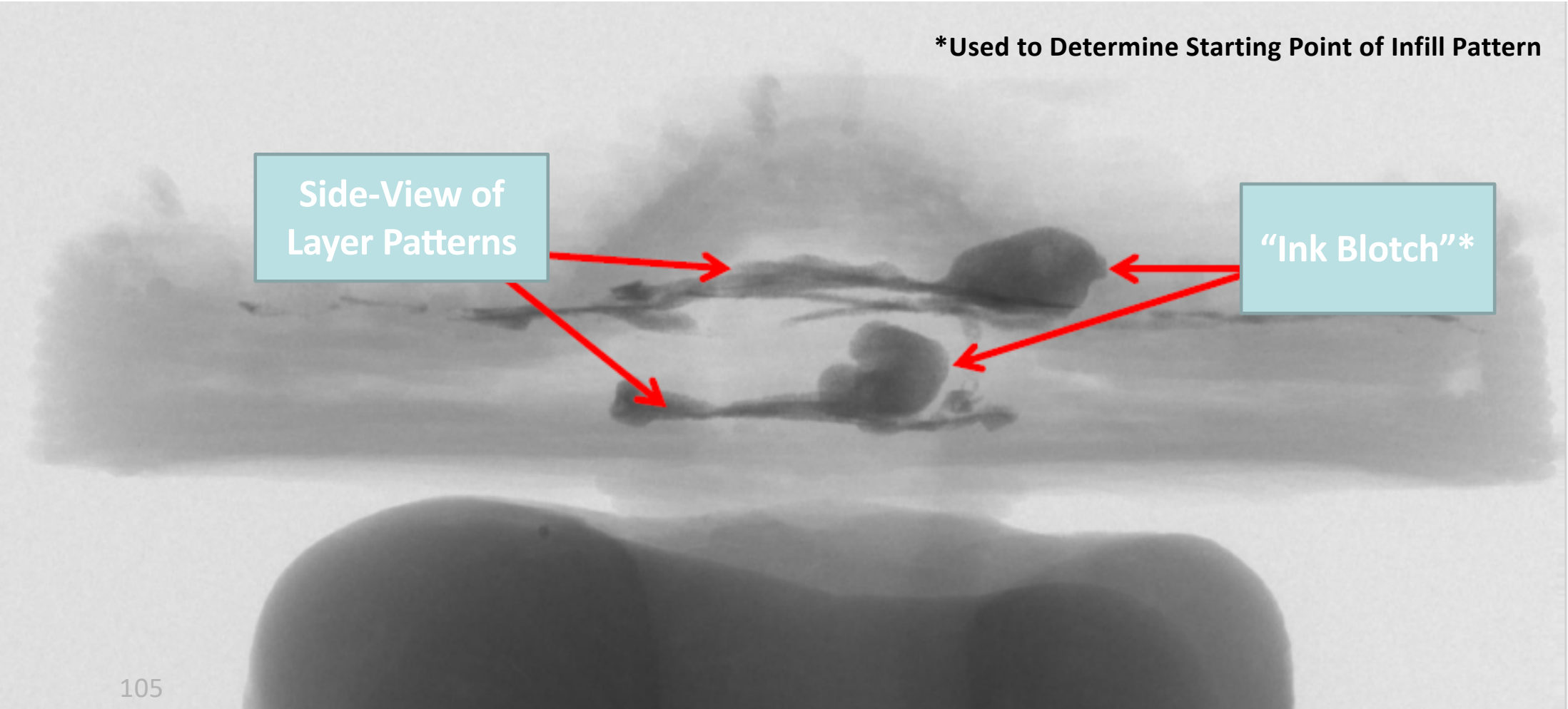


# Tibial Implant MicroCT Scan

\*Used to Determine Starting Point of Infill Pattern

Side-View of  
Layer Patterns

“Ink Blotch”\*





# Tell Me More Than Just Assembly! Reversing Cyber-physical Execution Semantics of Embedded IoT Controller Software Binaries

**Pengfei Sun**<sup>†</sup>, Luis Garcia\* and Saman Zonouz<sup>†</sup>

<sup>†</sup>Rutgers University, \*University of California, Los Angeles

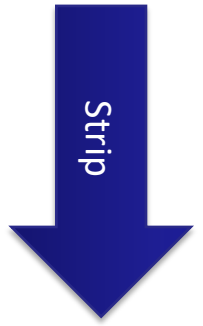
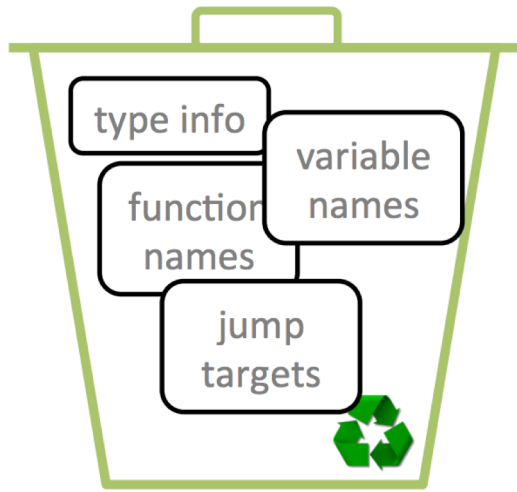
# We live in a cyber-physical world...



...and attacks are increasingly cyber-physical



```
int gcd(int a, int b){
  int com_divisor;
  if(b!=0){
    com_divisor = gcd (b, a % b);
  }
  else{
    com_divisor = a;
  }
  return com_divisor;
}
```



# Reverse Engineering



Reverse  
Engineering

```
8048094: push  ebp
8048095: mov   ebp, esp
8048097: sub   esp, 0x18
804809a: cmp   [ebp + 0xc]:32, 0x0
804809e: jnz   0x80480a5
80480a0: mov   eax, [ebp + 0x8]:32
```

## Domain knowledge

```
int32_t sub_8048094(int32_t arg1, int32_t arg2){
    int32_t eax1;
    if(arg2!=0){
        eax1=sub_8048094(arg2, arg1 % arg2);
    }
    else{
        eax1=arg1;
    }
    return eax1;
}
```

???

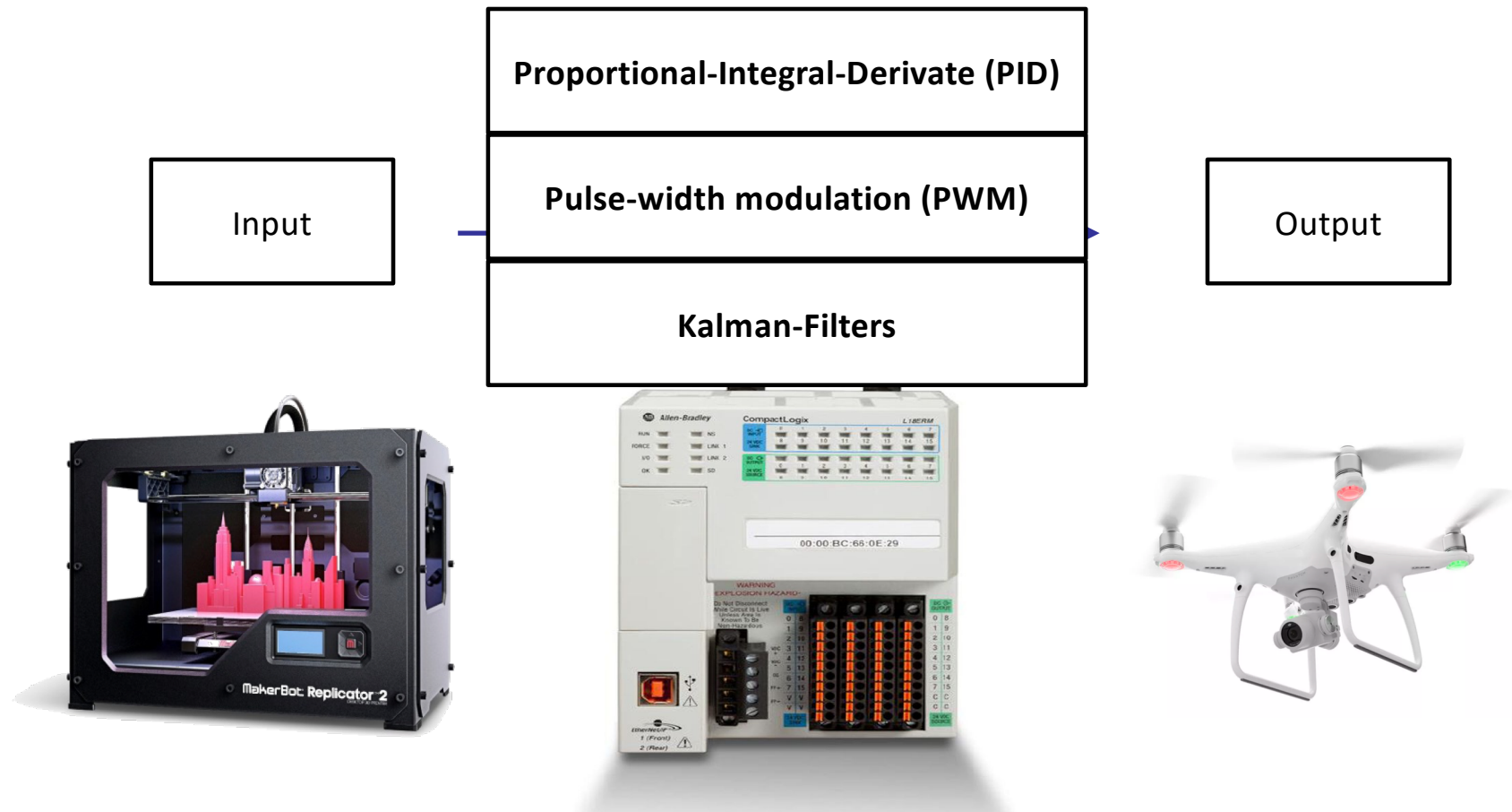


```
int32_t didWeLose(int gauntLetFound, int infinityStoneCount)
{
    int32_t eax1;
    if(arg2!=0){
        eax1=sub_8048094(arg2, arg1 % arg2);
    }
    else{
        eax1=arg1;
    }
    return eax1;
}
```

# Mismo: Domain-specific Reverse Engineering Framework

- Propose a **domain-specific reverse engineering** framework to extract **high-level algorithmic control- and data-flow semantics** from embedded binary executables.
- Introduce a **semantic mapping** using **dynamic binary analysis** and symbolic comparison of the **mathematical and binary expressions** to fill the semantic gap between high-level algorithm descriptions and low-level stripped binary segments.

# Domain Knowledge (Cyber-physical System)





# Current Solutions

- IDA PRO
- OllyDbg
- Ninja
- Snowman



Disassembler,  
Decompiler,  
Static and dynamic analysis

- Reward (NDSS'10)
- TIE (NDSS'11)
- Howard (NDSS'11)
- ReViver (ACSAC'16)



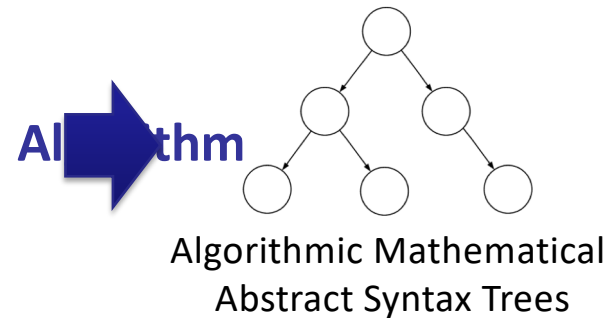
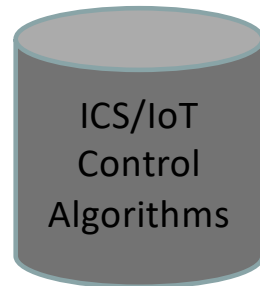
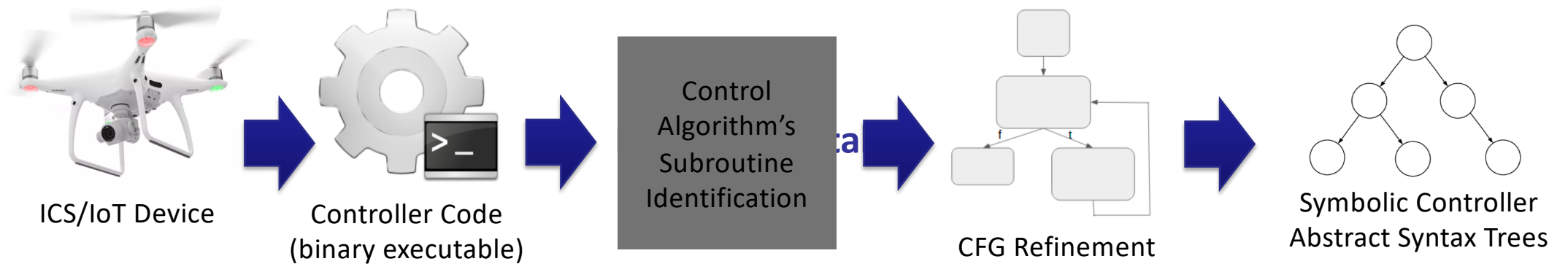
Data structure definition,  
Data structure memory instance

- BinDiff (SSTIC'05)
- BinJuice (PPREW '13)
- Blex (Usenix'14)



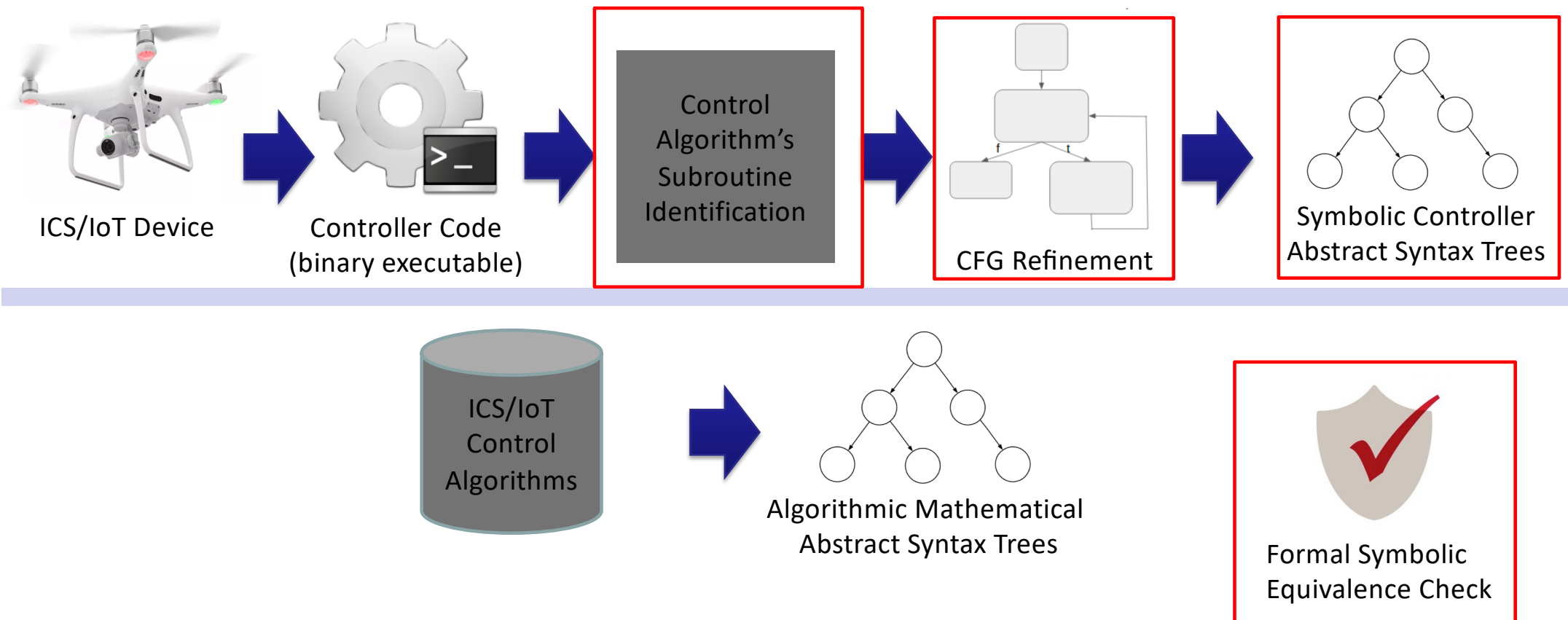
Binary similarity checking

# Mismo Overview





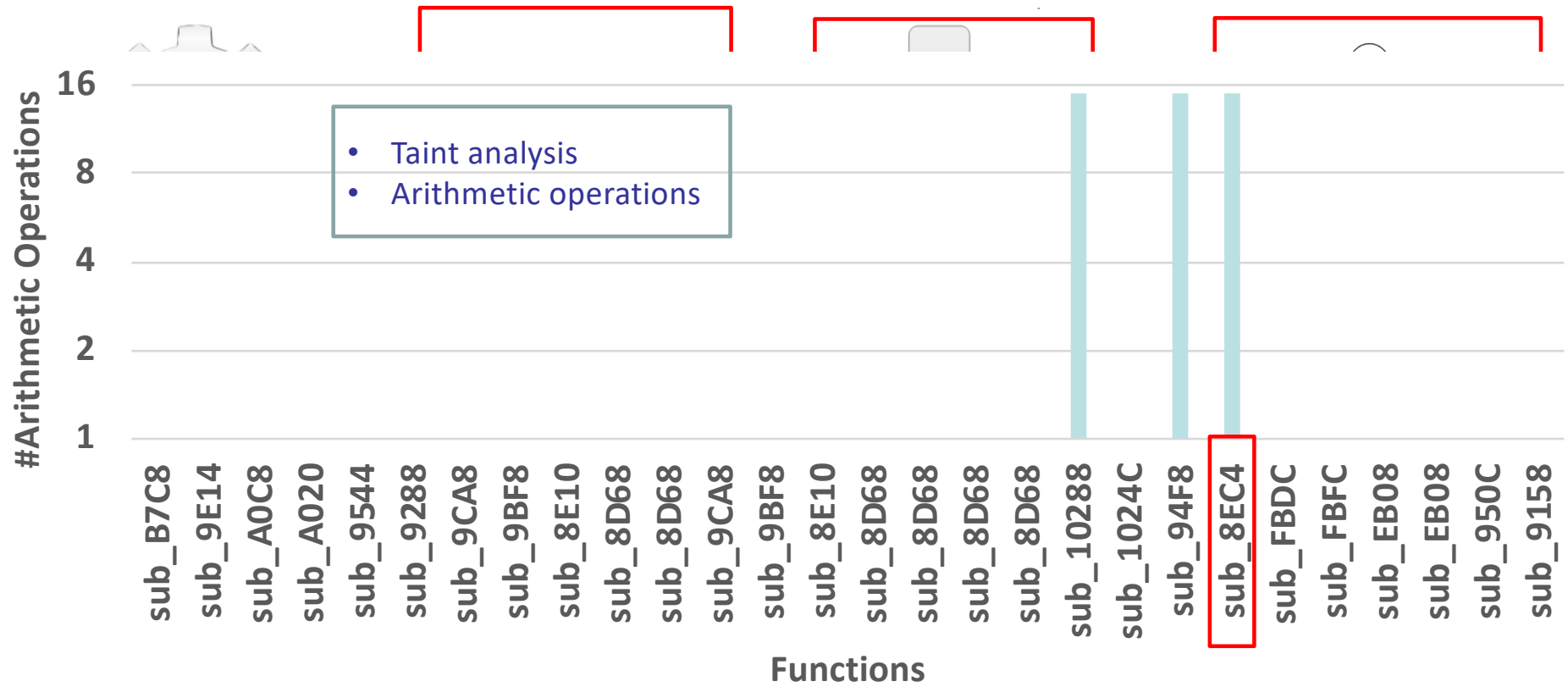
# Mismo Overview



# Case Study



ICS/IoT Device



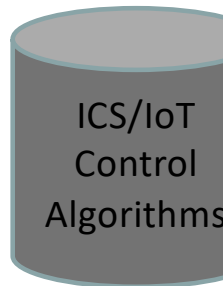
# Case Study



ICS/IoT Device



Controller Code  
(binary executable)



ICS/IoT  
Control  
Algorithms

```
sub_8EC4:
...
VSTR D7, [R7,#0x3C+var_3C]
VLDR D7, [R7,#0x3C+var_3C]
VCMP.EF64 D7, #0.0
VMRS APSR_nzcv, FPSCR
BPL loc_8F9C
```

true

```
LDR R3, =(off_1B0B8 - 0x1B000)
LDR R3, [R4,R3]
MOV R1, R3
LDRD.W R2, R3, [R7]
...
VSTR D7, [R7,#0x3C+var_14]
VLDR D6, [R7,#0x3C+var_14]
VLDR D7, =100.0
VCMP.EF64 D6, D7
VMRS APSR_nzcv, FPSCR
BPL loc_908E
```

false

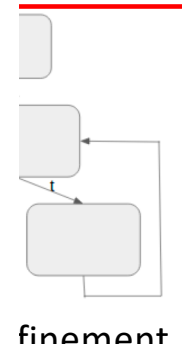
```
LDRD.W R2, R3, [R7,#0x28]
STRD.W R2, R3, [R7,#8]
B loc_909E
```

```
...
VLDR D7, [R7,#0x3C+var_3C]
VCMP.F64 D6, D7
VMRS APSR_nzcv, FPSCR
BEQ loc_90C6
```

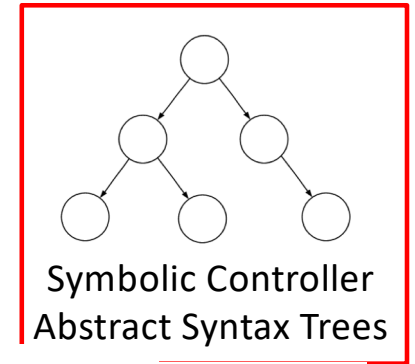
true

```
VLDR D6, [R7,#0x3C+var_3C]
VLDR D7, [R7,#0x3C+var_34]
VSUB.F64 D7, D6, D7
VSTR D7, [R7,#0x3C+var_3C]
```

```
VLDR D6, [R7,#0x3C+var_1C]
VLDR D7, [R7,#0x3C+var_3C]
VMUL.F64 D6, D6, D7
...
```

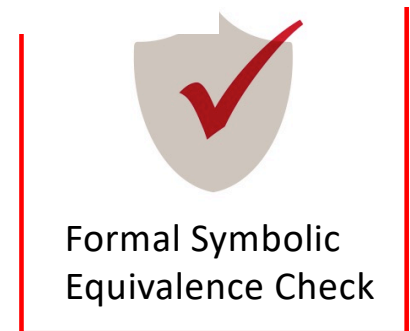


finement



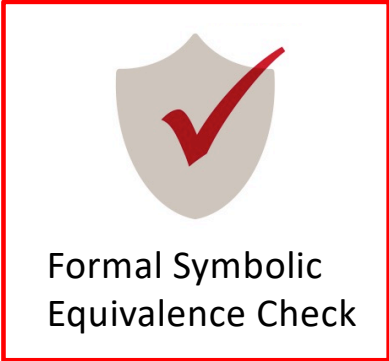
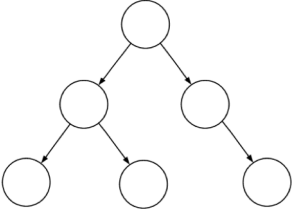
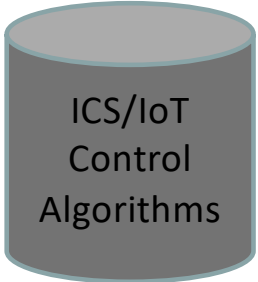
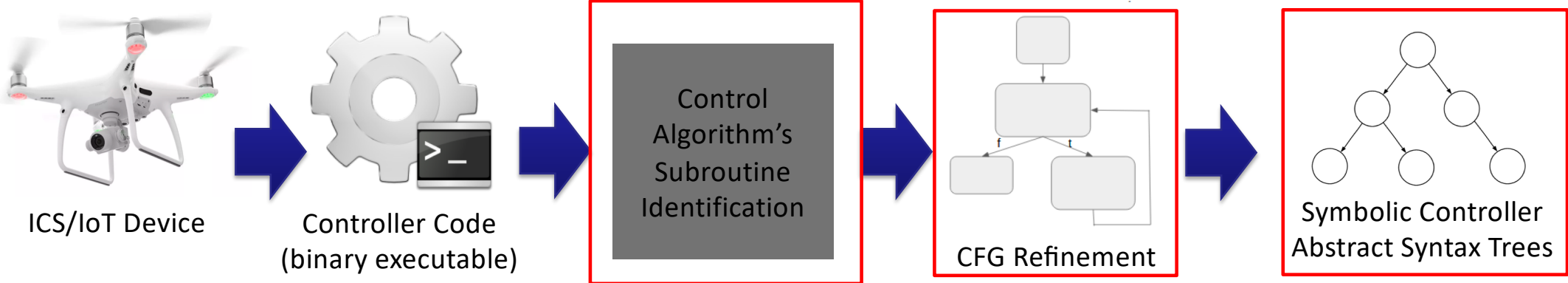
Symbolic Controller  
Abstract Syntax Trees

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S

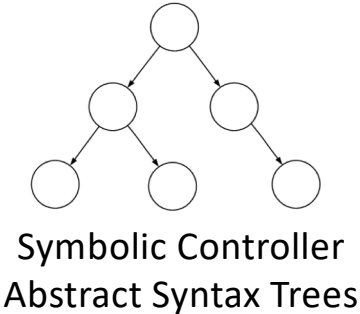


Formal Symbolic  
Equivalence Check

# Case Study



# Case Study



Symbolic expression



Symbolic execution



Symbolic output variable



```
...  
0x90DE: VMUL.F64   D6, D6, D7  
        -> 0xc0175999f54482ee  
0x90E2: VLDR    D7, [R7,#0x3C+var_2C]  
        -> 0x4028000000000000  
0x90E6: VADD.F64   D7, D6, D7  
        -> 0x4018a6660abb7d12  
...
```

Symbolic input variables



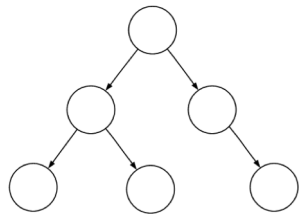
Backward slicing analysis



# Case Study

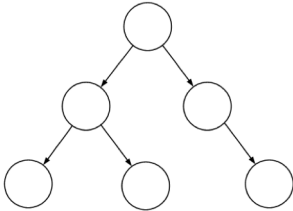
## Symbolic expression

$$\begin{aligned} & (((((\text{Sym\_15} * (((\text{Sym\_12} * \text{Sym\_11}) * \text{Sym\_13}) + \\ & \text{Sym\_14})) / ((\text{Sym\_19} * (\text{Sym\_15} * (((\text{Sym\_12} * \text{Sym\_11}) * \\ & \text{Sym\_13}) + \text{Sym\_14})))) + \text{Sym\_16})) * (((\text{Sym\_7}) ^ (-1) * \\ & \text{Sym\_8}) + ((\text{Sym\_5} * ((\text{Sym\_3}) ^ (-1) * \text{Sym\_4}) + \\ & ((\text{Sym\_0} * \text{Sym\_1}) + (\text{Sym\_18} * \text{Sym\_2})))) + (\text{Sym\_18} * \\ & \text{Sym\_6}))) - ((\text{Sym\_19} * ((\text{Sym\_18} * \text{Sym\_10}) + (\text{Sym\_11} * \\ & \text{Sym\_9}))) + (\text{Sym\_17} * \text{Sym\_18})))) + ((\text{Sym\_18} * \text{Sym\_10}) \\ & + (\text{Sym\_11} * \text{Sym\_9}))) \end{aligned}$$

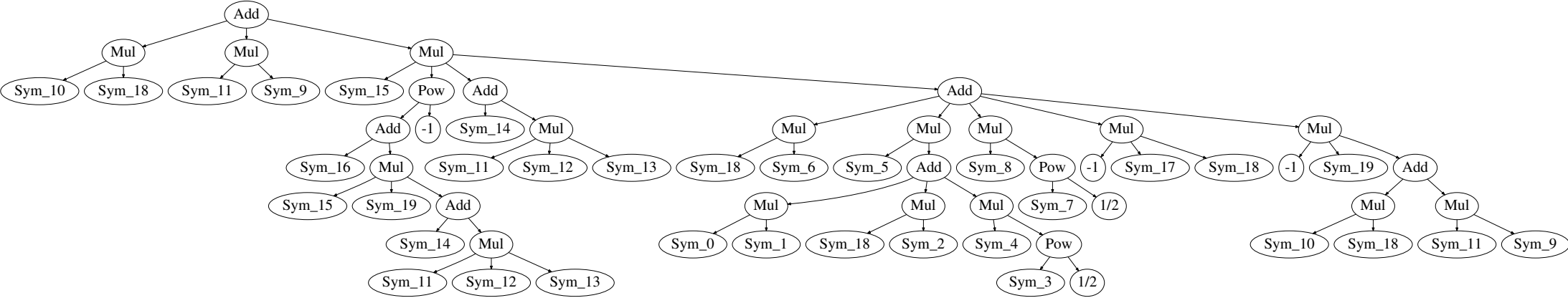


Symbolic Controller  
Abstract Syntax Trees

# Case Study

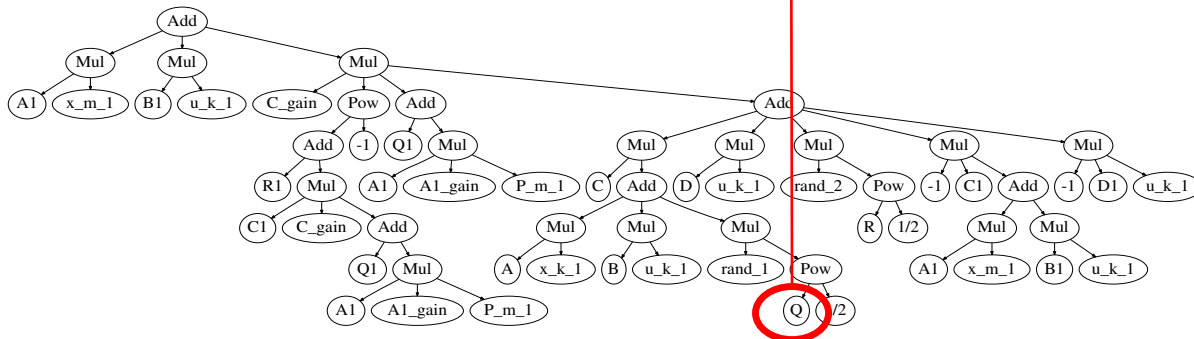
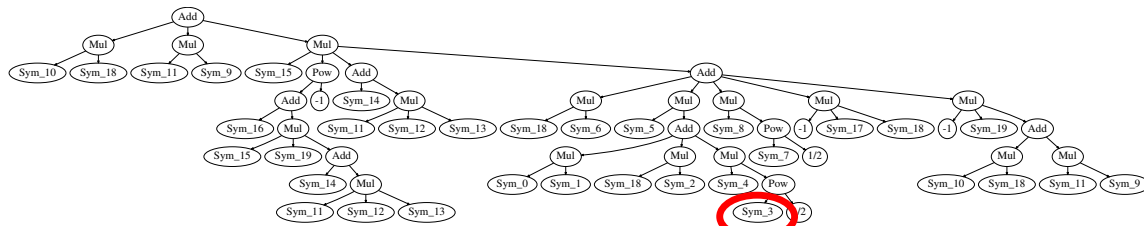


Symbolic Controller  
Abstract Syntax Trees

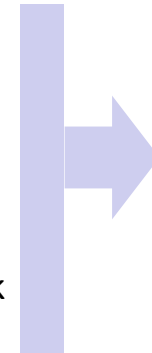




# Mismo Overview



Formal Symbolic  
Equivalence Check



Sym\_3 -> Q  
Sym\_7 -> R  
...

# Annotated Disassembly

IDA Pro disassembly result

Mismo's extracted semantic information

```

; Attributes: bp-based frame fpd=0x3C
sub_8EC4
var_3C= -0x3C
var_34= -0x34
var_2C= -0x2C
var_24= -0x24
var_1C= -0x1C
var_14= -0x14

PUSH      {R4,R7,LR}
VPUSH    {D8}
SUB      SP, SP, #0x34
ADD      R7, SP, #0
LDR      R4, =(dword_1B000 - 0x8ED4)
ADD      R4, PC ; dword_1B000
LDR      R3, =(off_1B09C - 0x1B000)
LDR      R3, [R4,R3] ; unk_1B258 ; ...
VLDR     D6, [R3] ; .....
LDR      R3, =(off_1B0B4 - 0x1B000)
LDR      R3, [R4,R3] ; unk_1B5E8 ; ...
VLDR     D7, [R3] ; .....
VMUL.F64 D6, D6, D7 ; .....
LDR      R3, =(off_1B09C - 0x1B000)
LDR      R3, [R4,R3] ; unk_1B258 ; ...
VLDR     D5, [R3,#0x10] ; .....
LDR      R3, =(off_1B0C0 - 0x1B000)
LDR      R3, [R4,R3] ; unk_1B640 ; ...
VLDR     D7, [R3] ; .....
VMUL.F64 D7, D5, D7 ; .....
VADD.F64 D8, D6, D7 ; .....
LDR      R3, =(off_1B09C - 0x1B000)
LDR      R3, [R4,R3] ; unk_1B258 ; ...

```

...struct pointer unk\_1B258  
...D6=A

...struct pointer unk\_1B5E8  
...D7=x\_k\_1  
...D6=A\*x\_k\_1

...struct pointer unk\_1B258  
...D5=B

...struct pointer unk\_1B640  
...D7=u\_k\_1  
...D7=B\*u\_k\_1  
...D8=A\*x\_k\_1+B\*u\_k\_1

# Evaluation

Accuracy

Category	Vendor	Control Algorithm					#firmwares	Data Accuracy (%)	Code Accuracy (%)
		BB	KF	PF	PID	PWM			
Drone	Bitcraze		✓		✓	✓	38	100.00	96.40
	Ardupilot		✓		✓	✓	168	78.57	86.96
	DJI		✓		✓	✓	66	100.00	93.69
	3D Robotics		✓		✓	✓	327	78.57	86.96
	Cleanflight				✓		48	71.43	50.26
	Fluoreon				✓		1	77.78	48.70
	Eachine				✓		1	77.78	48.70
	Paparazzi				✓		53	77.78	86.14
	Cheerson		✓		✓	✓	169	84.29	91.56
Automotive	Baidu		✓		✓		2	100.00	93.67
	PolySync				✓		3	100.00	97.01
	Microsoft				✓		1	100.00	100.00
	Tier IV		✓		✓		11	100.00	89.47
	Udacity		✓	✓	✓		2	100.00	97.14
3D Printer	LulzBot	✓					22	90.91	92.86
	Makerbot				✓		19	88.89	63.81
	Repetie	✓			✓	✓	6	100.00	82.96
	Printrbot	✓			✓		22	90.91	92.86
	BCN3D	✓			✓		15	81.82	50.26
	Robo3D				✓		1	90.91	92.86
	Teacup	✓			✓	✓	1	100.00	93.24
Solidoodle				✓		2	90.91	92.86	
Robotics	ROS		✓	✓	✓	✓	62	88.89	94.20
	Robotiq				✓		1	100.00	98.64
	LinuxCNC				✓		145	53.85	43.34
	Drake		✓		✓		8	85.71	87.38
Smart Home	SmartPID				✓		2	100.00	100.00
	Particle				✓		87	100.00	96.81
	MBED				✓	✓	147	100.00	100.00
Linux Kernel	Linux Kernel				✓		833	100.00	100.00
<b>Total/Average</b>	<b>30</b>						<b>2,263</b>	<b>89.82</b>	<b>84.96</b>

c variables

itions

# Potential Use-cases

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- Binary vulnerability assessment



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- Binary vulnerability assessment
- Memory forensics analysis
- Sensitive code and data segment protection
- Correct algorithm implementation verification
- Binary-level software similarity measures

# Compare with Snowman

Source Code	Snowman Reversed Result	MISMO Reversed Result
<pre>typedef struct {     double windup_guard;     double proportional_gain;     double integral_gain;     double derivative_gain;     double prev_input;     double int_error;     double control;     double prev_steering_angle; } PID;</pre>	<pre>signed int v6; // r3@2 double v19; // [sp+0h] [bp+0h]@1 double v20; // [sp+8h] [bp+8h]@8 int v21; // [sp+1Ch] [bp+1Ch]@1</pre>	<pre>struct {     0: double SymVar;     8: double Kp;     10: double Ki;     18: double Kd;     20: double prev_measured_value;     28: double integral;     30: double output; }</pre>
<pre>diff = ((input - pid-&gt;         prev_input)/dt);</pre>	<pre>_R3 = v21; --asm {     VLDR    D7, [R3,#0x20]     VLDR    D6, [R7,#0x4C+var_44]     VSUB.F64 D6, D6, D7     VLDR    D7, [R7,#0x4C+var_4C]     VDIV.F64 D7, D6, D7     VSTR    D7, [R7,#0x4C+var_24] }</pre>	<pre>reg-D6 = measured_value         - previous_measured_value; reg-D7 = reg-D6/dt;</pre>

# Linux Kernel Bug

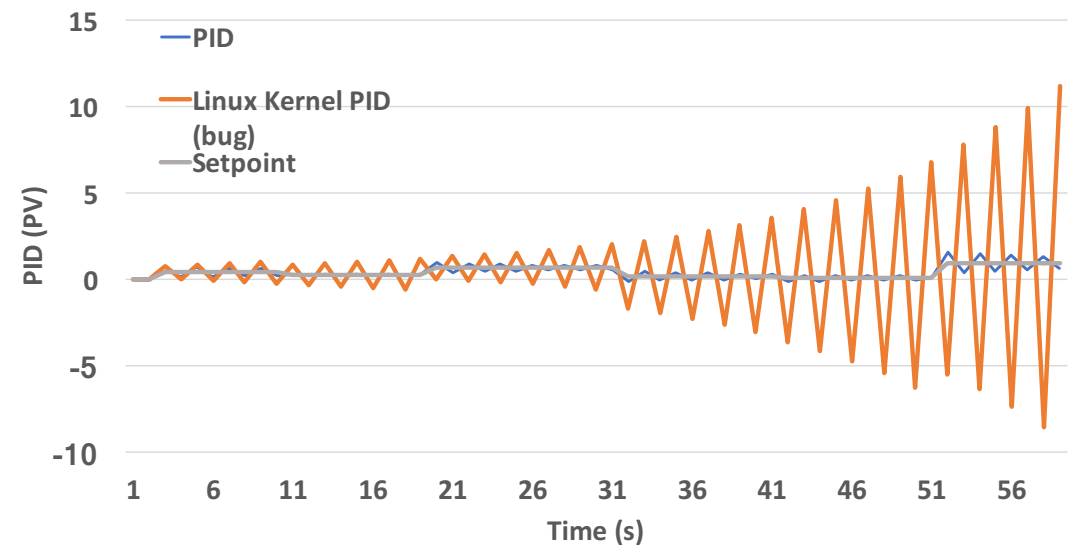
$$y_k = y_{k-1} - K_p * (x_k - x_{k-1}) + K_i * T_s * e_k - K_d * (x_k - 2 * x_{k-1} + x_{k-2}) / T_s$$

Algorithm

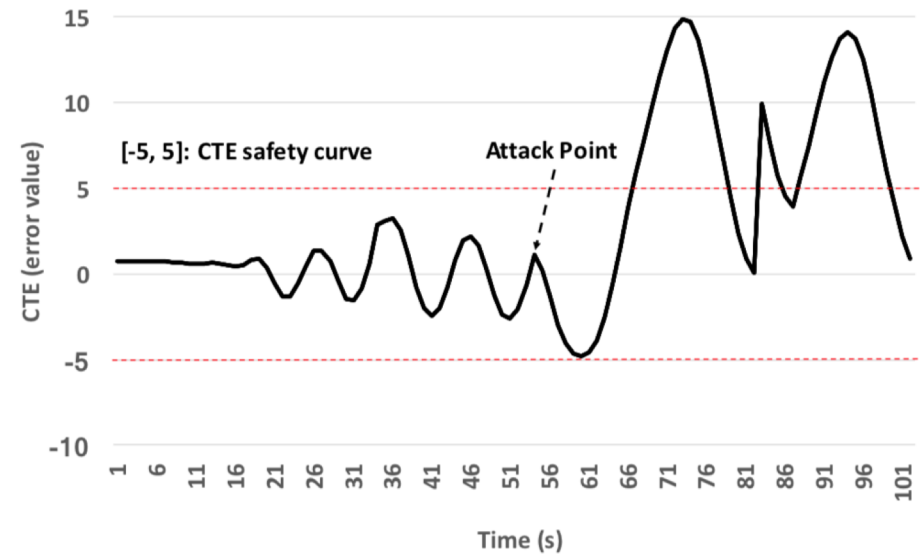
Mismo

Program Code

```
...  
/* compute intermediate PID terms */  
p_term = -p_param.kp * (xk - xk_1);  
i_term = p_param.kp * p_param.ki * p_param.ts * ek;  
d_term = -p_param.kp * p_param.kd * (xk - 2 * xk_1  
      + xk_2) / p_param.ts;  
  
/* compute output */  
*yk += p_term + i_term + d_term;  
...
```



# Attack Control Algorithm



(a) Car crash visualization using the autonomous controller. (b) Modified gain parameter of the controller causes the crash.

# Conclusions & QA

- A general framework to **extract semantic information** of an embedded firmware binaries with respect to its associated **high-level control algorithm**.
- Using **dynamic binary analysis** and **symbolic comparison** of the mathematical and binary expressions to fill the **semantic gap** between high-level algorithm descriptions and low-level stripped binary segments.

**Thank You**

**Questions?**

**Email:** [garcialuis@ucla.edu](mailto:garcialuis@ucla.edu)



# PID

$$u(t) = MV(t) = K_p e(t) + K_i \int_0^t e(\tau) d\tau + K_d \frac{de(t)}{dt},$$

where

$K_p$  is the proportional gain, a tuning parameter,

$K_i$  is the integral gain, a tuning parameter,

$K_d$  is the derivative gain, a tuning parameter,

$e(t) = SP - PV(t)$  is the error (SP is the setpoint, and  $PV(t)$  is the process variable),

$t$  is the time or instantaneous time (the present),

$\tau$  is the variable of integration (takes on values from time 0 to the present  $t$ ).

# Kalman Filter

## Predict [\[ edit \]](#)

Predicted (*a priori*) state estimate  $\hat{\mathbf{x}}_{k|k-1} = \mathbf{F}_k \hat{\mathbf{x}}_{k-1|k-1} + \mathbf{B}_k \mathbf{u}_k$

Predicted (*a priori*) error covariance  $\mathbf{P}_{k|k-1} = \mathbf{F}_k \mathbf{P}_{k-1|k-1} \mathbf{F}_k^\top + \mathbf{Q}_k$

## Update [\[ edit \]](#)

Innovation or measurement pre-fit residual  $\tilde{\mathbf{y}}_k = \mathbf{z}_k - \mathbf{H}_k \hat{\mathbf{x}}_{k|k-1}$

Innovation (or pre-fit residual) covariance  $\mathbf{S}_k = \mathbf{R}_k + \mathbf{H}_k \mathbf{P}_{k|k-1} \mathbf{H}_k^\top$

Optimal Kalman gain  $\mathbf{K}_k = \mathbf{P}_{k|k-1} \mathbf{H}_k^\top \mathbf{S}_k^{-1}$

Updated (*a posteriori*) state estimate  $\hat{\mathbf{x}}_{k|k} = \hat{\mathbf{x}}_{k|k-1} + \mathbf{K}_k \tilde{\mathbf{y}}_k$

Updated (*a posteriori*) estimate covariance  $\mathbf{P}_{k|k} = (\mathbf{I} - \mathbf{K}_k \mathbf{H}_k) \mathbf{P}_{k|k-1} (\mathbf{I} - \mathbf{K}_k \mathbf{H}_k)^\top + \mathbf{K}_k \mathbf{R}_k \mathbf{K}_k^\top$

Measurement post-fit residual  $\tilde{\mathbf{y}}_{k|k} = \mathbf{z}_k - \mathbf{H}_k \hat{\mathbf{x}}_{k|k}$

The formula for the updated (*a posteriori*) estimate covariance above is valid for any gain  $\mathbf{K}_k$  and is sometimes called the **Joseph form**. For the optimal Kalman gain the formula further simplifies to  $\mathbf{P}_{k|k} = (\mathbf{I} - \mathbf{K}_k \mathbf{H}_k) \mathbf{P}_{k|k-1}$ , in which form it is most widely used in applications. However, one must keep in mind, that it is valid only for the optimal gain that minimizes the residual error. Proof of the formulae is found in the [derivations](#) section.